

Dr. K. Udaya Kumar

Principal, BNMIT

Professor of CSE

Education

- Ph.D. Univ. of Ljubljana, Yugoslavia 1983
- M.Tech. Indian Institute of Science 1974
- M.Sc. (Physics) Central College, Bangalore 1972
- B.Sc. (Hons.) Central College, Bangalore 1970

Professional Experience

- Aug. 2007 to till date: Professor CSE & Principal, BNMIT
- Aug. 2006 to Aug. 2007: Professor & Head, Dept. of CSE, BNMIT
- Dec. 1990 to July 2006: Professor & Head, Dept. of CSE, Dr. AIT
- Feb. 1977 to Nov.1990: Asst. Professor, MSRIT
- Sept.1974 to Feb. 1977: Deputy Engineer, Bharat Electronics Ltd. (BEL)

Area of Expertise

Microprocessors, Micro controllers, Finite Automata, Computer Architecture

Subjects taught

Microprocessors, Advanced Microprocessors, Microcontrollers, Electronic Circuits
Operating Systems, Finite Automata & Formal Languages, Data Communications
Computer Networks, Computer Organization, Advanced Computer Architecture

Membership of Professional Bodies

- Institute of Electrical and Electronics Engineers (IEEE), USA
- Association for Computing Machinery (ACM), USA
- New York Academy of Sciences (NYAS), USA
- Computer Society of India (CSI)
- Instrumentation Society of India
- Indian Society of Technical Education (ISTE)
- Institution of Engineers, India

Publications

5 technical papers and 5 books

Guest lecturers Delivered

EDUSAT

At various Universities and institutions across the country and abroad.

Others

- Chairman / Member – Board of Studies of VTU, IETE, KSOU
- Member of the Academic Senate of VTU
- Organized a number of Short Term Training Programs and National Conferences
- Member - Doctoral Committee of various universities
- Governing Council member of various Engineering Colleges
- Listed in
 - The Worldwide Honours List, International Biographical Centre, England.
 - International Directory of Distinguished Leadership, American Biographical Institute Inc.
 - International Scientist of the year for 2001, International Biographical Centre, England.

