

BNM Institute of Technology, Bangalore

INNOVATIVE TEACHING METHODS

Title of innovation method / activity: Group activity

Faculty: Rohini T.

Designation: Assistant Professor

Goals or objectives of method: learn team work, improve Verilog coding, and learn applications of Verilog

Description of method:

1. Students were instructed to form a group of maximum 4 members.
2. Each group was instructed to prepare ppt with Verilog code and simulation waveforms for the allotted example in Samir Palnitkar book and to choose a relative topic from real world and explain it. Template ppt was sent.
3. Each member in the group worked on Xilinx tool for simulation and explained their ppt for 10minutes.
4. Grades were evaluated based on their presentation skills and level of understanding.

(Grade A- Excellent

Grade B- Good

Grade C- Satisfactory)

Benefits of method:

1. Understand verilog language effectively
2. Learn to use the XILINX tool
3. Understand the applications of Verilog

For review and critique contact: rohini.antharmuki@gmail.com

Student allotment for group activity

B.N.M Institute of Technology

DEPARTMENT : Electronics and Communication

Group activity for Verilog HDL


CLASS- V A

FACULTY-ROHINI T.

SL.No	USN No	Name of Student	Group no.	Assigned topic
1	1BG15EC001	Abhigna R	1	EXAMPLE 7-31 EXAMPLE 7-32
2	1BG15EC010	Amith K Shinde		
3	1BG15EC021	Ashwin Sundaresh		
4	1BG15EC036	G Kalyani		
5	1BG15EC024	Belagallu Dhanya Sree	2	EXAMPLE 7-13 EXAMPLE 7-14 EXAMPLE 7-17
6	1BG15EC030	Deepa M		
7	1BG16EC404	Deepthi M R		
8	1BG16EC405	Divyashree V.		
9	1BG15EC029	Deeksha More.E.K	3	EXAMPLE 7-33 EXAMPLE 7-34
10	1BG15EC035	Divya S		
11	1BG15EC039	Gowthami R		
12	1BG16EC423	Soumyashree P.K.		
13	1BG15EC011	Amogha Lokesh	4	EXAMPLE 7-37 EXAMPLE 7-38
14	1BG15EC014	Anup Surahonne		
15	1BG15EC017	Arjuna C Reddy		
16	1BG15EC041	Harshita Lokesh		
17	1BG15EC008	Akash Munikoti	5	EXAMPLE 7-22 EXAMPLE 7-23 EXAMPLE 7-24
18	1BG15EC027	Chandu B		
19	1BG15EC034	Dheeraj P R		
20	1BG15EC038	Ganesh Murthy V		
21	1BG15EC002	Ashith Kiran Parlakoti	6	EXAMPLE 7-25 EXAMPLE 7-26 EXAMPLE 7-27
22	1BG15EC003	Adithya B R		
23	1BG15EC004	Adithya N Simha		
24	1BG15EC006	Aditya Ranga M		
25	1BG16EC403	Bhavya T N	7	EXAMPLE 7-28 EXAMPLE 7-30
26	1BG15EC022	Ashwini S		
27	1BG15EC025	Bhoomika M		
28	1BG15EC026	Chaitra N		
29	1BG15EC007	Akash Gupta	8	EXAMPLE 7-18 EXAMPLE 7-20
30	1BG15EC009	Akshay Nadig N R		

31	1BG15EC013	Ankush		EXAMPLE 7-21
32	1BG15EC019	Ashish S		
33	1BG15EC020	Ashok A R		
34	1BG15EC031	Deepak Kumar	9	EXAMPLE 7-10 EXAMPLE 7-11 EXAMPLE 7-12
35	1BG15EC037	G Saipavan		
36	1BG15EC040	Hariharan M		
37	1BG15EC028	Chinmaye R	10	EXAMPLE 7-1 EXAMPLE 7-5
38	1BG15EC032	Deepika Jadav K		
39	1BG16EC411	Meghana D N		
40	1BG16EC414	Nandini M Samanth	11	EXAMPLE 7-6 EXAMPLE 7-7 EXAMPLE 7-8
41	1BG15EC042	Jagadish Naik		
42	1BG15EC122	Thyagarajan R.		
43	1BG15EC123	Vishwanathan R.		
44	1BG15EC005	Aditi H Deepak		

Instructions: Prepare ppt with code and simulation waveforms for the allotted example in Samir Palnitkar book. Choose a relative topic from real world and explain the code and simulation. Sample ppt is given .



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Verilog HDL, 15EC53

Group activity on 17-10-17

Group No. 0

USN	NAME	TOPICS COVERED