


Faculty Profile

Name of Faculty	Dr. Priyashree S	
Department	Electrical & Electronics Engineering	
Qualification	B.E., M.E., (Ph.D)	
Designation	Associate Professor	
Area of specialization	Power Quality Analysis and Digital Signal Processing	
Date of Joining BNMIT	16.02.2004	
Nature of Association (Regular/Contractual/Adjunct)	Regular	
e-mail	piyashrees@bnmit.in, priyashree_suresh@yahoo.com	
No. of years of Experience	Teaching:15½ years	

Academic Qualifications

- **Ph.D** (2019), Electrical & Electronics Engineering Sciences, VTU.
- **M.E.** (2004), Power & Energy Systems, UVCE, Bangalore University (**First Rank**).
- **B.E.** (2001), Electrical & Electronics Engineering, Dr. AIT, Bangalore University (FCD).

Working Experience Details

- Associate Professor, Dept. of EEE, BNMIT, Bangalore, India, (5th March 2012 – Till date).
- Assistant Professor, Dept. of EEE, BNMIT, Bangalore, India, (17th January 2011 – 5th March 2012).
- Senior Lecturer, Dept. of EEE, BNMIT, Bangalore, India, (2nd January 2008 – 17th January 2011).
- Lecturer, Dept. of EEE, BNMIT, Bangalore, India, (16th February 2004 – 2nd January 2008).

Courses taught

UG Courses: Signals & Systems, Digital Signal Processing, High Voltage Engineering, Power Electronics, Industrial Drives and Applications, Power System Analysis and Stability, Renewable Energy Sources, Transmission and Distribution, Switchgear and Protection, Electrical & Electronics Measurements.

PG Courses: Power Quality Issues and Mitigation, Electromagnetic Compatibility.

Research Experience Details

Ph.D

The Thesis entitled “The Mathematical Modeling and Power Quality Improvement for Industrial Furnaces” was carried out under the guidance of Dr. Vidya H A, Professor, R&D, Department of EEE, Global Academy of Technology, Bengaluru, India. In this work, the SIMULINK model of Induction

Furnace and Electric Arc Furnace are developed using MATLAB/SIMULINK based on the schematic diagram at the installation. The implementation of passive and active filters was exhibited to reduce harmonics for the SIMULINK model of IF and EAF with SPWM, Hysteresis PWM and Sliding mode control PWM techniques. Also, Wavelet transform technique was applied to mitigate the harmonics for practically obtained data collected at the installation of IF and EAF. The implementation of wavelet transform technique for different wavelets was analyzed using Fuzzy Expert System.

Academic Positions and other Responsibilities (University Level)

1. **Deputy Chief Superintendent** (External) at SJBIT, Bangalore from 26 – 05 – 2016 to 11 – 06 - 2016 for the conduction of June / July 2016 B.E./MBA examinations of VTU, Belgaum.
2. **Deputy Chief Superintendent** (External) at SJBIT, Bangalore from 15-07-2014 to 30-07-2014 for the conduction of June/July 2014 B.E. / MBA examinations of VTU, Belgaum.

Academic Positions and other Responsibilities (Department Level)

3. Department **Test** Coordinator: 2013-14
4. Department **Exam** Coordinator: 2014-15
5. Department Association (**ABEEE**) Coordinator: 2013-14, 2014-15
6. Department **Time Table** Coordinator: 2015-16
7. Department **VIII sem Technical Seminar** Coordinator: 2016-17
8. Department **Library** Coordinator: 2017-18, 2018-19
9. Department **IQAC** Coordinator: 2018-19
10. Department **Nature Club** Coordinator: 2018-19
11. Department **IPL** Coordinator: 2017-18, 2018-19

Experimental Skills

- Capable of analyzing power quality of an electric system using power quality analyzer and provide suggestions / remedial measures for power quality improvement.

Software's handled:

- **MATLAB / SIMULINK**
- **ORCAD / PSPICE**
- **MiPower**

Awards

- **First Rank in M.E.**

Professional Memberships:

- **Life Member, Indian Society for Technical Education (ISTE).**

Workshops /Seminars Organized:

1. Convener-One week FDP on “Application of Computational Intelligence in Engineering”,organized by BNMIT,Bengaluru, India, between 24th– 29thJune 2019.
2. Convener-One week FDP on “Modern Power Electronics: Design and Future Trends”,organized by BNMIT,Bengaluru, India, between 16th– 20thJanuary 2017.
3. Convener –“International Conference on Power and Advanced Control Engineering 2015 (ICPACE 2015)”, 12thto 14thAugust 2015,BNMIT,Bengaluru, India.
4. Organizing Committee Member, Workshop on “Programmable Logic Controller & SCADA Systems” 19thto 23rdJanuary 2015, BNMIT, Bangalore, India.
5. Organizing Committee Member, ‘Introduction to Research Investigations in Recent years in Electrical and Electronics Engineering’, 16thMarch 2009, BNMIT, Bangalore, India.

Research Publications:

I. National & International Journals

International Journals:

1. **Priyashree S**, Vidya H.A, Sumathi S, Triveni M.T. “Wavelet Based Power Quality Analysis of Induction Furnace using Fuzzy Expert System”, International Journal of Scientific Research and Review(IJSRR), Volume 7, Issue 4, 2018 ISSN NO: 2279-543X, APRIL 2018.
2. **Priyashree S**,Harshithananda B, Vidya H.A, “Comparative Analysis of Hysteresis and Sliding Mode Controllers for Effective Reduction of Harmonics in Electric Arc Furnace Using APF”, International Journal of Innovative Research in Computer and Communication Engineering (IJIRCCE), Vol. 5, Issue 4, ISSN(Online): 2320-9801, ISSN (Print): 2320-9798, APRIL 2017.
3. **Priyashree S**, Venkatesha K, Vidya H.A, Vijay kumar G, “Experimental Analysis of Single Phase Bidirectional AC Buck Converter for Power Quality Improvement”. International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE), Vol. 2, Issue 12, ISSN(Print) : 2320-3765, ISSN (Online) : 2278-8875, December 2013.
4. **Priyashree S**, Venkatesha K ,Vijay Kumar G, Vidya H.A,“Analysis and Mitigation of Harmonics in Non-Linear Loads using Passive Filters and Wavelet Transforms”. International Journal of Advanced Research in Electrical, Electronics and Instrumentation Engineering (IJAREEIE), Vol. 2, Issue 12, ISSN(Print) : 2320-3765, ISSN (Online) : 2278-8875, December 2013.

II. National/International Conference Proceedings

1. **Priyashree S**, Vidya H.A, Harshithananda B, “Mitigation of Harmonics using Hysteresis Control Technique of VSI Based Active Power Filter”, IEEE-International Conference on Smart grids, Power and Advanced Control Engineering (ICSPACE2017)”,GAT, Bengaluru, India, August 17 - 19th, 2017.
2. **Priyashree S**, Vidya H.A and K. Venkatesha, “Harmonic Analysis and its Elimination in Non Linear Loads Using Wavelet Transform”, International Conference on VLSI, Communication, Advanced devices, Signals & Systems And Networking, BNMIT, Bangalore, India, Jul 17th – 19th 2013.
3. **Priyashree S**,Vidya H.A, V Krishnan & K. Mallikarjunappa, “Denoising of Partial Discharge Signals in Epoxy Glass Mica Sheets using Wavelet Transform”, International Conference on Computer, Electrical and Systems Sciences, and Engineering, Penang, Malaysia, Feb 22nd – 24th 2011.

Papers presented in International/National Conferences

International

1. **Priyashree S**, Vidya H.A, Pooja A B and Mahesh E, “Harmonic Suppression in a Non-Linear Load using Three Phase Shunt Active Power Filter”, ISBN-978-93-84209-33-9,2016 Biennial International Conference on Power and Energy Systems:Towards Sustainable Energy (PESTSE), Amrita University, Bengaluru, India, 23rd Jan 2016.
2. **Priyashree S**, Vidya H.A and Triveni M.T, “Implementation of Wavelet Transform Technique for Detection and Reduction of Harmonics in an Induction Furnace”,ISBN-978-93-84209-33-9, ICITCE, IRAJ Research Forum, Bangalore, India, 5th July 2014.

National

1. **Priyashree S**,Poornima, “Intelligent Hybrid DC Grid Realization for Solar Applications”, Two Day National Conference on “Emerging Trends in Engineering Sciences and Management NCESM-2016”,SIET, Tumakuru, India, 23rd and 24th Mar 2016.
2. **Priyashree S**, Pooja, Mahesh, “Performance Investigation of three phase shunt active power filter using Hysteresis Current Control Method”, 6th National Conference on “Advanced Technique in Electrical and Electronics Engineering NCATEE-15”,SJBIT, Bengaluru, India, 9th May 2015.
3. **Priyashree S**, Pooja, Mahesh, “Harmonic Suppression in Non-linear loads using three phase shunt active filter with sliding mode controller”, 6th National Conference on “Advanced Technique in Electrical and Electronics Engineering NCATEE-15”,SJBIT, Bengaluru, India, 9th May 2015.

4. **Priyashree S**, Vidya H.A, & Sujatha B.C, “ Performance Analysis of Three Phase Induction Motor Under V/F Control using MATLAB”, National Conference of Energy Systems and Energy Issues-2011 (ESEI-2011), BMSCE, Bangalore, India, February 18th and 19th 2011.

Participation in Training courses/Seminars/Workshops

1. Faculty Development Program on “Working Model on Industry 4.0”, 08 – 07-2019 to 12- 07 - 2019, organized by Nettur Technical Training Foundation (NTTF), Bengaluru, India
2. Faculty Development Program on “Application of Computational Intelligence in Engineering”, organized by BNMIT, Bengaluru, India, 24th– 29thJune 2019, organized by BNMIT, Bengaluru, India.
3. Two Day Workshop on “Assessments for the Introductory Programming Course”, Dec 18th& 19th2018, organized by BNMIT, Bengaluru, India, in association with BITES.
4. Two Day Workshop on “A to Z Transformer Design”, Feb 17th& 18th2018, BNMIT, Bengaluru, India.
5. One day Workshop on “New Model Curriculum for PG Course (MTech/MBA/MCA) CBCS Detailed Syllabus (2018-19) as per Outcome Based Education (OBE) format including Course Outcomes (CO) and Blooms’ Taxonomy”, under TEQIP-1.3, organized by VTU, Belagavi, 6th August2018, BNMIT, Bengaluru, India.
6. Two Day Workshop on “Digital Signal Processing Lab”, 23rd to 24thJanuary2018, RNSIT, Bengaluru, India.
7. Faculty Development Program on “Modern Power Electronic Drives: Design & Future Trends”, Jan 16th to 20th 2018, BNMIT, Bengaluru, India.
8. Faculty Development Program on “Role of IT in Power Sector”, 9th to 13thJanuary2017, BNMIT, Bengaluru, India.
9. Conference on “CITIES IN TANSITION” CWF-2016, 28th to 30th November 2016, held at Palace Grounds, Bengaluru, India.
10. Faculty Development Program on “Integration of Green Energy to Smart Grid – Opportunities and Challenges”, 11th to 16thJuly2016, BNMIT, Bangalore, India.
11. Workshop on “Recent Trends in Power Quality Issues and Mitigations in Electrical Systems” 18th to 23rd January 2016, BNMIT, Bengaluru, India.
12. Workshop on “Preparation for Accreditation by NBA” 3rd to 5th December 2015, BNMIT, Bengaluru, India.
13. Workshop on “Programmable Logic Controller & SCADA Systems” 19th to 23rd January 2015, BNMIT, Bangalore, India.

14. Faculty Development Program on 'IT Infrastructure Management Services', 5th to 9th January 2015, BNMIT, Bangalore, India.
15. Workshop on 'Power Quality and Custom Power Devices', 3rd to 7th June 2013, RVCE, Bangalore.
16. International Workshop on 'Power Quality Issues and Challenges', 18th and 19th November 2011, CPRI, Bangalore, India.
17. Workshop on 'Research Methodologies & Latex', 5th and 6th August 2011, VTU e-learning center, VTU Belgaum, India.
18. 'Research Workshop on Signal and Image Processing, 28th and 29th January 2010, BNMIT, Bangalore, India
19. 'Introduction to Research Investigations in Recent years in Electrical and Electronics Engg', 16th March 2009, BNMIT, Bangalore, India.
20. 'Advancements in High Power Drives', 21st and 22nd November 2008, UVCE, Bangalore.
21. 'Power Electronics and Power Management', 4th and 5th June 2008, SJCE, Mysore
22. 'Renewable Energy for Electrical Power Generation and Thermal Applications', 31st August to 1st September 2007, MSRIT, Bangalore
23. Workshop on 'Power System Protection', 23rd and 24th February 2007, SSIT, Tumkur, India.
24. Seminar on 'e- Education', 3rd June 2006, RVCE, Bangalore, India.
25. Seminar on 'Current Trends in Transmission and Distribution of Electrical Power', 10th to 11th March 2006, NHCE, Bangalore, India.
26. Workshop on 'Power System Protection – Recent Industrial Practices', 19th October 2005, MSRIT, Bangalore, India.
27. Workshop on 'Emerging trends in applications of Electrical and Electronics Engineering' 28th May 2005, MVJCE, Bangalore, India
28. Quality Improvement Program on 'Instructional Design and Delivery', 8th to 14th August 2004, BNMIT, Bangalore, India.
29. Short Term Training Program on 'Recent Trends in Electrical Engineering.' 23rd to 28th February 2004, Nitte Institute of Technology, Bangalore, India.

Visits Abroad:

1. Visited Penang, Malaysia, as a co-author along with guide Dr. Vidya H.A, to deliver lecture on "Denoising of Partial Discharge Signals in Epoxy Glass Mica Sheets using Wavelet Transform", at the International Conference on Computer, Electrical and Systems Sciences, and Engineering, held between 22nd to 24th February 2011.

Personal Details:

- **Date of Birth:** 02 - 10 -1979
- **Gender:** Female.
- **Father's Name:** M.V. Suresh (Retd. JIO, Bharat Electronics, Bangalore)
- **Mother's Name:** Smt. S. Vasantha, Homemaker.
- **Husband's Name:** M.N. Sridhar (Regional Manager, Tungaloy India Pvt. Ltd, Mumbai)
- **Daughters' Name:** Ms. Dhanyatha S Sharma and Ms. Dheeptha S Sharma
- **Passport Number:** B3156667

17th July 2019

Priyashree S