


Faculty Profile

Name of Faculty	CHANDRASHEKAR C.	
Department	Electronics & Communication Engineering.	
Qualification	B.E, M. Tech (Ph.D)	
Designation	Assistant Professor	
Area of specialization	VLSI Design & Embedded Systems, Analog & Digital Communication.	
Date of Joining BNMIT	23.07.2012	
Nature of Association (Regular/Contractual/Adjunct)	Regular	
e-mail	chandrashekar@bnmit.in , chandrubnm@gmail.com	
No. of years of Experience	Teaching: 7 years.	

Academic Qualifications

- **M. Tech** (2012), Department of Electronics & Communication Engineering, VTU, India.(Distinction)
- **B.E.** (2009), Department of Electronics & Communication Engineering, VTU, India (First Class).
- Pursuing Ph.D at Research center, BNMIT, VTU, Belagavi.

Working Experience Details

- Asst. Professor, Dept. of Electronics & Communication Engineering, BNMIT, Bangalore, India, (2012 till date).
- Lecturer, Dept. of Electronics & Communication Engineering, BNMIT, Bangalore, India, (2009-2010).

Subject taught

Basic Electronics, Digital Electronics, Analog Electronics, Electronic Instrumentation, Microcontrollers, Microprocessors, Principles of Communication systems, Digital Communication, Embedded System design, Real Time Operating Systems.

Administrative responsibilities/positions held

Department Level:

1. IEEE Student branch Coordinator.
2. Placement Co-ordinator.
3. Test Co-ordinator.
4. NSS and sports Co-ordinator.

Research Publications:

I. In National & International Journals

International Journals:

1. Chandrashekar C, Akshay Nadig Kishan D G, Deepthi M R, Divyashree V, "**Three Tire Secured Voting Machine**", International Research Journal Of Engineering And Technology, Volume 6, ISSUE 5, May 2019.
2. Sapna, Chandrashekar C "**Constrained Random Verification Of PCIe Transaction Layer**" in International Research Journal Of Engineering and Technology. April 2017.
3. Shanthala N, Chandrashekar C, Dr Shiva Yellampalli, "**Basic Operation Performed on Arithmetic Logic Unit (ALU) For 32-Bit Floating Point Numbers**" (Initial Results). International Journal of Applied Engineering Research.

II. In National/International Conference Proceedings

1. Chandrashekar C, S.B. Bhanu Prashanth "**The Design Of High-Performance Asynchronous Static-Pipelines: Initial Results.**" in International conference on VLSI & Signal Processing at K.S. INSTITUTE OF TECHNOLOGY Bangalore, held on 4th -5th May 2012.

Participation in Training courses/Seminars/Workshops

1. Attended six day faculty development programme on "IoT Based project Design and Development" from 24th to 29th June 2019, organized by B N M Institute of Technology Bengaluru.
2. Attended two day faculty programme on "Design For Test Using Mentor Tessent Tool Suite" from 13th to 14th June 2019, organized by Department of electronics & Communication Engineering PES University.
3. Attended a five days Faculty Development Programme on "**MSP430 Microcontroller and Its Applications**" during 31st July, 2017 to 5th August, 2017, organized by B N M Institute of Technology Bengaluru.
4. Attended a Six day FDP on "**ARM Cortex M3**" held at BNMIT, Bengaluru from 8th to 14th January 2016 in association with Tronikbit Tech LLP, Bengaluru.
5. Attended a FDP on "**Electronic Communication & Trends**", held at BNMIT, Bengaluru from 19th to 23rd January 2015.
6. Attended a six day FDP on "**Physical Design Challenges in DSM Node VLSI Systems**".
7. Attended a FDP on "**VLSI Design Using Cadence Tools**". Held at KSIT, Bangalore from 29 June to 3rd July 2015.
8. Participated in workshop on "**Preparation for Accreditation by NBA**" from 3-5, December 2015 at BNMIT, Bengaluru.
9. Participated in workshop on "**Microprocessor and Microcontrollers Programming, Interfacing and Applications**" from 19th -21st, February 2014 at VIT, Bengaluru.

Personal Details:

- **Date of Birth:** 10-06-1988
- **Gender:** Male.
- **Marital Status:** Married

03rd August 2018

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