


Faculty Profile

Name of Faculty	DR. V. MURALIDHARA	
Department	Electrical & Electronics Engineering	
Qualification	B.E., M.E., Ph.D.	
Designation	Professor	
Area of specialization	Power systems and H V D C Insulators	
Date of Joining BNMIT	06/01/2018	
Nature of Association (Regular/Contractual/Adjunct)	Regular	
e-mail	v.muralidhara.1952@gmail.com vmuralidhara@bnmit.in	
No. of years of Experience	Teaching: 43 years inclusive of Research: 07 years.	

Academic Qualifications:

- **Ph.D.** (2012), Kuvempu University, Karnataka, India.
- **M.E.** (1978), Mysore University, Karnataka, India (I Class with Distinction).
- **B.E.** (1975), Mysore University, Karnataka, India (I Class with Distinction). (**IV Rank**).

Working Experience Details

- Professor of Electrical Engineering, BNMIT, Bangalore, (2018 to till date).
- Professor of Electrical Engineering and Associate Director, School of Engineering and Technology, Jain University, Bangalore.(2013 to 2017)
- Professor & HOD, Dept. of EEE, BIT, Bangalore, India, (1990 to 2012).
- Asst. Professor, Dept. of EEE, BIT, Bangalore, India (1981 to 1990).
- Lecturer, Dept. of EEE, PES College of Engineering, Mandya, India, (1976 to 1981).

Courses taught

Basic Electrical Engineering, Electric Network Analysis, Control systems, Electrical Machines, Power System Analysis, Transmission and Distribution, Electrical Power Utilization.

M.E.

Dissertation on “**Power frequency over voltages in EHV Power systems**” done with the support of Dept. of Power Engineering, IISc, Bangalore.

Research Experience Details:

- Topic on the “**Quality Analysis of Insulators for DC applications – A detailed study**”. This is a total experimentation work carried out at CPRI, Bangalore. As a result of the research work 4

papers have been published in International Conferences and in one paper in an International Journal.

- Presently guiding five research scholars under Jain University, in the domains of Lightning arrestors, Smart grids, Control of Induction Motors using Power Electronics.
- Developed the Electrical Machines laboratory, Relay and High Voltage laboratory, Electrical Measurements and Control Systems laboratory, at B I T Bangalore.

Research Supervision:

Sl. No.	Name of the student	University	Year of Registration	Status	Title of Research Topic
1	Ms.Avani Pujara	Jain University, Bangalore	24-8-2013	Pursuing	Priority based load Management and reduction in Unscheduled interchange charges in Smart Grid environment using ABT metering.
2	Ms.Geeta Vellal		24-8-2013	Pursuing	A Novel Distance Estimation Technique for Single line to Ground Fault in electrical transmission and Distribution Network Using smart Meter.
3	Ms.A.M.Leela		26-8-2013	Pursuing	Study on ageing characteristics of polymer housed Arresters.
4	Mr.Butta Mallikarjuna		26-8-2013	Pursuing	Study on the performance of Insulators under DC voltages.
5	Mr.Reddy Sudarshana		22-04-2017	Pursuing	Simulation With Experimental Validation and Study of Common Mode Voltage with Total Harmonic Distortion in Multilevel Inverter Fed Induction Motor Drive.

Academic Positions and other Responsibilities (University Level):

- **Chaired a session** at School of Engineering and Technology, Jain University in the one day National conference ‘JNANACHILUME-2019’ on the theme “Automation, Control and communication (NCACC-2019) On March 29th 2019
- **Member**, BoS, RVCE, Bangalore, VTU nominee.
- **Resource person**, first year Basic Electrical Engineering syllabus formation, VTU, Belagavi.
- **Member**, Academic council, Jain University, Bangalore.

- **Chairman, BoS and BoE in EEE, Jain University,**
- **Member for third party inspection** authority to ensure the quality and workmanship of Ganga Kalyana scheme, water works and regularization of unauthorized IP sets in Ramanagar Division of BESCO, (GOK assignment).
- **Chairman, BoE, in EEE, VTU, Belagavi.**
- **Member, BoE, in EEE, VTU, Belagavi.**
- **Member, BoS, NMIT, Bangalore.**
- **Member, BoS, PES College of Engineering, Mandya.**
- **Deputy Custodian, Engineering Valuations, VTU, Bangalore Region.**
- **Deputy Custodian, Engineering Valuations, Bangalore University,**
- **Subject expert for staff selection at, REVA Institute of Technology, New Horizon College of Engineering, BMS College of Engineering, KSIT, Bangalore.**
- **Subject expert for the standardization** of entrance examination questions for the selection of Engineers to State Electricity Board in support of IIM, Indore.
- **NSS officer at PES College of engineering, Mandya under University of Mysore.**

Academic Positions and other Responsibilities (Institute Level):

- **Chef Coordinator, NBA, BIT (2010 to 2012)**
- **Chief Superintendent** of Examinations at SET, Jain University, Bangalore from 2013 to 2017,
- Deputy Chief Superintendent (external), of examinations, VTU exams at SCTIT, Chickaballapur, DSCE, Bangalore.

Awards/ Achievements:

- Sixth rank in Pre-final year B.E., Mysore University.
- Fourth rank in Final year B.E., Mysore University.

Professional Memberships:

- Life Member, Indian Society for Technical Education (ISTE).
- Life Member, Institute of Engineers, India. (MIE).
- Life Member, International Microelectronics Assembly and Packaging Society (IMAPS).

Research Publications:

I. National & International Journals

- *V. Muralidhara, B. Ramachandra N. Vasudev, P.V. Vasudevan Nambudiri, K.N. Ravi.* **“Study of Quality Assessment Insulators subjected to DC voltages”**, The Academic Journal of High Voltage Engineering. Chongqing University, Chongqing, China 2008. Vol. 34, No. 12. December 2008.
- **“A Novel Distance Estimation Technique for Single Line to Ground Fault in Electrical Distribution Network Using Smart Meter”**, Published in the Journal of CPRI, Volume 11, Issue 01, March 2015, ISSN 0973-0338, PP. 19-26.

- **“A Review on Smart Grid in India”** Published in international journal, of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering, Vol. 3, Issue 3, and March 2015, PP.201-203, ISSN (Online) 2321-2004 ISSN (Print) 2321-5526
- **“Communication Feasibility Study for Smart Grid with Advanced Metering Infrastructure”** Paper is Published in International Journal of Electrical and Electronics, Engineering Research, Volume 5, issue 6, Dec 2015 impact factor 6.289 and NAAS rating 2.40, ISSN (P):2250-155X; ISSN(E): 2278-943X
- **“Smart Way of Controlling Frequency Linked Component of Availability Based Tariff”** Published in IEEMA Journal Volume 8 Issue no.8, PP. 54-57, ISSN 0970-2946 April 2017.
- **“Smart Transformer using Advanced Metering Infrastructure (AMI) and Advance Sensor Infrastructure (ASI)”** Published in International Journal of Innovative Research in Electrical, Electronics, Instrumentation and Control Engineering. (Impact factor 3.885) Vol. 3, Issue 8, August 2015, PP.5-8. ISSN (Online) :2321 – 2004. ISSN (Print) :2321 – 5526.
- *Reddy Sudarshana, A.Ramachandra, V.Muralidhara.* **“An experimental measurement of shaft voltage in induction motor driven by three level inverter”** IJARSE, Vol-6, Issue No.7, July 2017, pp 317-322, ISBN: 978 -988 -14047-4-9, ISSN:2078-0958(Print) ISSN:2078-0966 (Online)WCE 2017.
- *Reddy Sudarshana, A.Ramachandra, V.Muralidhar* **“Simulation and Experimental Evaluation of Current Harmonics under Steady State Conditions of variable speed Induction Motor drive system fed by Three level Inverter. a. IJEEE, Vol-9, Issue-No.1, Jan-June 2017, pp 603-612, IDIIF: 2.7, ISSN:2321-2055 (O) ISSN:2321-2045 (P).**
- *Reddy Sudarshana, A.Ramachandra, V.Muralidhara.* **“Simulation and Experimental Evaluation of variable speed Induction Motor drive system fed by VSI and study of current harmonics under steady state conditions. IJEEE, Vol-9, Issue-No.1, Jan-June 2017, pp 1-10, IDIIF: 2.7, ISSN:2321-2055 (O) ISSN:2321-2045 (P).**
- **“A Novel Approach of Fault Location in Electrical Transmission System using Smart Meters.”** Published on 28.02.2018 in Majlesi Journal of Electrical Engineering (indexed by: Scopus cite factor=0.24) , Vol 12 No 1 (2018): March, pp 39-46 ,p-ISSN: 2345-377X, e-ISSN: 2345-3796 and is available online on <http://mjee.iaumajlesi.ac.ir/index/index.php/ee/issue/view/53>.
- **“Smart Meter Modeling and Fault Location Communication in Smart Grid”** Published on 01.06.2018 in Majlesi Journal of Electrical Engineering (indexed by: Scopus cite factor=0.24) , Vol 12 No 2 (2018): June, p ISSN: 2345-377X, e-ISSN: 2345-3796, and is available online on <http://mjee.iaumajlesi.ac.ir/index/index.php/ee>
- *Avani Pujara, S M Bakre, V Muralidhara,* **“Smart Solution For Low Frequency Problem In Smart Grids”**, IJCEA, Vol-12, Special Issue, March-2018, ISSN 2321-3469

National Journals:

Papers presented in International/National Conferences

1. International

International conferences.

- *V. Muralidhara, M.Murugesh Mudaliar, N. Vasudev, K.N. Ravi, P.Krishnamurthy, Channakeshava.* **“Study on ion migration in insulators subjected to DC voltages”**, 13th International Symposium on High Voltage Engineering [ISH] 2003. Delft University of Technology, Delft. Netherlands, 25th to 29th August 2003.
- *V. Muralidhara, B. Ramachandra, N. Vasudev, P.V. Vasudevan Nambudiri, Sriramulu, K.N. Ravi.* **“Performance Evaluation of Insulators for DC Application”**, International Conference on Power Systems [ICPS] 2007 at Central Power Research Institute, Bangalore, 12th to 14th Dec’2007,

- *V. Muralidhara, B. Ramachandra, N. Vasudev, P.V. Vasudevan Nambudiri, Sriramulu, K.N. Ravi.* **“Study of Thermal-runaway tests on Insulators subjected to DC voltages”**, International Conference on High Voltage Engineering and Application [ICHVE 2008], Chongqing University, Chongqing, China, 9th to 13th Nov. 2008.
- *V. Muralidhara, B. Ramachandra N. Vasudev, P.V. Vasudevan Nambudiri, K.N. Ravi,* **“A detailed study on the quality assessment of insulators subjected to DC voltages”**, International Conference on Properties and Applications of Dielectric Materials [ICPADM] 2009, July 19th to 23rd ‘2009 at Harbin, China.

International Proceedings.

- *Reddy Sudarshana, A.Ramachandra, V.Muralidhara, R.Srinivasan,* **“Simulation and Experimental validation of Common Mode Voltage in Induction Motor driven by Inverter using Arduino Microcontroller”**, Proceedings of the World Congress on Engineering 2017, vol-1, WCE 2017, July 5-7, 2017, London U.K., pp 295-300, ISBN 979-988 -14047-4-9 ISSN: 2078-0958 (print) ISSN: 2078-0966 (online).

International / National Conferences.

- *Raghunath Ramaswamy, V Muralidhara, D Thukaram,* **“Influence of Ground Impedance on unbalanced Analysis of Grid,”** National conference NPSC-2019 at NIT Trichi held from 14th to 16th Dec’2019.
- *Raghunath Ramaswamy, V Muralidhara, D Thukaram,* **“Experimental Validation and Review of Neutral Voltage Modeling for Unbalanced System Analysis”**, INDICON-2018 ,15th to 17th Dec’2018 ,Coimbatore.
- *Leela A M, I K N Ravi, V Muralidhara, N Vasudev,* **“Study of Characteristics of Zno Surge Arrester”**, International Conference on High Voltage Engineering and Technology (ICHVET-2015)-January, 29-30, 2015, Hyderabad .
- *Leela A.M., V. Muralidhara, K.N. Ravi, N. Vasudev, R.S. Shivakumara Aradhya,* **“ Thermal Behavior of Metal Oxide Surge Arrester Block”**, I J C T A, 9(10), 2016, pp. 1-11, ISSN: 0974-5572.
- *Velhal Geeta Vilas, Avani Pujara ,Dr. S.M.Bakre, Dr. V.Muralidhara* **“Fault Current Limitation Communication in Electrical Distribution System using Smart Meter”** Published in National conference proceeding “Inspire Innovations and Emerging trends in Electronics Telecommunication and Electrical Engineering on March 2016 at JSPM college, Hadapsar Pune.).
- *Velhal Geeta Vilas, Avani Pujara ,Dr. S.M.Bakre, Dr. V.Muralidhara,* **“UI Charge Reduction and Load Management in Smart Grid”**, Published in National conference proceeding “Inspire Innovations and Emerging trends in Electronics Telecommunication and Electrical Engineering on March 2016 at JSPM college, Hadapsar, Pune.)

International Conferences.

- *Velhal Geeta Vilas, Avani Pujara ,Dr. S.M.Bakre, Dr. V.Muralidhara,* **“On Progress of Communication Infrastructure for Fault Location and ABT Metering”** Paper has been presented and Published in proc. Wireless Computing and Networking (GCWCN), 2014 IEEE

Global Conference onand is archived in IEEE Xplore digital library, Date Added to IEEE Xplore: 05 February 2015 ,PP. 47-50. **Electronic ISBN: 978-1-4799-6298-3 DVD ISBN: 978-1-4799-6297-6.**

- *Velhal Geeta Vilas, Avani Pujara ,Dr. S.M.Bakre, Dr. V.Muralidhara* “**Implementation of Metering Practices in Smart Grid**”, Paper has been presented and Published in proc. Smart Technologies and Management for Computing, Communication, Controls, Energy and Materials (ICSTM), 2015, (1 CITATION), Date Added to IEEE Xplore: 27 August 2015 , Date Added to IEEE Xplore: 27 August 2015, PP.484-487 ,**Electronic ISBN: 978-1-4799-9855-5.**
- *Velhal Geeta Vilas, Avani Pujara ,Dr. S.M.Bakre, Dr. V.Muralidhara* “**A Diagnosis of Fault Location System for Electrical Transmission And Distribution Network**”, Proc. 2015 International Conference on Energy Systems and Applications (ICESA 2015) Dr. D. Y. Patil Institute of Engineering and Technology, Pune, India 30 Oct - 01 Nov, 2015, PP 63-66, **Electronic ISBN: 978-1-4673-6817-9’.** **Date Added to IEEE Xplore: 04, July 2016.** **DOI: 10.1109/ICESA.2015.7503332.**
- *Velhal Geeta Vilas, Avani Pujara ,Dr. S.M.Bakre, Dr. V.Muralidhara* “**Hybrid P2P Communication System for Fault Location on Ring Main Distribution System**”, Published in proceeding of IEEE International Conference on Energy Systems and Applications, D.Y.Patil College of Engineering Pune, and archived in IEEE Xplore digital library. **Electronic ISBN: 978-1-4673-6817-9,Date Added to IEEE Xplore: 04 July 2016, DOI: 10.1109/ICESA.2015.7503417.**
- *Leela A.M., V. Muralidhara, K.N. Ravi, N. Vasudev, R.S. Shivakumara Aradhya* "**Simulation and Analysis of ZnO block Characteristics Using MATLAB**", 2017 IEEE International Conference on Intelligent Techniques In Control, Optimization And Signal Processing. 978-1-5090-4778-9/17 ©2017 IEEE.
- *Leela A M K N Ravi V Muralidhara R S Shivakumara Aradhya N Vasudev,* "**Simulation of ZnO block Characteristics using MATLAB**", National Conference on High Voltage Engineering & Technology (NCHVET2017), CPRI, Bangalore, 27-28 January 2017.
- *Leela A M, V Muralidhara , K N Ravi , N Vasudev , R S Shivakumara Aradhya,* "**Behavior of Polymeric Housed Metal Oxide Surge Arrester Under Pollution**" *IJARIE-ISSN (O)-2395-4396, Vol-1 Issue-5 2016. C-1402.*

Visits Abroad:

- Visited Chongqing University, Chongqing, China during 2008 to present a paper at International Conference, ICHVE-2008

Personal Details:

- **Date of Birth: 18 - 12 –1952**
- **Sex: Male.**
- **Family Details:** Wife and one son and one daughter.
- **Passport Number: L9227435** (valid till 30TH April 2024)

1th August 2019

V.MURALIDHARA