

Faculty Profile

Name of Faculty	Sumathi A
Department	Electronics and Communication Engineering
Qualification	B.E. M.Tech., (Ph.D.)
Designation	Associate Professor
Area of specialization	Digital Electronics and Communication
Date of Joining BNMIT	14.02.2007
Nature of Association (Regular/Contractual/Adjunct)	Regular
Mobile Number	9901995455
e-mail	sumathia@bnmit.in, sumathi_ayn@yahoo.com
No. of years of Experience	Teaching:17 years



Academic Qualifications

- **Ph.D.** pursuing, Department of ECE, BMSCE, VTU, Belagavi.
- **M.Tech.**, (2006), Digital Electronics and Communication, Department of ECE, Dayanand Sagar College of Engineering, VTU, Belagavi. (I Class).
- **B.E.** (1998), Instrumentation Technology, Malnad College of Engineering, Mysore University, Mysore (I Class).

Working Experience Details

- Associate. Professor from August 2015 till date, Department of Electronics and Communication Engineering, BNM Institute of Technology, Bangalore.
- Associate. Professor from March 2012 to July 2015 Department of Telecommunication Engineering, BNM Institute of Technology, Bangalore.
- Assistant. Professor from January 2011 to February 2012, Department of Telecommunication Engineering, BNM Institute of Technology, Bangalore.
- Sr. Lecturer from January 2008 to December 2010, Department of Telecommunication Engineering, BNM Institute of Technology, Bangalore.
- Lecturer from February 2007 to December 2007, Department of Telecommunication Engineering, BNM Institute of Technology, Bangalore.
- Lecturer from September 2006 to January 2007, Department of Instrumentation Technology, DSCE, Bangalore.
- Lecturer from September 2005 to December 2005 and March 2004 to December 2004, Department of ECE, BMS Evening College of Engineering, Bangalore.
- Lecturer from April 2001 to June 2002, Department of Instrumentation Technology, BMS College of Engineering, Bangalore.
- Lecturer from June 2000 to March 2001, Department of Instrumentation Technology, DSCE, Bangalore.
- Lecturer from June 1999 to December 1999 at SCBRP, Arasikere.

Subjects taught:

Control Systems, Wireless Communication, Antennas and Propagation, Logic Design, programming in C++, Microcontrollers, Hardware Description Language, Analog Communication, VLSI Design, Embedded System Design, Electronic Instrumentation, Digital Switching System, Microprocessor

(8085 & 8086), Process Instrumentation, Optical Instrumentation, Computer Organization, Power Electronics

Research Experience Details:

M. Tech.,: Project: High Speed PCB Design – Issues

This project involved studying the effect of high frequency signals in a Printed Circuit Board and there by designing an optimum Printed Circuit Board which has better performance for high frequency signals. This better performance is obtained by improving signal integrity and EMI performance

Academic Positions and other Responsibilities (University Level):

1. **Deputy Chief Superintendent** (External) for examination centre RNS Institute of Technology, from 27th May to 21st June 2019
2. **Deputy Chief Superintendent** (External) for examination centre RNS Institute of Technology, from 6th to 23rd January 2014

Academic Positions and other Responsibilities (Institute Level):

1. **Main Co-coordinator(VTU)** for conducting VTU theory exam during May - July 2015

Academic Positions and other Responsibilities (Department Level):

1. **FCD/BETA Coordinator** from Aug 2016 to May 2019.
2. **Organizing Committee member** for Faculty Development Programme on Computer and Wireless Networks from 11th to 16th July, 2016 and “MSP430 Microcontroller and Its applications” from 31st July to 5th August, 2017 held in Dept. of ECE
3. **Seminar Coordinator** for 8th Semester, from February 2016 to May 2016
4. **Time table** Coordinator from August 2015 to May 2016.
5. **Sports Coordinator** from Aug 2013 to December 2013
6. **Test coordinator** from February 2010 to November 2010, February 2012 to June 2012,
7. **ISO coordinator** from February 2009 to December 2009 and June 2010 to December 2012.

Awards/ Achievements/Memberships:

- Received Merit Incentive scholarship, BE IV Sem at MCE, Hassan

Participation in Training courses/Seminars/Workshops

1. **Faculty Development Programme** on “IoT based Project Design and Development” at **BNMIT** from 24th June to 29th August, 2019.
2. **Faculty Development Programme** on “MSP430 Microcontroller and Its applications” at **BNMIT** from 31st July to 5th August, 2017.
3. **Faculty Development Programme** on “Physical Design Challenges in DSM Node VLSI Systems” at **BNMIT** from 16th to 21st January, 2017.
4. **Symposium** on “Tech Treat on Emerging Technologies” at **BNMIT** on 4th& 5th August 2016

5. **Faculty Development Programme** on “Computer and Wireless Networks” at **BNMIT** from 11th to 16th July, 2016.
6. **Faculty Development Programme** on “ARM CORTEX M3” at **BNMIT** from 8th to 14th January, 2016.
7. **Faculty Development Program** on “Electronic Communication & Trends” at **BNMIT** from 19th to 23rd January, 2015.
8. **Training** on “Cyber Security and Cyber Laws” on 20th Aug 2014
9. **Faculty Development Program** on “Antenna Design” at **RVCE** from 26th to 28th June 2014
10. **Faculty Development Program** on “Antenna Technologies” at **BMSCE** from 27th Jan to 1st Feb 2014
11. **Faculty Development Program** on “Innovative Teaching and Effective Communication” at **BNMIT** from 18th to 20th Jul 2012.
12. **Faculty Development Program** on “Innovative Teaching and Effective Communication” at **BNMIT** from 18th to 20th Jul 2012.
13. **Faculty Development Program** on “Emerging Trends and Technology in RF Design” at **AMC College of Engineering**, Bangalore from 17th to 19th Jan 2011.
14. **Research Workshop** on “Signal and Image processing” at **BNMIT** on 28th and 29th of January 2010.
15. **COMSOL Conference 2009** at ITC Windsor Manor, Bangalore on 13th & 14th November 2009.
16. **Awareness Program** on “ISO 9001:2008 Quality Management System” on 23rd September 2009 at **BNMIT**.
17. **One day workshop** on “Wireless networks” by **BITES- WIPRO** at **GAT** on 18th April 2009.
18. **Two day workshop** on “Programmable System On Chip” at **NMIT** from 9th to 11th February 2009.

Personal Details:

- **Date of Birth:** 01 - 11 -1976
- **Gender:** Female.
- **Marital Status:** Married

1st August 2019

SUMATHI A