

Faculty Profile

Name of Faculty	Vrunda Kusanur
Department	ECE
Qualification	M.Tech., (Ph.D.)
Designation	Assistant Professor
Area of specialization	Wireless Sensor Networks, Arduino Programming, Machine Learning, Implementation of digital circuits on FPGA using Verilog & VHDL, Embedded Systems
Date of Joining BNMIT	11.08.2004
Nature of Association (Regular/Contractual/Adjunct)	Regular
Mobile Number	9902401201
e-mail	vrundakusanur@bnmit.in, vrunda1978@gmail.com
No. of years of Experience	Teaching: 16 years



Academic Qualifications

- **(Ph.D.)**, Department of Electronics and Communication Engg. , VTU, India.
- **M.Tech.** (2006), Department of Applied Electronics, Dr. M.G.R. Educational and Research Institute (Deemed University), Chennai, India (First Class with Honours).
- **B.E.** (2000), STJ Institute of Technology (Karnataka University), Ranebennur (Haveri district), India (First Class).

Working Experience Details

- Asst. Professor, Dept. of ECE, BNMIT, Bangalore, India, (2012 – Till date).
- Senior Lecturer, Dept. of ECE, BNMIT, Bangalore, India, (2008 – 2012).
- Lecturer, Dept. of ECE, BNMIT, Bangalore, India, (2004-2008).
- Lecturer, Dept. of ECE, Dayananda Sagar College of Engineering, Bangalore, India, (2001-2003).

Subject taught

Topics Taught: Digital Electronics, Basic Electronics, Verilog HDL, HDL, Information Theory and Coding, Programming in C++, Embedded System Design, DSP architecture and algorithms, Network Security, Logic Design, Computer Networks, System Verilog

Academic Positions and other Responsibilities (Institute Level):

1. **Deputy Chief Superintendent** (External) at SJBIT, Bangalore from 29/12/2016 to till completion of the exam for the conduction of December 2016 / January 2017 UG/ PG examinations of VTU, Belgaum.
2. **Deputy Chief Superintendent** (External) at JSSAT, Bangalore, for the conduction of January 2013 PG examinations of VTU, Belgaum.

3. **Deputy Chief Superintendent** (External) at KSIT, Bangalore from 29/11/2012 to 13/12/2012 for the conduction of December 2012 / January 2013 UG/ PG examinations of VTU, Belgaum.

Academic Positions and other Responsibilities (Department Level):

- ISO Coordinator
- Time Table Coordinator
- VTU Exam Coordinator
- Sports Coordinator
- Test Coordinator
- CMS Coordinator
- T5 Mentor

Instruments Handled/Software's handled:

- HDL Lab
- DSP Lab
- Computer Networks Lab
- Logic Design Lab
- AC-LIC Lab
- Microcontroller Lab
- ADE Lab

Awards/ Achievements/Memberships:

- The project titled “Communication Aid for Neurological Patients” in UG sponsored by KSCST, Bangalore

Professional Memberships:

- Life Member, Indian Society for Technical Education (ISTE)

Research Publications:

I. International Journals:

1. “Design and Implementation of Verification IP for AMBA-AXI Lite Bus Protocol Using System Verilog for SOC Systems”, Supriya Netkal, Vrunda Kusanur & Aravindkumar Gumtanj, IRJET, Volume 3, Issue 6 held during June 2016.
2. “Implementation of Error Recovery in TMR Systems using Scan Chain-based Technique”, Sarfraz Quareshi and Vrunda Kusanur, International Journal of Engineering Research & Technology, Volume 4, Issue 5 held during May 2015.

Papers presented in International/National Conferences

International

1. **“Smart Urban Rooftop Greenhouse with WSN based precision technology”**, Vrunda Kusanur, Dr. Veena S Chakravarthi and Siddalingayya G, 2nd International Conference on Agriculture, Food and Biotechnology-ICAFB 2019 at Singapore during January 23-25, 2019
2. **“Conceptual Frame Work of Smart WSN for Bangalore Urban Environment Monitoring”**, Dr. Veena S Chakravarthi, Rashmi S Bhaskar and Vrunda Kusanur, IEEE-2012 at Phuket, Thailand, 4th International Conference on Computational Intelligence, Communication Systems and Networks held during 24-26th July 2012, ISBN:978-0-7695-4821-0

National

1. **“Implementation of Score Board to verify LPDDR2 Memory Controller”**, Gadve Nikita Shradhanand and Vrunda Kusanur, 7th IETE Conference on RF and Wireless (ICoN RFW-14) @ the HKBKCE, Bangalore held during 8-10 May 2014.
2. **“Study and Implementation of Topologies for WSN based routing Protocols”**, Deepa V Swamy and Vrunda Kusanur, Conference on Recent Trends in Industrial Electronics and Instrumentation, CRTIEI-2013 at RNSIT, Bangalore held during 2013.

Participation in Training courses/Seminars/Workshops

1. Participated in FDP Programme on **“IOT based Project Design and Development”**, from 24th to 29th June 2019 at BNMIT, Bengaluru.
2. Participated in FDP Programme on **“Artificial Intelligence & Machine Learning by M/s Cognizan”**, on 20th & 21st June 2019 at BNMIT, Bengaluru.
3. Participated in FDP Programme on **“IoT in Real Life Applications from Product Development Perspective”**, from 9th to 13th JULY, 2018 at BNMIT, Bengaluru.
4. Participated in FDP Programme on **“Recent Trends in Wireless Sensor Networks with hands on sessions using NS-2”** from 1st to 3rd Feb 2017 at SVIT, Bengaluru.
5. Participated in FDP Programme on **“Physical Design Challenges in DSM Node VLSI Systems”** from 16th to 21st Jan 2017 at BNMIT, Bengaluru.
6. Participated in two days Symposium on **“Tech Treat on Emerging Technologies” on 26th August 2016 at BNMIT, Bangalore.**
7. Participated in FDP Programme on **“Computer and Wireless Networks”** from 11th to 16th July 2016 at BNMIT, Bengaluru.
8. Participated in FDP Programme on **“NS-3 and its Advanced Usage”** from 27th June to 1st July 2016 at RNSIT, Bengaluru.
9. Participated in FDP Programme on **“ARM CORTEX M3(Practically Intensive)”** from 8th to 14th Jan 2016 at BNMIT, Bengaluru.
10. Participated in Workshop on **“Preparation for Accreditation by NBA”** from 3rd to 5th December 2015 at BNMIT, Bengaluru.
11. Participated in FDP Programme on **“Electronic Communication & Trends”** from 19th to 23rd Jan 2015 at BNMIT, Bengaluru.
12. Participated in FDP Programme on **“Research Methodologies & Latex”** from 29th to 31st Dec 2014 at Vidya Vikas Institute of Engineering & Technology, Mysore.
13. Participated in one day workshop on **“Wireless and Adhoc Sensor Networks”** on 26th April 2013 at RVCE, Bangalore.
14. Participated in FDP Programme on **“Innovative Teaching & Effective Communication”** from 18th to 20th July 2012 at BNMIT, Bengaluru.
15. Participated in two days workshop on **“Research Workshop on Signal & Image Processing”** from 28th to 29th January 2010 at BNMIT, Bengaluru.
16. Participated in short Term Training Programme on **“Microcontrollers and HDL Lab”** from 30th Jan to 1st Feb 2008 at RNSIT, Bangalore

17. Participated in two days seminar on “**Trends in Electronics & Communication**” from 19th to 20th June 2007 at DSCE, Bangalore.
18. Participated in two days seminar on “**Recent Trends in VLSI & Embedded System Design Technology**” from 12th to 13th April 2007 at Global Academy of Technology, Bangalore.
19. Participated in Quality improvement programme on “**Instructional Design And Delivery**” from 9th Jan to 14th Aug 2004 at BNMIT, Bangalore.

Personal Details:

- **Date of Birth:** 22 - 07 –1977; **Gender:** Female.
- **Marital Status:** Married.
- **Family Details:** Husband and two sons
- **Passport Number:** (valid till 21st December 2027)

1st August 2019

VRUNDA KUSANUR