


## Faculty Profile

<b>Name of Faculty</b>	Mrs Anuradha J P	
<b>Department</b>	Electronics & Communication Engineering	
<b>Qualification</b>	B.E, M.S.	
<b>Designation</b>	Assistant Professor	
<b>Area of specialization</b>	Embedded Systems & VLSI Design	
<b>Date of Joining BNMIT</b>	02.04.2008	
<b>Nature of Association (Regular/Contractual/Adju nct)</b>	Regular	
<b>Mobile Number</b>	9886595808	
<b>e-mail</b>	anuradhajp@bnmit.in, anu1572@yahoo.co.in	
<b>No. of years of Experience</b>	Teaching: 25.6 years	

### Academic Qualifications

- **M.S.** (1997), Electronics & Control, BITS, Pilani, India (CGPA-6.6).
- **B.E .** (1993), Electronics & Communication (I Class with Distinction).

### Working Experience Details

- Asst. Professor, Dept. of Electronics & Communication, BNMIT, Bangalore, India, (2012 – Till Date).
- Sr. Lecturer, Dept. of Electronics & Communication, BNMIT, Bangalore, India, (2008 – 2012).
- Asst. Professor, Dept. of Electronics & Communication, SJMIT, Chitradurga, India, (2006-2008)
- Sr. Lecturer Dept. of Electronics & Communication, SJMIT, Chitradurga, India, (2005-2006 )
- Lecturer Dept. of Electronics & Communication, SJMIT, Chitradurga, India, (1993-98,1999-2004)

### Subject taught

Basic Electronics, Digital Electronics, Analog Electronics, Microprocessors -8085  
Microprocessors - 8086, Microcontrollers, DSP Architecture & Algorithms, Computer Networks,  
Hardware Description Language VHDL & Verilog, Embedded Systems, ARM CortexM3 & Embedded  
Systems, MSP430 Microcontrollers, IOT

### Academic Positions and other Responsibilities (University Level)

- **Deputy Chief Superintendent** (External) at Dayananda Sagar College of Engg, Bangalore in 2014 for the conduction of June 2014 B.E. examinations of VTU, Belgaum

### **Academic Positions and other Responsibilities (Department Level):**

- Time Table Coordinator
- Test Coordinator
- Project Coordinator
- Seminar Coordinator
- Web Site Coordinator
- Academic Counselor

### **Instruments Handled/Software's handled:**

- IDE for MSP430 microcontrollers
- IDE for ARM Cortex M3
- Xilinx tool for HDL
- FPGA Boards
- ARM Cortex M3 kits
- MSP430 Launch Pad

### **Professional Memberships:**

- **Life Member, Indian Society for Technical Education (ISTE) –LM 16193**

### **Workshops /Seminars Organized:**

- Coordinator for Two days students workshop for skill development on "MSP430 Microcontroller" in association with Jyothi Embedded Labs at BNMIT on 14th & 15th October 2017

### **Research Publications:**

#### **International Journals:**

1. Dhanushree, **Anuradha J.P**, "Automatic Medicine Dispenser for Hospitals and Old Age Homes", International Research Journal of Engineering & Technology, Vol. 05, Issue 4, April 2018, Impact factor-6.17
2. Kavya .M, **Anuradha J P**, "Wavelet selection for vein Pattern extraction" PiCES, Vol.1,Issue 3,June 2017
3. Kavya .M, **Anuradha J P**, "Vein Pattern Extraction: A Review", IJERT, Vol.6, Issue 05,May 2017
4. Basavaraj Police Patil D, **Anuradha J.P**, Shanthi.V. "Design & Development of Verification Environment to Verify GPIO Core" using UVM ,IJSRP Vol.5,Issue 6 under ISSN 2250-3153, June 2015

## **Papers In National/International Conference**

### **International**

1. Nayana , Anuradha J P “Electro-Encephalographic Study & Survey of Feature Extraction For Emotion Detection using Wavelets” at ICSPCCR, May 18-19,2016
2. Sandhya, Anuradha J P "Wireless Sensor Network for River Water Quality & Level Monitoring” in International Conference on Advances in Computer & Communication Engg. organized by Dept of Computer Science on 21<sup>st</sup> &22<sup>nd</sup> April2014

### **National**

1. Anitha Kumari, **Anuradha J P** “Design & Implementation of Command generation Unit for LPDDR2 Memory Controller” using system verilog at National Conference on 3 Cs Compute, Communicate & Control on 5<sup>th</sup> May 2014 at MVJ College of Engg, Blore.
2. **Anuradha J P** “Micro impedance biosensors for bacterial cell detection using microcontroller” at National Conference at AMC Engg. College on 3 May 2013.
3. **Anuradha J P** & Chellamma K " Secret Message Transfer system using microcontroller”, National Conference at BIT on April 6, 2013

### **Invited Talks Delivered:**

- "Microprocessors & its future" at SJM Polytechnic in Oct 2000 at Chitradurga
- "Engineering & Society" at Girls Junior College in 1999 at Chitradurga

### **Participation in Training courses/Seminars/Workshops**

- FDP on “Transcending Language Skills”, at BNMIT, held between 15<sup>th</sup> -25<sup>th</sup> July 2019.
- FDP on “IoT based Project Design &Development” Department of ECE, BNMIT, held between 24<sup>th</sup> -29<sup>th</sup> June 2019.
- FDP on “ARM CortexM3 & Embedded Controller” Department of ECE, BIT held between 1<sup>st</sup> to 5<sup>th</sup> Jan 2018
- FDP on “MSP430 Microcontroller & Its Application” Department of ECE, BNMIT, held between July 31<sup>st</sup> to 5<sup>th</sup> August 2017
- FDP on Physical Design Challenges in DSM Node in VLSI Systems”, Department of ECE, BNMIT, held between 16<sup>th</sup> to 21<sup>st</sup> January 2017.
- Short course on “Innovative Teaching Methods” at BNMIT from 18<sup>th</sup> to 23<sup>rd</sup> July 2016
- FDP on “Computer and wireless networks”, Department of ECE, BNMIT, held between July 11<sup>th</sup> to 16<sup>th</sup> 2016

- FDP on "Electronic Trends in Communication", Dept. of ECE, BNMIT, Bangalore, 19th to 24th Jan 2015
- 2 day workshop on "NuvotonNuMicro ARM M0 Cortex & its application" at MSRIT, Bangalore, held between September 10th&11th 2013
- Workshop on "TI Launchpad MSP430" at BNMIT, B'lore 7th March 2012
- 2 day workshop on Low Power Embedded Systems using MSP430 microcontrollers held at Texas Instruments India, Bangalore during Jan 18-19,2012
- FDP on "SoC Design-A Walk Through" conducted by BNMIT, Bangalore held during 17-19 January 2011
- Workshop on "Signal & Image Processing" conducted by BNMIT, Bangalore held during 28-29 January 2010
- Workshop on "Tanner Tools Pro" conducted by BITE, Davangere held on 16th November 2006
- Short Term Course on "Embedded System Design using Micro Controllers" at IISC between 24-28 October 2005
- 3 days Workshop on VLSI Design Labs at BIT, Bangalore held between 7th to 9th August 2004
- Certificate Course in "Advanced Microprocessor & PC Training conducted by UVCE, Bangalore between 18th January -11th February1999

#### Personal Details:

- **Date of Birth:** 01 - 05 -1972;
- **Gender:** Female.
- **Family Details:** Husband & One son
- **Passport Number:** P9788699 (valid till 19th April 2027)

4<sup>th</sup> August 2019

Anuradha J P