


Faculty Profile

Name of Faculty	Vyasaraj T	
Department	Electronics and Communication	
Qualification	B.E, M.Tech	
Designation	Assistant Professor	
Area of specialization	Biomedical Signal Processing, Image Processing, Communication	
Date of Joining BNMIT	28.01.2019	
Nature of Association (Regular/Contractual/Adjunct)	Regular	
Mobile Number	9036028477	
e-mail	vyasaraj.t@gmail.com	
No. of years of Experience	Teaching: 6 years	

Academic Qualifications

- **M.Tech** (2013), Digital Electronics and Communication, K.V.G. College of Engineering, Sullia, V.T.U. Belagavi, India (First Class with Distinction).
- **B.E** (2011), S.L.N. College of Engineering, Raichur, V.T.U. Belagavi, India (First Class).

Working Experience Details

- Asst. Professor, Dept. of Electronics and Communication, VCET, Puttur, D.K, India, (15 July 2013 to 25 Jan 2019).

Subject taught

Digital System Design Using Verilog, Verilog HDL, Fundamentals of HDL, Network Analysis, Linear Integrated Circuits, Computer Communication Networks, Information Theory and Coding, Basic Electronics, Electronic Instrumentation, Microprocessor, Operating Systems.

Academic Positions and other Responsibilities (Department Level):

- E&C Department coordinator for NPTEL and Spoken tutorial online courses at BNMIT Bangalore.
- E&C Department Internship coordinator at BNMIT Bangalore.
- E&C Department coordinator for implementing ISO, from 2014-2016, at VCET Puttur
- E&C Department Activity coordinator from 2014-2015, at VCET Puttur.
- E&C Department coordinator for Institute Image Building Activities from 2017-2018 at VCET Puttur.
- E&C Department coordinator for VivekaSankalpa from 2017-2018 at VCET Puttur.

Professional Memberships:

- **Life Member, Indian Society for Technical Education (ISTE).**

Research Publications:

I. In National & International Journals

International Journals:

- Amith V, Vyasraj T “Development and Fabrication of Electro Pneumatic Automatic Stamping Machine” in IJIRSET, Volume 5, Issue 9, September 2016.
- PramodKumarNaik,ArunSTigadi, HansrajGuhilot and Vyasraj T,Presentedandpublishedapaperentitled,“Design and Analysis of Real Time Video Processing Based on DWT Architecture for Mobile Robots” at International Conference on Information Security & Privacy (ICISP2015), 11-12 December 2015, Nagpur, INDIA. Paper is published is Science Direct Procedia Computer science and published in ELSEVIER
- Amith V, Vyasraj T “Modelling& Simulation of Capacitive Pressure Sensor Using COMSOL Multiphysics 5.0” in IJIRSET, Volume 5, Issue 5, May 2016.

Papers presented in International/National Conferences

National

- Pramod Kumar, Vyasraj T, RamachandraBallary “**Detection of Moving Objects in Surveillance video**” National Conference on “Recent Trends in Communication and Computing (NCRTECC-16)” held at JIT , Davanagere on 27th Feb 2016.
- Presented a paper titled “**Fingerprint based Locker system**” in Jnanasangama 2015 held on 2nd May 2015.
- Presented a paper titled “**Jpeg2000 Image Transmission Based On OFDM System Using Unequal Power Allocation**” in the two day National Symposium on “**Low Power Solutions for Communications**” held at R.V College of Engineering, Bangalore during 19-20 April 2013.

Participation in Training courses/Seminars/Workshops

- NPTEL-AICTE one week Faculty Development Program on "**Biomedical Signal Processing**" (Jan-April 2019)
- Participated in one week FDP on "**IoT based Project Design and Development**" organized by department of Electronics and Communication BNMIT Bangalore on 24/06/2019 – 29/06/2019
- Participated in one day training program on "**Software Defined Radio with Zynq using Simulink**" at Mathworks India Private Limited Bangalore on 31 May 2019
- Participated in one day workshop on "Moodle Learning Management System" held at KS School of Engineering and Management, Bangalore organized by Teaching Learning Centre (ICT) at IIT Bombay on 15/03/2019
- Participated in the Workshop On "**Python Productivity for Zynq using PYNQ-Z2**" organized by Department of Electronic Systems Engineering, **Indian Institute of Science** held on 15th Dec 2018 in association with CoreEL Technologies and Xilinx.
- NPTEL-AICTE one week Faculty Development Programme on "**Hardware Modelling using verilog**" (Aug-Oct 2018)
- Participated in Faculty Training Program on "**Internet of Things**" at RDL labs, Sahyadri college of Engineering Mangaluru on 18/07/2018 to 28/07/2018
- Participated Three day Faculty Development Programme on "**Introduction to Python Programming and its Applications in Signal and Image Processing**" held at Vivekananda College of Engineering and Technology, Puttur, on 15/09/2017 to 18/09/2017
- Participated in five day Workshop on "**Board Design**" held at Vivekananda College of Engineering and Technology, Puttur, on 19/07/2016 to 23/07/2016.
- Participated in three day Workshop on "**Application of SCILAB to Digital Signal Processing**" held at Vivekananda College of Engineering and Technology, Puttur, on 01/09/2016 to 03/09/2016
- Participated in the Faculty Development Programme on "**PN Junction and BJT Devices**" held at Vivekananda College of Engineering and Technology, Puttur, during 11-13 April 2016.
- Participated in the Faculty Development Programme on "**Teaching Pedagogy-Level 2**" held at Vivekananda College of Engineering and Technology, Puttur, during 10-12 March 2016.
- Participated in the Foundation Course on "**Signals & System**" held at Vivekananda College of Engineering and Technology, Puttur, during 20-24 Aug 2015.
- Participated in the Faculty Development Programme on "**Teaching Pedagogy**" held at Vivekananda College of Engineering and Technology, Puttur, during 11-16 May 2015.
- Participated in two day National Conference on "**Signal and Image processing-2015**" held at Vivekananda College of Engineering and Technology, Puttur, during 01-02 May 2015.

- Participated in three day Workshop on “**Analog and Digital Design Using Cadence Tools**” held at Vivekananda College of Engineering and Technology, Puttur, on 30/07/2014 to 01/08/2014
- Participated in the Faculty Development Programme on “**Advanced Digital Design using Verilog**” held at Srinivas School of Engineering, Mangalore during 29-30 October 2013.
- Participated in two day National Conference on “**Low Power Solutions for Communications**” held at R.V College of Engineering, Bangalore during 19-20 April 2013.

Personal Details:

- **Date of Birth:** 20 - 05 –1990;
- **Sex:** Male.
- **Address :** #15,6th Main, DattatreyaNagara, Hosakerehalli, BSKIII, Bengaluru-560085
- **Languages:** To Write and Speak: English, Hindi, Kannada

03 Aug 2019
VYASARAJ T