

## Faculty Profile

<b>Name of Faculty</b>	Champa.PN
<b>Department</b>	Electrical & Electronics Engineering
<b>Qualification</b>	B.E, M.Tech.
<b>Designation</b>	Assistant Professor
<b>Area of specialization</b>	Computer Application in Industrial Drives, Power Electronics
<b>Date of Joining BNMIT</b>	12.09.2014
<b>Nature of Association (Regular/Contractual/Adjunct)</b>	Regular
<b>e-mail</b>	champapn@bnmit.in, champa.pn@gmail.com
<b>No. of years of Experience</b>	Teaching: 9 year



### Academic Qualifications

- **M.Tech.** (2014), Department of Electrical and Electronics Engineering, BNMIT, VTU, India.(FCD).
- **B.E.** (2006), Department of Electrical and Electronics Engineering, AIT, Chickmanglur, VTU, India (FCD).

### Working Experience Details

- Assistant Professor, Dept. of EEE, BNMIT, Bangalore, India, (Sep 2014-Till Date).
- Lecture, Dept. of EEE, SJBIT, Bangalore, India, (July 2010- July 2012).
- Full time teaching assistant cum faculty, Dept. of EEE, IETE, Bangalore, India, (July 2007– Oct'08)
- Lecture, Dept. of EEE, AIT, Chickmanglur, India, (Aug2006 – June 2007).

### Courses taught

- **Topics Taught** :Testing & Commissioning of Electrical Equipment ,Transformer And Generators, Basic Electrical Engineering ,DSP Application to drives, PEDC, Transformer and Induction Machine, Field Theory, Industrial Drives& Application, Switchgear & protections, Renewable Energy Recourses.

### Research Experience Details:

#### **BE PROJECT:"HOURLY ELECTRICAL LOAD FORECASTING"**

- Electrical load forecasting using **Artificial Neural Network for large power system**, by which we can predict the probable load for next day using the previous load data from K.E.B

- **Software Tool:** Artificial Neural Network, MATLAB 6.1 and MATLAB 6.5

### **M-Tech Project :”Implementation Of Interleaved Boost Converter With Photovoltaic Array Cells”**

- The project presents a detailed design and analysis of interleaved boost converter for a switching frequency of 41 kHz and different ripple reducing techniques. The high voltage gain converter is far suitable for applications where a high step up voltage is required, as in some renewable energy systems, which use, for example photovoltaic panels and/or fuel cells. As the power demand from these supply increases, a single boost converter may be insufficient. One of the disadvantages is that the input current ripple and output voltage ripple is more. In order to overcome this disadvantage of a single boost converter an interleaved boost converter can be implemented. In this project the simulation result of an interleaved boost converter is compared with single boost converter and the other ripple reducing techniques (ZVS & ZCS). Hardware implementation was also carried out; the results were compared with the simulation and were found to be satisfactory reducing ripple.
- **Software Tool :** Matlab Simulink

### **Ph.D**

- Enrolled for Ph.D. during 2018-19

### **Academic Positions and other Responsibilities (Department Level)**

- Time-Table coordinator (2018-2019)
- NBA Coordinator (2017-18, 2018-19)
- Website In-charge (2017-18)
- NAAC Coordinator (2016-17, 2017-18)
- VTU Exam Coordinator (Odd Sem 2017-18)
- T5, Tw5 Coordinator (2017-Till date)
- ISO-Coordinator (2015-2016,2016-17)
- Test coordinator (2015-16)
- NSS coordinator(2014-15)

### **Experimental/ Computational/ Any other Skills:**

### **One Year Advanced Diploma Course On Real Time Embedded Systems At Cranes Varsity**

- Advanced Diploma in RTOS from Cranes Software International Ltd, Bangalore which is a comprehensive training on embedded system programming, Vx-works, device driver etc

### **Projects (Done At Cranes Varsity)**

- **ATM Transaction (C++ Module):** Implementation of bank ATM Transactions - Withdrawals, Deposits, Fund transfer Generating transaction details.
- **Radio Frequency Identification (VX works Module):** Recognizing different ID's using RFID and maintaining LOG.
- **Micro Controller 8051:** Real time clock - Interfacing LCD and displaying time, date, month, year using DS1307/I2C protocol/keel micro vision c 51.

### **Awards/ Achievements/Memberships:**

- Topper during my B.E. (Final Year) AIT, Chickmangalur.
- Stood as Second topper during my masters at BNMIT, Bangalore.

### **Professional Memberships:**

- **Life Member, IAENG Society of Electrical Engineering.**
- **Life Member, ISTE Member**

### **Workshops /Seminars Organized:**

- Organized a workshop “**Programmable Logic Controllers and SCADA systems**” for a period of 5 days in EEE Dept. BNMIT Bangalore from 9-23 Jan 2015.

### **Research Publications:**

#### **I. In International Journals**

1. **Abhishek Kumar, Prashant Bajpai, Kumar shashant shekhar, Girish Kumar,Champa.PN** **Wireless Power Transfer for Mobile Charging Applications**, International Journal for Research in Applied Science & Engineering Technology Volume 6, Issue VI, **June 2018**[**Impact factor:4.59**]
2. **Champa.PN, Kruthi Jayaram, Design and development of a laboratory kit module for a dc-dc converter** , International Journal of Science and research (IJSR), Volume 7, Issue 5, **May 2018** [**Impact factor:7.296**]
3. **Sudha.B.V , Champap.PN , High power Factor Bridgeless SEPIC Converter to Improve Total Harmonic Distortion”** Volume 5, Issue 4, **April 2017** ,ISSN(online):2320-9801,ISSN(print): 2320-9798 “International Journal of Innovative Research in Computer and Communication Engineering” (Page No- 7768 to7774 ) **Impact Factor-6.577**
4. **Champa.PN, Implementation of SVM based Direct Torque Control for Traction Application** Issue 7 volume 1, **January –February 2017**, ISSN 2249-9954 of “International Journal of Advanced Scientific and Technical Research” (Page No-344-353)  
Available online on <http://www.rpublication.com/ijst/index.html>**Impact Factor-3.94**
5. **Champa.PN, Pavan.SK, Reduction of common mode voltage in Induction Motor Drives using various Scalar Based PWM Techniques** Issue 6, volume 4,**June 2016**,ISSN 2321-919X of The International Journal of Science and Technoledge.(Page No-184-189)  
**Impact Factor-1.002**

6. **Vindhya. N, Champa.PN, Modeling and analysis of Hybrid power systems with power converters**” has been accepted for publication in Issue 5, Volume 3, **May-June 2015**, ISSN 2249-9954 of “International Journal of Advanced Scientific and Technical Research” by RS Publications.(Page No-517-520) Impact Factor-3.94
7. **Champa.PN, Implementation Of Interleaved Soft Switching Boost Converter With Photovoltaic Source** has been accepted for publication in Issue 6, volume 4, June 2014 ,ISSN 2321-919X of “The International Journal of Science and Technoledge” .(Page No-222-229) **Impact Factor-1.002**

## **II. In National/International Conference**

1. **Champa.PN, Flexible AC Transmission System** , 3<sup>rd</sup> National Conference on Advanced Techniques in Electrical Engineering on 8<sup>th</sup> May 2010 at SJBIT, Bangalore
2. **Rakesh. H.L, Rajiv Stanilas Antony, Champa.P.N Implementation of PLC in Pharmaceutical Industry**” ,National Conference on “**Neoteric in Electrical and Electronics Enginnering**”**NEE-12** on 9<sup>th</sup>& 10<sup>th</sup> May 2012 at Rajarajeshwari College of Engineering, Bangalore
3. **Roshan.C.Ponnappa, Nayana.B.N, Champa.P.N , Evaluation of different PLC program methodologies for the support of flexible manufacturing processes** ,National Conference on “**Neoteric in Electrical and Electronics enginnering**”**NEE-12** on 9<sup>th</sup>& 10<sup>th</sup> May 2012 at Rajarajeshwari College of Engineering, Bangalore
4. **Champa.PN, Simulation Study of Single and Interleaved Soft Switching Boost Converter with Photovoltaic Source**, 5<sup>th</sup> National Conference on Advanced Techniques in Electrical Engineering on 10<sup>th</sup> May 2014 at SJBIT, Bangalore.
5. **Vindhya. N, Champa.PN , Modeling and Analysis of Hybrid Power systems** at National Conference on “**Recent Development in power Engineering**” held on 5<sup>th</sup> August 2015 at Sri Venkateshwara College of Engineering, Bengaluru

## **Participation in Training courses/Seminars/Workshops**

1. FDP on “**Application of Computational Intelligence in Engineering**” held during **24<sup>th</sup>-29<sup>th</sup> June 2019** organized by Dept. of EEE at BNM Institute of Technology, Bangalore-70
2. Two day workshop on “**Digital Signal Processing Lab**” held during **23<sup>rd</sup> -24<sup>th</sup> Jan 2018** conducted by department of EEE ,RNSIT, Bangalore
3. Workshop on “**A to Z of Transformer Design**” held during **17<sup>th</sup> & 18<sup>th</sup> Feb 2018** Organized by Dept. of EEE at BNM Institute of Technology, Bangalore
4. FDP on “**Modern Power Electronics Drives: Design & Future trends**” from **16<sup>th</sup> -20<sup>th</sup> Jan 2018** organized by Dept. of EEE at BNM Institute of Technology, Bangalore
5. Workshop on “**Innovative Teaching Methods**” organized by BNMIT in association with NITTTR, Chennai during **18<sup>th</sup> to 23<sup>rd</sup> July 2016**.(Quality Improvement Programme)
6. Workshop on “**Preparation for Accreditation by NBA**” from **3<sup>rd</sup> to 5<sup>th</sup> December 2015** held at BNM Institute of Technology, Bangalore.
7. FDP on “**Role of IT in Power Sector**” from **9<sup>th</sup> to 13<sup>th</sup> Jan 2017** organized by Dep. of EEE at BNM Institute of Technology, Bangalore

8. FDP on “**Integration of Green Energy to Smart Grid-Opportunities and Challenges**” from **11<sup>th</sup> to 16<sup>th</sup> July 2016** organized by Dept. of EEE at BNM Institute of Technology, Bangalore
9. FDP on “**Recent Trends in power Quality Issue and Mitigations in Electrical Systems**” from **18<sup>th</sup> to 23<sup>rd</sup> Jan 2016** organized by Dept. of EEE at BNM Institute of Technology, Bangalore
10. FDP on “**IT Infrastructure Management Services**” from **5<sup>th</sup> to 9<sup>th</sup> Jan 2015** organized by Dept. of ISE in association with Engineering Staff college of India, Hyderabad held at BNM Institute of Technology, Bangalore.
11. Three days FDP on “**Advanced Techniques in Electrical Engineering**” organized by Dept. of EEE during **Dec 2013** at SJB Institute of Technology, Bangalore.
12. Faculty Development Programme on “**Advances in Renewable & Power Engineering – Impulse towards Green Environment**” for 3-days during **25<sup>th</sup> July to 27<sup>th</sup> July 2013** held at SJB Institute of Technology, Bangalore.

#### **Personal Details:**

- **Date of Birth:** 14 - 02 –1985;
- **Sex:** Female
- **Family Details:**
  - Father Name – Nagarajappa.PS (Retired Principle from Government First Grade College)
  - Mother Name – Rukmani.JG (House Wife)
  - Brother Name – Chethan .PN
  - Husband Name - Yogesh.HG (Senior Manager in Sales & Marketing)
  - Daughter’s Name - Tanmayi HY& Namita. HY
- **Passport Number:** T0016255

21<sup>th</sup> July 2019

Champa.PN