

B.N.M. Institute of Technology

An Autonomous Institution under VTU, Approved by AICTE

Department of Electronics and Communication Engineering

Minutes of the Meeting

Meeting Details	PG BOS Meeting
Date	30-10-2021
Time	10:30 am
Venue	A-215 (Auditorium Block Seminar Hall)

Members Present	<ol style="list-style-type: none">1. Dr. P. A. Vijaya, Chairman, Professor & Head, Dept. of ECE, BNMIT, Bengaluru2. Dr. Vasundara Patel, VTU Nominee, Professor, Dept. of ECE, BMS College of Engineering, Bengaluru.3. Dr. T. V Prabhakar, Subject Expert (External), Principal Scientist, Indian Institute of Science, Bengaluru.4. Dr. S. N. Prasad , Subject Expert (External), Professor, School of ECE, Reva University, Bengaluru5. Dr. Pushpa Mala, Subject Expert (External), Associate Professor, Department of ECE, Dayananda Sagar University, Bengaluru6. Mr. Manjunath V, Subject Industry Expert (External), Staff Engineer, Synopsys India Pvt. Ltd., Bengaluru7. Dr. Subodh Kumar Panda, Member Secretary, Associate Professor, Dept. of ECE, BNMIT.8. Dr. Yasha Jyothi M Shirur, Subject Expert, Professor, Dept. of ECE, BNMIT9. Smt. Vrunda Kusanur, Subject Expert, Assistant Professor, Dept. of ECE, BNMIT, Bengaluru.10. Smt. Padmaja Jain, Subject Expert, Assistant Professor, BNMIT, Bengaluru.11. Smt. Prabhavathi P, Subject Expert,(Co-opted Member), Associate Professor , Dept. of ECE, BNMIT12. Smt. Anuradha J P, Subject Expert, (Co-opted Member), Assistant Professor, Dept. of ECE, BNMIT <p>Other Faculty Members Present for the presentation of the Syllabus: Dr. Bindu S, Dr. Jyothi R M, Dr. Rekha P, Dr. Bhuvana Suganthi D, Mrs. Ashwini Savanth and Mr. Chandrashekar C.</p>
Members Absent (Reasons)	<ol style="list-style-type: none">1. Mr. Santosh M. S, Post Graduate Meritorious Alumnus, System Validation Engineer, Intel Technologies India Pvt. Ltd, Bengaluru. (Not well and Health Issues)
Agenda	<ol style="list-style-type: none">1. Welcome Address2. Introduction3. Presentation of Scheme of Postgraduate 1st to 4th Semester4. Presentation of Syllabus5. Any Other Suggestions to the Department6. Concluding Remarks by Member Secretary

Proceedings of the meeting

The first BoS meeting for PG program in M. Tech VLSI Design & Embedded Systems, was held on 30/10/2021 in A-215, Auditorium Building Seminar Hall, BNMIT, Bengaluru. The meeting started at 10.30 am. Dr. P.A Vijaya, HoD of ECE, BoS Chairman (UG & PG) welcomed everyone to the meeting and introduced the external and internal BoS members. The meeting was conducted in blended mode. Dr. P. A. Vijaya, Chairperson, Dr. Vasundara Patel, VTU Nominee, Mr. Manjunath V, Industry Expert (External) and internal subject experts attended the meeting in-person whereas External Subject Experts - Dr. T. V Prabhakar, Dr. S. N. Prasad, Dr. Pushpa Mala, and the two internal BoS members Dr. Yasha Jyothi M Shirur and Mrs. Padmaja Jain joined the meeting through online. The chairperson presented Semester Wise Credit Distribution and the detailed Scheme for 1st semester to 4th semester M. Tech (VLSI Design & Embedded Systems) to the committee. The scheme is framed as per the VTU Regulations and AICTE Norms. Dr. S Y Kulkarni, Additional Director, BNMIT also attended the meeting in online and explained about the highlights of the Autonomous scheme for the PG program to all the members present.

The syllabi of the courses were presented by the respective faculty members and the highlights of the syllabus were mentioned. The members were informed about the integrated lab and mini-project component. The faculty members who presented the syllabus were: Dr. P. A. Vijaya, Dr. Yasha Jyothi M S, Dr. Bindu S, Dr. Jyothi R M, Dr. Subodh Kumar Panda, Dr. Rekha P, Dr. Bhuvana Suganthi D, Mrs. Prabhavathi P, Mrs. Vrunda K, Mrs. Ashwini Savanth, Mrs. Anuradha J P and Mr. Chandrashekar C.

The chair mentioned about the MOOC courses which encourage and initiate the self-learning capability of the students. The inclusion of the humanity courses and skill development courses to meet the NEP requirement were also discussed. All the BoS members gave the suggestions to make the curriculum better to meet the requirement of the industry and satisfy the societal needs. The member secretary noted down the suggestions given by the committee and provided the concluding remarks and thanked all the BoS members. Few photographs/snapshots of the BoS meeting have been attached herewith. The attendance sheet of the BoS meeting is also enclosed herewith.

Recommendations/Suggestions from the BOS Members

<p>Dr. Vasundara Patel, Professor, Dept of ECE, BMS College of Engineering, Bengaluru</p>	<p>Recommendations/Suggestions from Dr. Vasundara Patel, VTU Nominee:</p> <ol style="list-style-type: none"> 1. For CMOS Circuit Design (Integrated Lab), Lecture hours should be 3 hours in order to cover the syllabus. . 2. If possible, give only 1 credit to Research Methodology in first semester. 3. Include finFETS/New MOSFET and interconnect like carbon nanotube in Advances in VLSI Design course.
<p>Dr. T. V Prabhakar, Principal Scientist, Indian Institute of Science, Bengaluru</p>	<p>Recommendations/Suggestions from Dr. T. V Prabhakar , Subject Expert (External)</p> <ol style="list-style-type: none"> 1. Improve the Programming skills for students through assignments, workshop and SDP. 2. In project work, introduce niche concept 3. Try to incorporate RISC-5 Processor architecture in a relevant course 4. Include flexible electronics and TFT topics. 5. Include finFET and latest topics from Technical papers in the domain of VLSI. 6. Include In memory processor low power VLSI 7. Include Single electron transistors and graphene transistors in Nanotechnology

	8. Include Entrepreneurship and startup concepts in Project Management and IPR course
Dr. S. N. Prasad, Professor, School of ECE,Reva University, Bengaluru	Recommendations/Suggestions from Dr. S. N. Prasad , Subject Expert (External) 1. Appreciated the scheme and syllabus framed for the program 2. Appreciated the presentation and the conduction of the meeting
Dr. Pushpa Mala,Associate Professor, Department of ECE, Dayananda Sagar University, Bengaluru	Recommendations/ Suggestions from Dr. Pushpa Mala , Subject Expert (External) 1. Motivate the students to take up the IPR exams, which will be held only for 2 times a year. 2. Include content to verify how different delays can affect overall circuitry in Advanced Digital system Design using Verilog. 3. Include MESFETs and Memresistors in Advances in VLSI Design
Mr. Manjunath V, Staff Engineer, Synopsys India Pvt. Ltd., Bengaluru	Recommendations/Suggestions from Mr. Manjunath V, Industry Expert 1. Basic programming skills to be imparted to the students 2. Perl script programming to be taught to students through SDP or Workshops 3. Demo of synthesis circuit is required in Static Timing Analysis subject.
Concluding Remarks by Member Secretary, Dr. Subodh Kumar Panda	Noted the suggestions and recommendation given by PG BOS Committee Members. Mentioned that the suggestions will be incorporated in M. Tech (VLSI Design and Embedded Systems) autonomous curriculum to make it better to meet the requirements of the Industry and Societal needs. He thanked all the external and internal BoS members.

 Panda 2-11-2021

Member Secretary

Dr. Subodh Kumar Panda

 P. A. Vijaya 2-11-2021

HoD

Dr. P. A. Vijaya

Photographs / Snapshots during BoS Meeting



ECE - PG Autonomous BOS Meeting on 30-10-2021, 10:20am

03:27 Request control Leave

Recording has started. This meeting is being recorded. By joining, you are giving consent for this meeting to be recorded. [Priv...](#) Dismiss

Participants

Type a name

Share invite

In this meeting (7) Mute all

- Prabhakar T V External
- Dr.Yasha Jyothi M.Shirur
- Dr. Pushpa Mala S (Guest)
- Dr.S N Prasad (Guest)
- HOD Department of Electroni...
- S Y Kulkarni
- Smt. Padmaja Jain

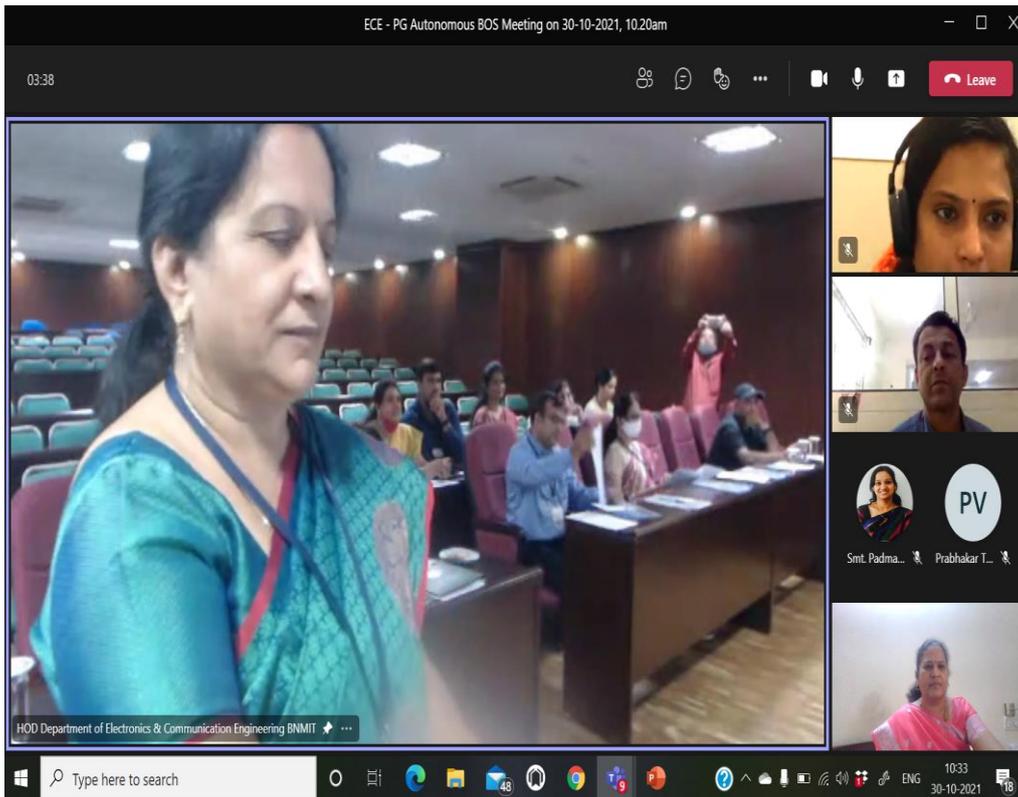
Others invited (10)

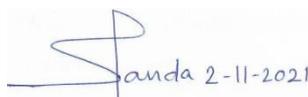
BoS of PG Cell of Electronics
Autonomous Institute under TTE
Department: Electronics & Communication Engineering
M.Tech (VLSI Design and Embedded Systems)
School of Technology and Entrepreneurship Innovations
(Effective from Session 2021-22)

Sl. No.	Course Code	Course Title	Practical	Project	Workshop	Internship	Entrepreneurship	Examination	Expenditure (2021)
			1	2	3	4	5	6	7
1	2219001	VLSI Circuit Design (Integrated Lab)	2	2	2	2	2	2	200
2	2219002	Smart Time Embedded System with R2709	2	2	2	2	2	2	200
3	2219003	Linux Server	2	2	2	2	2	2	200
4	2219004	Advanced Digital System Design using Verilog (Integrated Lab + Shop Project)	2	2	2	2	2	2	200
5	2219005	VLSI Course Lab (Shop Project) (Integrated Lab + Shop Project)	2	2	2	2	2	2	200
6	2219006	Introduction to VLSI Design	2	2	2	2	2	2	200
7	2219007	Power Electronics	2	2	2	2	2	2	200
8	2219008	Power of Things and its Application	2	2	2	2	2	2	200
9	2219009	Research Methodology	2	2	2	2	2	2	200
10	2219010	Workshop	2	2	2	2	2	2	200
TOTAL									2000

Discontinued course - Course with practical/ lab on completion during this year
 Elective
 Discontinued course - Course with practical/ lab on completion during this year for this Project

HOD Department of Electronics & Communication Engineering BNMIT




2-11-2021
Member Secretary
Dr. Subodh Kumar Panda


2-11-2021
HoD
Dr. P. A. Vijaya

B.N.M. Institute of Technology

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Department of Electronics and Communication Engineering

BOS Meeting Attendance Sheet

Meeting Details PG BOS Meeting – M. Tech (VLSI Design & Embedded Systems)
Date 30-10-2021
Time 10:30am
Venue A-215 (Auditorium Block Seminar Hall)

Members present

Sl. No	Name	Signature
Chairperson		
1.	Dr. P. A. Vijaya, Professor & Head, Dept. of ECE, BNMIT, Bengaluru	P. A. Vijaya
VTU Nominee		
2.	Dr. Vasundara Patel, Professor, Dept of ECE, BMS College of Engineering, Bengaluru	Vasundara K. S 30.10.2021
External BOS Members		
Academic Subject Experts		
3.	Dr. T. V Prabhakar, Principal Scientist, Indian Institute of Science Bangalore	Joined in Online
4.	Dr. S. N. Prasad, Professor, School of ECE,Reva University, Bengaluru	Joined in Online
5.	Dr. Pushpa Mala, Associate Professor, Department of ECE, School of Engineering, Dayananda Sagar University, Bangalore	Joined in Online
Industry Expert		
6.	Mr. Manjunath V, Staff Engineer, Synopsys India Pvt. Ltd., Bangalore.	20/10/21
Internal BoS Members		
7.	Dr. Subodh Kumar Panda, Member Secretary, Assoc. Professor, Dept. of ECE, BNMIT	Panda
8.	Dr. Yasha Jyothi M Shirur, Professor, Dept. of ECE, BNMIT	Joined in Online
9.	Smt. Vrunda Kusanur, Assistant Professor, Dept. of ECE, BNMIT	Vrunda
10.	Smt. Padmaja Jain, Assistant Professor, Dept. of ECE, BNMIT	Joined in Online
Co-opted Members from the department of ECE		
11.	Smt. Prabhavathi P, Associate Professor, Dept. of ECE, BNMIT	Prabhavathi
12.	Smt. Anuradha J P, Assistant Professor, Dept. of ECE, BNMIT	Anuradha
Post Graduate Alumnus		
13.	Mr. Santosh M. S. System Validation Engineer, Intel Technologies India Pvt. Ltd, Bangalore,	ABSENT

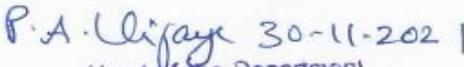
P. A. Vijaya 30-10-2021
HoD-ECE
BNMIT, Bengaluru
Head of the Department
Electronics & Communication Engineering
B.N.M. Institute of Technology
Bangalore - 560 070

Online Attendance for PG BoS Meeting

Meeting Summary

Total Number of Participants	10
Meeting Title	
Meeting Start Time	10/30/2021, 10:12:38 AM
Meeting End Time	10/30/2021, 2:13:38 PM
Meeting Id	e145d2b7-f4cc-4fa7-886a-8f18b16af260

Full Name	Join Time	Leave Time	Duration	Email	Role	Participant ID (UPN)
HOD Department of Electroni	10/30/2021, 10:12:38 AM	10/30/2021, 2:13:15 PM	4h	hodece@bnmit.in	Presenter	hodece@bnmit.in
Prabhakar T V	10/30/2021, 10:16:06 AM	10/30/2021, 1:29:39 PM	3h 13m	tvprabs@IISc.ac.in	Presenter	tvprabs@IISc.ac.in
Smt. Padmaja Jain	10/30/2021, 10:16:22 AM	10/30/2021, 11:22:58 AM	1h 6m	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 11:25:23 AM	10/30/2021, 11:25:30 AM	7s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 11:28:07 AM	10/30/2021, 11:28:52 AM	44s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 11:33:11 AM	10/30/2021, 11:34:37 AM	1m 25s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 11:38:24 AM	10/30/2021, 11:50:05 AM	11m 40s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 12:30:31 PM	10/30/2021, 12:38:06 PM	7m 34s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 12:39:32 PM	10/30/2021, 1:08:35 PM	29m 2s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 1:14:24 PM	10/30/2021, 1:41:06 PM	26m 42s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Smt. Padmaja Jain	10/30/2021, 1:57:22 PM	10/30/2021, 2:06:11 PM	8m 48s	padmajajain@bnmit.in	Presenter	padmajajain@bnmit.in
Dr.S N Prasad (Guest)	10/30/2021, 10:18:21 AM	10/30/2021, 12:58:41 PM	2h 40m		Presenter	
Dr. Pushpa Mala S (Guest)	10/30/2021, 10:27:09 AM	10/30/2021, 11:25:07 AM	57m 57s		Presenter	
Dr.Yasha Jyothi M.Shirur	10/30/2021, 10:29:26 AM	10/30/2021, 2:13:14 PM	3h 43m	yashajyothimshirur@bnmit.in	Presenter	yashajyothimshirur@bnmit.in
S Y Kulkarni	10/30/2021, 10:37:25 AM	10/30/2021, 11:26:31 AM	49m 5s	sykulkarni@bnmit.in	Presenter	sykulkarni@bnmit.in
Dr. Pushpa Mala S (Guest)	10/30/2021, 11:26:21 AM	10/30/2021, 2:12:20 PM	2h 45m		Presenter	
Dr. Subodh Kumar Panda	10/30/2021, 11:28:51 AM	10/30/2021, 11:29:23 AM	31s	subodhkumarpanda@bnmit.in	Organizer	subodhkumarpanda@bnmit.in
Dr. Subodh Kumar Panda	10/30/2021, 11:32:03 AM	10/30/2021, 12:00:45 PM	28m 42s	subodhkumarpanda@bnmit.in	Organizer	subodhkumarpanda@bnmit.in
Dr. Subodh Kumar Panda	10/30/2021, 2:13:28 PM	10/30/2021, 2:13:38 PM	9s	subodhkumarpanda@bnmit.in	Organizer	subodhkumarpanda@bnmit.in
Dr.S.N Prasad	10/30/2021, 12:58:26 PM	10/30/2021, 2:06:37 PM	1h 8m	prasadsn@reva.edu.in	Presenter	prasadsn@reva.edu.in


 Heard by the Department
 Electronics & Communication Engineering
BNM Institute of Technology
 Bangalore - 560 070