

## Department: Electronics and Communication Engineering

### Scheme of Teaching and Examination-Autonomous - (Effective from Academic year 2023-24) Batch:2022-2026

### Third Semester B.E (ECE)

Sl. No	Course Type	Course Code	Course Title	Teaching Dept	Teaching Hours /Week					Credits	Examination		
					L	T	P	J	Hours/Week		CIA Marks	SEA Marks	Total Marks
1	MAT	22MAC131	Fourier Series, Transforms, Numerical and Statistical Techniques	MAT	2	2	-	-	4	3	50	50	100
2	PCC	22ECE132	Network Analysis	ECE	2	2	-	-	4	3	50	50	100
3	PCI	22ECE133	Data structures using C	ECE	2	-	2	-	4	3	50	50	100
4	PCI	22ECE134	Analog Electronics Circuits	ECE	3	-	2	-	5	4	50	50	100
5	PCI	22ECE135	Digital System Design using Verilog	ECE	3	-	2	-	5	4	50	50	100
6	PBL	22ECE136	Python Programming on RaspberryPi	ECE	-	-	2	2	4	2	50	50	100
7	UHV	22CIP137	Constitution of India and Professional Ethics	HSS	1	-	-	-	1	1	100	-	100
8	AEC	22SFT138	Soft Skill -1	HSS	-	2	-	-	2	1	100	-	100
9	IPL	22ECE139	Innovative Project Lab -1	ECE	-	-	-	2	2	1	100	-	100
<b>TOTAL CONTACT HOURS</b>					<b>13</b>	<b>6</b>	<b>8</b>	<b>4</b>	<b>31</b>	<b>22</b>	<b>600</b>	<b>300</b>	<b>900</b>

**L-Theory lecture, T – Tutorial, P – Practical, J – Project NCMC- Non Credit Mandatory Course**  
**CIA: Continuous Internal Assessment, SEA: Semester End Assessment**

**Audit Course: All lateral entry students have to register and complete course on Additional Mathematics- II**

Sl. No	Course Type	Course Code	Course Title	Teaching Dept	Teaching Hours /Week					Credits	Examination		
					L	T	P	J	Hours/Week		CIA Marks	SEA Marks	Total Marks
10	MDP	22MATDIP1310	Bridge Mathematics - II		2	-	-	-	2	NCMC	100	-	100

**Credit definition:** 1 hour Lecture (L) per week = 1 Credit 2 hours Project Component (J) per week = 1 Credit

2 hours Practical /Drawing (P) per week = 1 Credit

(a) 4 Credit courses are to be designed for 50 hours Teaching – Learning process. (b) 3 Credit courses are to be designed for 40 hours Teaching – Learning process.

(c) 2 Credit courses are to be designed for 25 hours Teaching – Learning process (d) 1 Credit course are to be designed for 12-15 hours Teaching – Learning process

# B N M Institute of Technology

An Autonomous Institution under VTU. Approved by AICTE

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## Fourth Semester B.E (ECE)

Sl. No	Course Type	Course Code	Course Title	Teaching Dept	Teaching Hours /Week						Examination		
					L	T	P	J	Hours/Week	Credits	CIA Marks	SEA Marks	Total Marks
1	MAT	22MAC141	Complex Analysis, Probability and Random Process	MAT	2	2	-	-	4	3	50	50	100
2	PCC	22ECE142	Digital Signal Processing	ECE	3	2	-	-	5	4	50	50	100
3	PCI	22ECE143	Control Systems	ECE	1	2	2	-	3	3	50	50	100
4	PCI	22ECE144	ARM Microcontroller and its Applications	ECE	3	-	2	-	5	4	50	50	100
5	PCI	22ECE145	Analog and Digital Communication	ECE	3	-	2	-	5	4	50	50	100
6	PBL	22ECE146	Signal Processing Applications using MATLAB	ECE	-	-	2	2	4	2	50	50	100
7	AEC	22SFT147	Soft Skill -2	HSS	-	2	-	-	2	1	100	-	100
8	INT	22ECE148	Internship - I / Innovative Project Lab - 2	ECE	-	-	2	2	4	2	100	-	100
TOTAL CONTACT HOURS					12	8	10	4	32	23	500	300	800

**Audit Course:** All lateral entry students have to register and complete course on Additional Mathematics- II

Sl. No	Course Type	Course Code	Course Title	Teaching Dept	Teaching Hours /Week						Examination		
					L	T	P	J	Hours/Week	Credits	CIA Marks	SEA Marks	Total Marks
9	MDP	22MATDIP141	Bridge Mathematics - II		2	-	-	-	2	NCCM	100	-	100

**Summer Internship -I to be carried out during the vacation between II & III Semester OR III & IV Semester**

**Summer Internship - I (22ECE149):** All the students registered to II year of BE shall have to undergo mandatory internship of 4 weeks during II semester or III semester vacation. Semester End Assessment will be conducted in IV semester and the prescribed credit will be included. Internship shall be considered as a head of passing and shall be considered for the award of degree. Those, who do not take up / complete the internship shall be declared fail and shall have to complete during subsequent examination after satisfying the internship requirements. (The faculty coordinator or mentor has to monitor the students' internship progress and interact to guide them for the successful completion of the internship.)