

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU, Approved by AICTE

## Department of Mathematics

### Syllabus

| Semester: III   |  |                                |
|---|--|--------------------------------|
| Course: Fourier Series, Transforms, Numerical and Statistical Techniques  |  |                                |
| Course Code: 21MAC131 (Common to ECE, EEE & ME)   |  |                                |
| <b>L:T:P:J</b>  | <b>2:2:0:0</b>   | <b>CIA : 50</b>                |
| <b>Credits:</b>   | <b>03</b>  | <b>SEA : 50</b>                |
| <b>Hours:</b>   | <b>40</b>  | <b>SEA Duration : 03 Hours</b> |
| <b>Course Learning Objectives:</b> The students will be able to   |  |                                |
| 1   | Have an insight into Fourier series, Fourier transforms, Difference equations and Z-transforms.  |                                |
| 2   | Develop knowledge of solving ODE's arising in engineering applications, using numerical methods. |                                |
| 3   | Develop knowledge of Statistical methods and curve fitting arising in engineering.               |                                |
| Module-1: Fourier Series  |  | Blooms cognitive Levels        |
| Periodic functions, Introduction to Fourier Series, Dirichlet's condition. Fourier series of periodic functions with period $2\pi$ and arbitrary period. Half range Fourier sine and cosine series. Practical harmonic analysis over the interval $(0, 2l)$ .<br><b>Self-study:</b> Applications of Fourier series in Engineering.  |  | Apply                          |
| Module-2: Fourier Transforms & Z -Transforms  |  |                                |
| <b>Fourier Transforms:</b> Fourier transform and properties-problems, Fourier sine and cosine transforms. Inverse Fourier transforms.<br><b>Z-Transforms:</b> Introduction to Z-transform, Z-transform of standard functions and properties (without proof). Initial value and final value theorems, problems.<br><b>Self-study:</b> Applications of Fourier & Z-Transform in Engineering.  |  | Apply                          |
| Module-3: Numerical Solutions of Ordinary Differential Equations  |  |                                |
| Numerical solution of ordinary differential equations of first order - Taylor series method, Euler's method, Modified Euler's method, Runge-Kutta method of fourth order, Milne's predictor and corrector methods (without proof)<br>Numerical solution of second order ordinary differential equation using Runge-Kutta method of fourth order.<br><b>Self-study:</b> Solution of first order ordinary differential equation using Adam-Bashforth predictor and corrector methods. |  | Apply                          |
| Module-4: Statistical Methods   |  |                                |
| Introduction to Measures of Central tendency and Dispersion. Moments, Skewness, kurtosis and problems. Karl Pearson's coefficient of correlation and lines of regression. Rank correlation and problems<br><b>Self-study:</b> Problems on mean, median and mode.  |  | Apply                          |
| Module-5: Curve Fitting & Linear Programming  |  |                                |
| <b>Curve Fitting:</b> Curve fitting by the method of least squares- fitting the curves of the form: $y = ax + b$ , $y = ax^b$ and $y = ax^2 + bx + c$ .<br><b>Linear Programming problems (LPP):</b> General Linear programming problem, canonical and standard forms of LPP, Basic solution, Basic feasible solution, Optimal solution, Simplex method-problems.<br><b>Self-study:</b> Linear programming problems using graphical method.   |  | Apply                          |

**Course Outcomes:** After completing the course, the students will be able to

- CO 1: Demonstrate Fourier series to study the behavior of periodic functions and their applications in system communications, digital signal processing and field theory.
- CO 2: Make use of Fourier transform and Z-transform to illustrate discrete / continuous function arising in wave and heat propagation, signals and systems.
- CO 3: Solve first and second order ordinary differential equations arising in engineering problems using single step and multistep numerical methods.
- CO 4: Make use of correlation and regression analysis to fit a suitable mathematical model for the statistical data.
- CO 5: Fit a curve and solve linear programming problems by simplex method

**Reference Books:**

1. E. Kreyszig: "Advanced Engineering Mathematics", John Wiley & Sons, 10<sup>th</sup> Ed.(Reprint), 2016.
2. B.S. Grewal: "Higher Engineering Mathematics", Khanna Publishers, 44<sup>th</sup> Ed., 2017.
3. H. K. Dass, "Advanced Engineering Mathematics" S. Chand publication.
4. C.Ray Wylie, Louis C. Barrett : "Advanced Engineering Mathematics", 6<sup>th</sup> Edition, 2. McGraw-Hill Book Co., New York, 1995.
5. James Stewart : "Calculus —Early Transcendentals", Cengage Learning India Private Ltd., 2017.
6. B.V. Ramana: "Higher Engineering Mathematics" 11<sup>th</sup> Edition, Tata McGraw-Hill, 2010.
7. Srimanta Pal & Subobh C Bhunia: "Engineering Mathematics", Oxford University Press, 3 Reprint, 2016.
8. Gupta C. B., Singh S. R. and Mukesh Kumar: "Engineering Mathematics for Semester I & II", McGraw Hill Education (India) Pvt. Ltd., 2015.

**Web links and Video Lectures:**

1. <https://archive.nptel.ac.in/courses/111/106/111106111/>
2. <https://www.youtube.com/watch?v=SO7wRj9vXM8>
3. <https://nptel.ac.in/courses/111107107>
4. <https://www.youtube.com/watch?v=ir1U8sIog78>
5. <https://nptel.ac.in/courses/111105090>
6. <http://academicearth.org/>
7. <https://archive.nptel.ac.in/courses/111/104/111104027/>

| Dept. of Electronics and Communication Engineering<br>Choice Based Credit System (CBCS and Outcome Based Education (OBE))   |   |                        |                         |
|---|---|------------------------|-------------------------|
| Semester: III   |   |                        |                         |
| Course Name: Network Analysis and Control System  |   | Course Code:21ECE132   |                         |
| L: T: P: J  | 3: 2: 0 :0  | CIA Marks: 50          |                         |
| Credits:  | 4   | SEA Marks: 50          |                         |
| Hours/Week (Total)  | 5   | SEA Duration: 03 Hours |                         |
| Pre-Requisites: Basic Electrical, Mathematical Preliminaries  |   |                        |                         |
| Course Learning Objectives: The students will be able to  |   |                        |                         |
| 1   | Use the concepts of mesh analysis, nodal analysis and network theorems in analyzing the electrical circuits |                        |                         |
| 2   | Study two port network parameters like Z, Y, h and T and their inter-relationships and applications         |                        |                         |
| 3   | Obtain mathematical model of Electrical and Mechanical systems  |                        |                         |
| 4   | Find time response from Transfer Functions  |                        |                         |
| 5   | Determine the stability of system in Time and Frequency Domain  |                        |                         |
|   |   |                        |                         |
| Module-1: Basic Concepts and Network Theorems   |   | No. of Hours           | Blooms Cognitive Levels |
| Basic Concepts: Loop and node analysis with linearly dependent and independent sources for DC and AC networks.<br>Network Theorems: Network Analysis using Superposition, Thevenin’s and Norton’s theorems, Maximum Power transfer theorem, Millman’s Theorem Reciprocity Theorem. (All theorems for independent sources only)  |   | 10                     | Apply CO1               |
| Module–2: Two port Network parameters   |   |                        |                         |
| Two port network parameters:<br>Definition of Z, Y, h and Transmission parameters, modelling with these parameters, Network Analysis using of two port networks, Relationship between Parameters.<br>Laplace transform and its applications: Step, Ramp, Impulse functions, initial and final value theorem, solution of networks using Laplace transform, Interconnection of two ports, Laplace Transform. |   | 10                     | Apply CO2               |
| Module–3: Introduction to Control Systems and Transfer function   |   |                        |                         |
| Introduction to Control Systems: Types of Control Systems, Differential equation of Physical Systems, Mechanical Systems, Electrical Systems, Analogous Systems. Differential equation of electro-mechanical Systems.<br>Transfer function: Block diagram algebra, Signal Flow graph.   |   | 10                     | Apply CO3               |
| Module–4: Time Response and Stability Analysis  |   |                        |                         |
| Time response analysis: Standard test signals, Step response of first order, second order systems, Time response specification, steady state error and error constants.<br>Stability Analysis: Concept of stability, R H criterion, applications of R H criterion with limitations. Concepts for P, PD, PI and PID Controllers.   |   | 10                     | Apply CO4               |
| Module–5: Root Locus Technique and Frequency Domain Analysis  |   |                        |                         |
| Root locus technique: Introduction to root locus concepts, Construction rules, Analysis of stability by root locus plot<br>Frequency domain analysis: Correlation between frequency response and transient response. Bode and inverse bode plots.<br>Self-study component/Case study: Effect of addition of open loop poles and zeros on root locus and stability.  |   | 10                     | Apply CO5               |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE132.1</b>   | Apply the concepts of mesh analysis, node analysis, and network theorems to solve and analyse the electrical circuits.  |
| <b>21ECE132.2</b>   | Solve the given network using specified two port network parameters.  |
| <b>21ECE132.3</b>   | Develop the mathematical model of mechanical, electrical systems and transfer function for a given control system (block diagram and signal flow graph method). |
| <b>21ECE132.4</b>   | Determine the time domain specifications for first and second order systems and stability of a system in time domain using Routh-Hurwitz criterion.             |
| <b>21ECE132.5</b>   | Determine the stability of a system using Root locus and bode plots.  |
| <b>21ECE132.6</b>   | Explain the method of conserving energy using closed loop control system.   |

| <b>Text Books</b>  |  |
|--|--|
| 1. Network analysis, M.E. Van Valkenberg, Prentice Hall of India, 3rd edition, 2000, ISBN: 9780136110958.<br>2. Control Engineering, J. Nagrath & M. Gopal, New Age International Publishers/ 5 <sup>th</sup> edition/ 2005.   |  |
| <b>Reference Books</b>   |  |
| 1. Engineering Circuit Analysis, Hayt, Kemmerly and Durbin, TMH 7th Edition, 2010.<br>2. Networks and systems, Roy Choudhury, 2nd edition, New Age International Publications, 2006, ISBN: 9788122427677.<br>3. Automatic Control Systems, Benjamin C. Kuo, John Wiley India Pvt. Ltd./ 8 <sup>th</sup> Edition/ 2008. |  |

#### **Marks Distribution for Assessment:**

| <b>CIA (50)</b>                   | <b>Component</b> | <b>Description</b>   | <b>Marks</b> |
|-----------------------------------|------------------|--|--------------|
|                                   | Written Test     | <ul style="list-style-type: none"> <li>● Total Number of Test: 3</li> <li>● Each Theory test will be conducted for 30 marks</li> <li>● Average of 3 tests = 30 Marks</li> </ul>      | 30           |
|                                   | Assignment       | Average of 2 Assignments for 10 marks each   | 10           |
|                                   | AAT              | Quiz will be conducted on all the modules  | 10           |
| <b>Total Marks</b>                |                  |  | <b>50</b>    |
| <b>SEA (50)</b>                   | <b>Component</b> | <b>Description</b>   | <b>Marks</b> |
|                                   | Written Exam     | Theory exam will be conducted for 100 marks and scaled down to 50 Marks<br>The question paper will have 10 full questions each of 20 marks. Students have to answer 5 full questions | <b>50</b>    |
| <b>Total marks for the Course</b> |                  |  | <b>100</b>   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses.



| B.E. (Electronics and Communication Engineering)<br>Choice Based Credit System (CBCS and Outcome Based Education (OBE))  |  |                          |
|--|--|--------------------------|
| Semester: III  |  |                          |
| COURSE: Data Structures using C  |  | Course Code: 21ECE133    |
| L: T: P: J   | 2: 0: 2: 0   | CIE Marks: 50            |
| Credits:   | 3  | SEE Marks: 50            |
| Hours/Week (Total)   | 40 hours   | SEE Duration: 03 Hours   |
| Pre-Requisites: Basic C Programming knowledge  |  |                          |
| Course Learning Objectives: The students will be able to   |  |                          |
| 1  | Understand the role of data structures and time complexity analysis in algorithms.   |                          |
| 2  | Analyze the linear data structures arrays and linked lists with the operations performed.  |                          |
| 3  | Illustrate the concept of linear data structures stacks and queues with the operations performed.  |                          |
| 4  | Illustrate the working of non-linear tree data structure, operations performed and applications  |                          |
| 5  | Demonstrate the non-linear data structure – graphs and their applications along with sorting and searching algorithms. Also, apply the above data structures suitably to solve practical problems. |                          |
|  |  |                          |
| Module-1: Introduction to Data Structures & Algorithms   |  | No. of Hrs               |
| Introduction and Overview: Introduction, Basic Terminology, Elementary Data Organization, Data Structures, Data Structure Operations, Abstract Data Types (ADT).<br>Algorithms: Complexity, Time-Space Trade off, Algorithms Notation, Complexity of Algorithms and other asymptotic notations for complexity of algorithms.                                   |  | 8                        |
|  |  | Bloom's Cognitive Levels |
|  |  | Understand CO1           |
| Module-2: Linear Data Structures   |  |                          |
| Arrays: Introduction, Linear Arrays, Representation of Linear Arrays in memory, Traversing Linear Arrays, Inserting and Deleting, Sorting; Bubble Sort, Two dimensional Arrays.<br>Linked Lists: Introduction, linked lists, Representation of Linked lists in memory, traversing a linked list, searching linked list, memory allocation, garbage collection. |  | 8                        |
|  |  | Apply CO2                |
| Module-3: Linear Data Structures -Stacks & Queues  |  |                          |
| Stacks: Introduction, Stacks, Array representation of Stacks, linked representation of Stacks, Arithmetic expressions; Polish notations, Quick sort, an application of stacks.<br>Queues: Queues, linked representation of queues, dequeue   |  | 8                        |
|  |  | Apply CO3                |
| Module-4: Non-Linear Data Structures – Trees   |  |                          |
| Trees: Introduction, Binary trees, representing binary trees in memory, traversing binary trees, binary search trees, searching and inserting in binary search trees, deleting in a binary search tree, AVL search trees   |  | 8                        |
|  |  | Apply CO4                |
| Module-5: Graphs, Sorting & Searching  |  |                          |
| Graphs and their applications: Introduction, Graph theory Terminology, linked representation of a graph, operation on graphs, traversing of graphs (Breadth-First Search, Depth first search)  |  | 8                        |
|  |  | Apply CO5                |

|   |  |  |
|---|--|--|
| <b>Sorting &amp; Searching:</b> Introduction, sorting, insertion sort, selection sort, merge sort, searching and data modification, hashing (hash functions only) |  |  |
|---|--|--|

### List of Programs

**Using C compiler, demonstrate the concepts using following programs:**

|   |
|---|
| <ol style="list-style-type: none"> <li>1. Write a C program to Insert an element in an array and delete an element in the same array</li> <li>2. Write a C program to sort the array elements using selection sort</li> <li>3. Write a C program to sort the array elements using bubble sort</li> <li>4. Write a C program to create of 'n' nodes in singly linked list and display them</li> <li>5. Write a C program to insert a node at the beginning of linked list</li> <li>6. Write a C program to insert a node at the middle of linked list</li> <li>7. Write a C program to insert a node at the end of linked list</li> <li>8. Write a C program to delete a node in linked list</li> <li>9. Write a C program to create and display Doubly linked list in both direction</li> <li>10. Write a C program to implement the stack in array.</li> <li>11. Write a C program to implement stack using Linked list.</li> <li>12. Write a C program to Reverse String using STACK</li> <li>13. Write a C program to implement the queue in array</li> <li>14. Write a C program to search the number/node in a tree</li> <li>15. Write a C program to find the largest item in binary tree</li> <li>16. Write a C program to implement Graph</li> <li>17. Write a C program for Heap Sort</li> </ol> |
|---|

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE133.1</b>   | Gain knowledge on the importance of data structures, algorithms and time complexity computations.        |
| <b>21ECE133.2</b>   | Apply linear data structures to analyse and obtain solutions   |
| <b>21ECE133.3</b>   | Apply non-linear tree data structure to analyse and obtain solutions                                     |
| <b>21ECE133.4</b>   | Apply non-linear graph data structure to analyse and obtain solutions                                    |
| <b>21ECE133.5</b>   | Apply the concepts of sorting and searching to problem solving   |
| <b>21ECE133.6</b>   | Analyse real time practical problems and apply appropriate data structures to obtain efficient solutions |

| <b>Reference Books</b>  |
|---|
| <ol style="list-style-type: none"> <li>1. Seymour Lipschutz, "Data Structures", Tata McGraw Hill Education, Revised 1<sup>st</sup> Edition, 2008.</li> <li>2. Horowitz, Sahni &amp; S.Anderson-Freed, "Fundamentals of Data structures in C", University Press, Second edition, 2008.</li> <li>3. Thomas H. Cormen, C.E. Leiserson, R L.Rivest and C. Stein, Introduction to Algorithms , Third edition, MIT Press, 2009</li> <li>4. R.L. Kruse, B.P. Leary, C.L. Tondo, "Data structure and program design in C", PHI, 2009(Fourth Impression)</li> <li>5. Tannenbaum, "Data Structures", PHI, 2007(Fifth Impression)</li> <li>6. Jean Paul Tremblay, Paul G. Sorenson," An introduction to Data Structures with Applications, Second Edition, Tata McGraw-Hill,1991.</li> <li>7. Mark Allen Weiss, "Data Structures and Algorithm Analysis in C", Second Edition, Pearson Education, 1996.</li> </ol> |

**Marks Distribution for Assessment:**

| <b>CIA<br/>(50)</b>               | <b>Component</b>         | <b>Description</b>  | <b>Marks</b> |
|-----------------------------------|--------------------------|---|--------------|
|                                   | Written Test             | <ul style="list-style-type: none"><li>● Total Number of Test: 3</li><li>● Each Theory test will be conducted for 30 marks</li><li>● Average of 3 tests = 30 Marks</li></ul>               | 30           |
|                                   | Lab Assignment           | Lab records - 05 marks<br>Performance day wise – 05 Marks   | 10           |
|                                   | Laboratory Internal Test | Conduction – 05 Marks<br>Viva – 05 Marks  | 10           |
| <b>Total Marks</b>                |                          |   | <b>50</b>    |
| <b>SEA<br/>(50)</b>               | <b>Component</b>         | <b>Description</b>  | <b>Marks</b> |
|                                   | Laboratory Exam          | SEA to be conducted for 100 marks and scaled down to 50 Marks,<br>2 theory questions write-up - 20 Marks<br>Conduction - 50 Marks<br>Viva-Voce - 10 Marks<br>(One program to be executed) | <b>50</b>    |
| <b>Total marks for the Course</b> |                          |   | <b>100</b>   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

| Dept. of Electronics and Communication Engineering<br>Choice Based Credit System (CBCS and Outcome Based Education (OBE))   |   |                        |                         |
|---|---|------------------------|-------------------------|
| Semester: III   |   |                        |                         |
| Course Name: Analog Electronics Circuits  |   | Course Code: 21ECE134  |                         |
| L: T: P: J  | 3: 0: 2: 0  | CIA Marks: 50          |                         |
| Credits:  | 4   | SEA Marks: 50          |                         |
| Hours/Week (Total)  | 5 (50 hours)  | SEA Duration: 03 Hours |                         |
| Pre-Requisites: Physics and Electronics fundamentals  |   |                        |                         |
| Course Learning Objectives: The students will be able to  |   |                        |                         |
| 1   | Explain various BJT parameters, connections and configurations.,                |                        |                         |
| 2   | Design and demonstrate the transistor amplifiers.                               |                        |                         |
| 3   | Explain various types of FET biasing and demonstrate the use of FET amplifiers. |                        |                         |
| 4   | Analyze Power amplifier circuits in different modes of operation.               |                        |                         |
| 5   | Design op-amp for linear and non-linear applications                            |                        |                         |
|   |   |                        |                         |
| Module-1: BJT Biasing, Small signal operation and Modelling   |   | No. of Hours           | Blooms Cognitive Levels |
| Biasing in BJT amplifier circuits: The Classical Discrete circuit bias (Voltage-divider bias), Biasing using a collector to base feedback resistor.<br>Small signal operation and Models: Collector current and transconductance, Base current and input resistance, Emitter current and input resistance, voltage gain, The hybrid II model, and The T model. BJT current mirrors.   |   | 8                      | Apply CO1               |
| Module-2: : MOSFETs Biasing, Small signal operation and Modelling   |   |                        |                         |
| MOSFETs: Biasing in MOS amplifier circuits: Fixing VGS, Fixing VG, Drain to Gate feedback resistor.<br>Small signal operation and modelling: The DC bias point, signal current in drain, voltage gain, small signal equivalent circuit models, transconductance, The T equivalent circuit model, MOSFET differential amplifier.   |   | 8                      | Apply CO2               |
| Module-3: MOSFET Amplifier  |   |                        |                         |
| MOSFET Amplifier configuration: Basic configurations, characterizing amplifiers, CS amplifier with and without source resistance RS.<br>MOSFET internal capacitances and High frequency model: The gate capacitive effect, Junction capacitances, High frequency model.<br>Frequency response of the CS amplifier: The three frequency bands, high frequency response, Low frequency response. Fast Switching MOSFETs.  |   | 8                      | Apply CO3               |
| Module-4: Feedback Amplifier, Output Stages and Power Amplifiers  |   |                        |                         |
| Feedback Amplifier: General feedback structure, Properties of negative feedback, The Four Basic Feedback Topologies, The series-shunt, series-series, shunt-shunt and shunt-series amplifiers (Qualitative Analysis).<br>Output Stages and Power Amplifiers: Introduction, Classification of output stages, Class A output stage, Class B output stage: Transfer Characteristics, Power Dissipation, Power Conversion efficiency, Class AB output stage, Audio Power Amplifier Systems. |   | 8                      | Apply CO4               |
| Module-5: Op-Amp Circuits, 555 Timer and its applications   |   |                        |                         |
| Instrumentation Amplifier, DAC - Weighted resistor and R-2R ladder, ADC-Successive approximation type, Small Signal half wave rectifier, Active Filters, First order low-pass and high-pass Butterworth filters, Band-pass filters, Band reject filters.<br>555 Timer and its applications: Monostable and Astable Multivibrators. Comparator & Schmitt Trigger, Wien Bridge Oscillators using Opamp.   |   | 8                      | Apply CO5               |

| <b>Lab Experiments (Lab sessions + 1 Lab Test)</b> |   |
|--|---|
| Sl. No.  | Experiments   |
| 1  | Design and set up the BJT common emitter voltage amplifier without feedback and determine the gain- bandwidth product, input and output impedances. |
| 2  | Design and set up the FET common source voltage amplifier without feedback and determine the gain- bandwidth product, input and output impedances.  |
| 3  | Experiment to determine the Power efficiency of class C amplifier   |
| 4  | Design and set up the circuits using Opamp: i) Integrator, ii) Differentiator   |
| 5  | Design of Op- Amp as comparator circuit   |
| 6  | R-2R DAC  |
| 7  | Simulation Experiment: Narrow Band-pass Filter  |
| 8  | Simulation Experiment: Active second order Butterworth low pass and high pass filters   |
| 9  | Simulation Experiment: Monostable & Astable Multivibrator using 555 Timer   |
| 10   | Simulation Experiment: Narrow band-reject filter  |

**Course Outcomes: After completing the course, the students will be able to**

|                   |   |
|-------------------|---|
| <b>21ECE134.1</b> | Understand the characteristics of BJTs for switching and amplifier circuits.                          |
| <b>21ECE134.2</b> | Understand the characteristics of FETs for switching and amplifier circuits.                          |
| <b>21ECE134.3</b> | Design and analyze FET amplifiers with different circuit configurations and biasing conditions.       |
| <b>21ECE134.4</b> | Understand the feedback topologies and approximations in the design of amplifiers                     |
| <b>21ECE134.5</b> | Design of circuits using linear ICs for wide range applications such as ADC, DAC, filters and timers. |
| <b>21ECE134.6</b> | Design real life application based on discrete Analog and linear IC circuits                          |

**Reference Books**

1. Microelectronic Circuits, Theory and Applications, Adel S Sedra, Kenneth C Smith, 6th Edition, Oxford, 2015. ISBN:978-0-19-808913-1
2. Op-Amps and Linear Integrated Circuits, Ramakant A Gayakwad, 4th Edition, Pearson Education, 2018. ISBN: 978-93-325-4991-3.
3. Integrated Electronics: Analog and Digital Circuits and Systems, Jacob Millman, Christos C. Halkias, McGraw-Hill, 2015.
4. Electronic Devices and Circuit, Boylestad & Nashelsky, Eleventh Edition, Pearson, January 2015.
5. Electronic Principles, Albert Malvino, David J Bates, 7th Edition, McGraw Hill Education (India) Private Limited, 2017, ISBN:978-0-07-063424-4.

**Marks Distribution for Assessment:**

| <b>CIA (50)</b>                   | <b>Component</b>     | <b>Description</b>   | <b>Marks</b> |
|-----------------------------------|----------------------|--|--------------|
|                                   | Written Test         | <ul style="list-style-type: none"> <li>● Total Number of Test: 3</li> <li>● Each Theory test will be conducted for 30 marks</li> <li>● Average of 3 tests = 30 Marks</li> </ul>      | 30           |
|                                   | <b>Lab Component</b> | Observation + Record=10 Marks<br>Lab Internal Assessment=10 Marks  | 20           |
| <b>Total Marks</b>                |                      |  | <b>50</b>    |
| <b>SEA (50)</b>                   | <b>Component</b>     | <b>Description</b>   | <b>Marks</b> |
|                                   | Written Exam         | Theory exam will be conducted for 100 marks and scaled down to 50 Marks<br>The question paper will have 10 full questions each of 20 marks. Students have to answer 5 full questions | <b>50</b>    |
| <b>Total marks for the Course</b> |                      |  | <b>100</b>   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses.

| Dept. of Electronics and Communication Engineering   |   |                         |
|--|---|-------------------------|
| Choice Based Credit System (CBCS and Outcome Based Education (OBE))  |   |                         |
| Semester: III  |   |                         |
| Course Name: Digital System Design Using Verilog   |   | Course Code: 21ECE135   |
| L: T: P: J   | 3: 0: 2: 0  | CIA Marks: 50           |
| Credits:   | 4   | SEA Marks: 50           |
| Hours/Week (Total)   | 5 / (50 Hours)  | SEA Duration: 03 Hours  |
| Pre-Requisites: Digital Circuits   |   |                         |
| Course Learning Objectives: The students will be able to   |   |                         |
| 1  | Simplifying Boolean expression using K-map techniques and Quine-McCluskey minimization techniques |                         |
| 2  | Designing and analyzing combinational logic circuits.   |                         |
| 3  | Design methods and analysis of sequential logic circuits  |                         |
| 4  | Design of digital systems using Verilog HDL-data flow models.                                     |                         |
| 5  | Design of digital systems using Verilog HDL behavioral and structural models.                     |                         |
|  |   |                         |
| Module-1: Principles of Combinational Logic  |   | No. of Hours            |
| Definition of combinational logic, Canonical forms, Generation of switching equations from truth tables, Karnaugh maps- up to 4 variables, Karnaugh maps using Don't care, Simplifying Maxterm equation up to 4 variables. Quine-McCluskey Minimization Technique. Quine-McCluskey using Don't Care Terms.   |   | 8                       |
|  |   | Blooms Cognitive Levels |
| Module-2: Logic Design with MSI Components and Programmable Logic Devices  |   |                         |
| Binary Adders and Subtractors, Comparators, Decoders, Encoders, Multiplexers.  |   | 8                       |
|  |   | Apply CO2               |
| Module-3: Flip-Flops and its Applications  |   |                         |
| Latches, SR Latch, S'R'Latch, Gated SR latch, Gated D Latch, Timing Considerations (Propagation delay, Minimum pulse width, Setup and Hold Times), The Master-Slave Flip-flops (PulseTriggered flip-flops): SR flip-flops, JK flip flops, edge triggered flip flops, Characteristic equations, Registers, Binary Ripple Counters, Synchronous Binary Counters, Design of Synchronous mod-n Counter using clocked JK and D flip-flops |   | 8                       |
|  |   | Apply CO3               |
| Module-4: Introduction to Verilog and Verilog Data flow description  |   |                         |
| Structure of Verilog module, Operators, Data Types, Styles of Description. Highlights of Data flow description, Structure of Data flow description.  |   | 8                       |
|  |   | Apply CO4               |
| Module-5: Verilog Behavioral and Structural description  |   |                         |
| Structure, Variable Assignment Statement, Sequential Statements, Loop Statements, Verilog Behavioral Description of Multiplexers Highlights of Structural description, Organization of structural description, Structural description of ripple carry adder  |   | 8                       |
|  |   | Apply CO5               |

### Laboratory Experiments

Using suitable simulation software, demonstrate the operation of the following circuits:

| <b>SL. No.</b> | <b>Programs</b>   |
|----------------|---|
| <b>1.</b>      | Simplify the given 3/4 variable Boolean expressions. and simulate the design using Verilog dataflow description.        |
| <b>2.</b>      | Design a Full Adder using two half adders and simulate using Verilog structural flow Description.                       |
| <b>3.</b>      | Realize 32-bit ALU using Verilog Behavioral description.  |
| <b>4.</b>      | Realize using Verilog Behavioral description: 8:1 mux, 8:3 Priority encoder.  |
| <b>5.</b>      | Realize using Verilog Behavioral description: 3:8 decoder, 2-bit Comparator.  |
| <b>6.</b>      | Realize using Verilog Behavioral description: Flip-flops: a) JK b) SR c) T d) D and verify the design using FPGA board. |

| SL. No. | Programs   |
|---------|--|
| 7.      | Design 4 bit Binary and BCD counters with synchronous and asynchronous reset using Verilog Behavioral description and verify the design using FPGA board |
| 8.      | Design 8-bit shift register for shift left and right operation using Verilog Behavioral Description.   |
| 9.      | Develop a Verilog Program to interface a Stepper motor to the FPGA and rotate the motor in the specified direction.                                      |
| 10.     | Interface DAC to generate square and triangular waveform using Verilog program and implement into the FPGA board.  |

**Course Outcomes: After completing the course, the students will be able to**

|            |  |
|------------|--|
| 21ECE135.1 | Simplify Boolean functions using K-map and Quine-McCluskey minimization technique.                     |
| 21ECE135.2 | Analyze and design for combinational logic circuits.   |
| 21ECE135.3 | Analyze the concepts of Flip Flops (SR, D, T and JK) and to design the synchronous sequential circuits |
| 21ECE135.4 | Design of combinational and sequential circuits using Verilog dataflow descriptions.                   |
| 21ECE135.5 | Design of combinational and sequential circuits using Verilog behavioral and structural descriptions.  |
| 21ECE135.6 | Design the applications of combinational and sequential circuits                                       |

**Reference Books**

1. Digital Logic Applications and Design, John M Yarbrough, Thomson Learning, 2001.
2. Digital Principles and Design, Donald D Givone, McGraw Hill, 2002.
3. HDL Programming VHDL and Verilog, Nazeih M Botros, press, 2009.
4. Fundamentals of logic design, Charles H Roth Jr., Cengage Learning.
5. Verilog HDL-a guide to digital design and synthesis, Sameer Palnitkar 2nd edition, Pearson Edition 2003.

**Marks Distribution for Assessment:**

| CIA (50)                          | Component    | Description  | Marks      |
|-----------------------------------|--------------|--|------------|
|                                   | Written Test | <ul style="list-style-type: none"> <li>● Total Number of Test: 3</li> <li>● Each Theory test will be conducted for 30 marks</li> <li>● Average of 3 tests = 30 Marks</li> </ul>      | 30         |
|                                   | Lab Exam     | Observation + Record=10 Marks<br>Lab Internal Assessment=10 Marks  | 20         |
| <b>Total Marks</b>                |              |  | <b>50</b>  |
| SEA (50)                          | Component    | Description  | Marks      |
|                                   | Written Exam | Theory exam will be conducted for 100 marks and scaled down to 50 Marks<br>The question paper will have 10 full questions each of 20 marks. Students have to answer 5 full questions | 50         |
| <b>Total marks for the Course</b> |              |  | <b>100</b> |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses.

| Dept. of Electronics and Communication Engineering   |   |                        |
|--|---|------------------------|
| Choice Based Credit System (CBCS and Outcome Based Education (OBE))  |   |                        |
| Semester: III  |   |                        |
| Course Name: Python Programming on Raspberry PI  |   | Course Code: 21ECE136  |
| L: T: P: J   | 0 : 0 : 2 : 2   | CIA Marks: 50          |
| Credits:   | 2   | SEA Marks: 50          |
| Hours/Week (Total)   | 4 / 25  | SEA Duration: 03 Hours |
| Pre-Requisites: Basics of C and C++ language, Students should be familiarized about Python installation and setting Python environment |   |                        |
| Course Learning Objectives: The students will be able to   |   |                        |
| 1  | Learn syntax and semantics in Python  |                        |
| 2  | Handle Strings, Files, Functions in Python  |                        |
| 3  | Understand Lists and Dictionaries in Python   |                        |
| 4  | Understand interface of Sensors with Raspberry Pi   |                        |
| 5  | Learn interface of display devices with Raspberry Pi  |                        |
| Part A- Python Programs  |   |                        |
| Sl. No.  | List of Programs (To be Covered in 5 lab sessions)  |                        |
| 1.   | Aim: Introduce the Python fundamentals, data types, operators, flow control and exception handling in Python.<br>a) Write a python program to find the best of two test average marks out of three test's marks accepted from the user<br>b) Develop a Python program to check whether a given number is palindrome or not and also count the number of occurrences of each digit in the input number |                        |
| 2.   | Aim: Demonstrating creation of functions, passing parameters and return values.<br>Develop a python program to perform the following code conversions using functions.<br>a) Binary to Decimal<br>b) Octal to Hexadecimal   |                        |
| 3.   | Aim: Demonstration of manipulation of strings using string methods.<br>Write a Python program that accepts a sentence and find the following.<br>a) Number of words and digits<br>b) Number of uppercase letters and lowercase letters  |                        |
| 4.   | Aim: Discuss different collections like list and dictionary.<br>a) Write a python program to implement insertion sort and merge sort using lists<br>b) Write a program to convert roman numbers in to integer values using dictionaries   |                        |
| 5.   | Aim: Demonstration of reading, writing and organizing files.<br>Write a python program to accept a file name from the user and perform the following operations.<br>a) Display the first N line of the file<br>b) Find the frequency of occurrence of the word accepted from the user in the file   |                        |
| Part B- Python Programs on Raspberry PI  |   |                        |
| Sl. No.  | List of Programs (To be Covered in 5 lab sessions)  |                        |
| 6.   | Aim: Demonstrate the interfacing of IR/PIR sensors to Raspberry Pi.<br>Write a Python program to interface IR/PIR motion sensor to Raspberry Pi.  |                        |
| 7.   | Aim: Demonstrate the interfacing of output device to Raspberry Pi.<br>Write a Python program to interface LED to Raspberry Pi.  |                        |
| 8.   | Aim: Demonstrate the interfacing of Seven Segment Display device to Raspberry Pi.<br>Write a Python program to interface Seven Segment Display to Raspberry Pi.   |                        |
| 9.   | Aim: Demonstrate the interfacing of ultrasonic sensor to Raspberry Pi.<br>Write a Python program to interface ultrasonic to Raspberry Pi.   |                        |
| 10.  | Aim: Demonstrate the interfacing of Temperature Humidity sensor to Raspberry Pi.<br>Write a Python program to interface DHT11 sensor to Raspberry Pi.   |                        |



| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE136.1</b>   | Examine syntax and semantics using flow control in Python                          |
| <b>21ECE136.2</b>   | Demonstrate proficiency in handling strings and file systems                       |
| <b>21ECE136.3</b>   | Write, Execute and Manipulate the data structures like lists and dictionaries      |
| <b>21ECE136.4</b>   | Implement Python program to interface sensors with Raspberry Pi                    |
| <b>21ECE136.5</b>   | Apply Python programming techniques to interface display devices with Raspberry Pi |
| <b>21ECE136.6</b>   | Develop a Project using Python concepts  |

| <b>Text Books</b>  |  |
|--|--|
| <ol style="list-style-type: none"> <li>1. Al Sweigart, “Automate the Boring Stuff with Python”, 1<sup>st</sup> Edition, No Starch Press, 2015. (Available under CC-BY-NC-SA license at <a href="https://automatetheboringstuff.com/">https://automatetheboringstuff.com/</a>)</li> <li>2. Reema Thareja “Python Programming Using Problem Solving Approach” Oxford University Press.</li> <li>3. Allen B. Downey, “Think Python: How to Think Like a Computer Scientist”, 2nd Edition, Green Tea Press, 2015. (Available under CC-BY-NC license at <a href="http://greenteapress.com/thinkpython2/thinkpython2.pdf">http://greenteapress.com/thinkpython2/thinkpython2.pdf</a>)</li> </ol> |  |
| <b>AAT: ONLINE COURSES/VIDEO LECTURES</b><br><a href="https://www.coursera.org/learn/raspberry-pi-platform">https://www.coursera.org/learn/raspberry-pi-platform</a><br><a href="https://nptel.ac.in/courses/106106145">https://nptel.ac.in/courses/106106145</a>  |  |

#### **Mark Distribution for Assessment:**

| <b>CIA<br/>(50)</b>               | <b>Component</b>                | <b>Description</b>  | <b>Marks</b>   |
|-----------------------------------|---------------------------------|---|----------------|
|                                   | <b>Practical</b>                | <ul style="list-style-type: none"> <li>● Lab records</li> <li>● Performance day wise</li> </ul>   | 5<br>5         |
|                                   | <b>Internal Laboratory Test</b> | <ul style="list-style-type: none"> <li>● Conduction</li> <li>● Viva</li> </ul>  | 5<br>5         |
|                                   | <b>Project</b>                  | <ul style="list-style-type: none"> <li>● Demonstration</li> <li>● Presentation</li> <li>● Report</li> </ul>   | 10<br>10<br>10 |
| <b>Total Marks</b>                |                                 |   | <b>50</b>      |
| <b>SEA<br/>(50)</b>               | <b>Component</b>                | <b>Description</b>  | <b>Marks</b>   |
|                                   | <b>External Laboratory Exam</b> | External Lab exam will be conducted for 100 marks and scaled down to 50 Marks. The marks allocated is as follows:<br>Write up – 20<br>Conduction – 70<br>Viva-voce – 10 | <b>50</b>      |
| <b>Total marks for the Course</b> |                                 |   | <b>100</b>     |

# B.N.M. Institute of Technology

An Autonomous Institution under VTU

| Semester: III/IV  |  |                      |
|---|--|----------------------|
| COURSE: ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ<br>(ಕನ್ನಡ ಬಲ್ಲ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ)  |  |                      |
| Course Code: 21KAN1371  | L: T: P: J: 1:0:0:0  | CIA Marks: 50        |
| Credits:  | 1  | SEA Marks: 50        |
| Hours:  | 15 hrs   | SEE Duration: 1.5hrs |
| Course Learning Objectives: The students will be able to  |  |                      |
| 1   | ಪದವಿ ವಿದ್ಯಾರ್ಥಿಗಳಾಗಿರುವುದರಿಂದ ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡದ ಜೊತೆಗೆ ಕ್ರಿಯಾತ್ಮಕ ಕನ್ನಡವನ್ನು, ಕನ್ನಡ ಸಾಹಿತ್ಯ, ಸಂಸ್ಕೃತಿ ಮತ್ತು ನಾಡು ನುಡಿಯ ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು.                                       |                      |
| 2   | ಆಧುನಿಕ ಪೂರ್ವ ನಡುಗನ್ನಡ ಸಾಹಿತ್ಯದ ಪ್ರಮುಖ ಸಾಹಿತ್ಯ ಪ್ರಕಾರಗಳಾದ ವಚನ ಸಾಹಿತ್ಯ ಮತ್ತು ಕೀರ್ತನ ಸಾಹಿತ್ಯ ಇವೆರಡರ ಮಹತ್ವವನ್ನು ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು.  |                      |
| 3   | ಆಧುನಿಕ ಕಾವ್ಯಭಾಗದಲ್ಲಿ ಅನೇಕ ಪ್ರಮುಖ ಕವಿಗಳು ಇದ್ದರಾದರೂ ಇಲ್ಲಿ ಸಾಂಕೇತಿಕವಾಗಿ ಈ ನಾಲ್ಕು ಕವಿಗಳ ಸಮಕಾಲೀನ ಪ್ರಜ್ಞೆಯಿಂದ ಕೂಡಿದ ಕವನಗಳನ್ನು ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು.                                      |                      |
| 4   | ಕನ್ನಡದ ತಾಂತ್ರಿಕ ವಿಜ್ಞಾನ ಕ್ಷೇತ್ರದ ಅಸ್ತಿಭಾರ ಹಾಕಿದ ಸರ್ ಎಂ ವಿಶ್ವೇಶ್ವರಯ್ಯನವರ ಬಗ್ಗೆ ಜನಮಾನಸದಲ್ಲಿರುವ ನೆನಪಿನ ಪ್ರಸಂಗಗಳನ್ನು ಇಟ್ಟುಕೊಂಡು ಕರ್ನಾಟಕಕ್ಕೆ ಅವರು ಮಾಡಿದ ಸೇವೆಯನ್ನು ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು. |                      |
| 5   | ಕನ್ನಡ ಭಾಷಾಭ್ಯಾಸ,ಸಾಮಾನ್ಯ ಕನ್ನಡ ಹಾಗೂ ಆಡಳಿತ ಕನ್ನಡದ ಪದಗಳ ಪರಿಚಯ ಮಾಡಿಕೊಡುವುದು  |                      |
| Module 1 – ಕನ್ನಡ ನಾಡು ನುಡಿ ಮತ್ತು ಸಂಸ್ಕೃತಿಗೆ ಸಂಬಂಧಿಸಿದ ಲೇಖನಗಳು   |  | RBT                  |
| ಕರ್ನಾಟಕ ಸಂಸ್ಕೃತಿ - ಹಂಪಿನಾಗರಾಜಯ್ಯ<br>ಕರ್ನಾಟಕದ ಏಕೀಕರಣ, ಒಂದು ಅಪೂರ್ವ ಚರಿತ್ರೆ - ಪ್ರೊ ಜಿ ವೆಂಕಟಸುಬ್ಬಯ್ಯ<br>ಆಡಳಿತ ಭಾಷೆಯಾಗಿ ಕನ್ನಡ - ಡಾ  ಎಲ್ ತಿಮ್ಮೇಶ ಮತ್ತು ಪ್ರೊ.ವಿ ಕೇಶವಮೂರ್ತಿ |  | 1,2,3                |
| Hrs   |  | 3                    |
| Module 2 – ಕಾವ್ಯಭಾಗ (ಆಧುನಿಕ ಪೂರ್ವ)  |  | RBT                  |
| ವಚನಗಳು: ಜೇಡರ ದಾಸಿಮಯ್ಯ, ಅಲ್ಲಮಪ್ರಭು, ಬಸವಣ್ಣ, ಅಕ್ಕಮಹಾದೇವಿ,<br>ಆಯ್ದಕ್ಕಿ ಲಕ್ಕಮ್ಮ, ಆಯ್ದಕ್ಕಿ ಮಾರಯ್ಯ<br>ಕೀರ್ತನೆಗಳು : ಪುರಂದರದಾಸ, ಕನಕದಾಸ                                      |  | 1,2,3                |
| Hrs   |  | 3                    |
| Module 3 – ಕಾವ್ಯಭಾಗ (ಆಧುನಿಕ)  |  | RBT                  |
| ಮಂಕುತಿಮ್ಮನ ಕಗ್ಗ : ಡಿ.ವಿ.ಜಿ.<br>ಕುರುಡು ಕಾಂಚಣಾ : ದ.ರಾ. ಬೇಂದ್ರೆ<br>ಹೊಸ ಬಾಳಿನ ಗೀತೆ : ಕುವೆಂಪು<br>ಜೋಮನ ಮಕ್ಕಳ ಹಾಡು : ಸಿದ್ದಲಿಂಗಯ್ಯ  |  | 1,2,3                |
| Hrs   |  | 3                    |
| Module 4 – ತಾಂತ್ರಿಕ ವ್ಯಕ್ತಿ ಪರಿಚಯ   |  | RBT                  |
| ಕಥೆ ಮತ್ತು ಪ್ರವಾಸ ಕಥನ - ಸರ್ ಎಂ ವಿಶ್ವೇಶ್ವರಯ್ಯ - ವ್ಯಕ್ತಿ ಮತ್ತು ಐತಿಹ್ಯ - ಎ ಎನ್ ಮೂರ್ತಿರಾವ್   |  | 1,2,3                |
| Hrs   |  | 3                    |
| Module 5 – ತತ್ವಜ್ಞಾನ ಮತ್ತು ತಂತ್ರಜ್ಞಾನ   |  | RBT                  |
| ಭಗವದ್ಗೀತೆಯ ಸಾರ, ಭಗವದ್ಗೀತೆಯಲ್ಲಿ ಬರುವ ಗುರು ಶಿಷ್ಯ ಸಂಬಂಧ.<br>ತಾಂತ್ರಿಕ ಪದಕೋಶ - ತಾಂತ್ರಿಕ ಹಾಗೂ ಪಾರಿಭಾಷಿಕ ಕನ್ನಡ ಪದಗಳು   |  | 1,2,3                |
| Hrs   |  | 3                    |

## Reference Books

1. "ಸಾಂಸ್ಕೃತಿಕ ಕನ್ನಡ" ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯದ ಕನ್ನಡ ಮಾತೃಭಾಷೆಯ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ನಿಗದಿಪಡಿಸಿದ ಪಠ್ಯಪುಸ್ತಕ  
ಪ್ರಧಾನ ಸಂಪಾದಕರು - ಕುಲಪತಿಗಳು ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ ಬೆಳಗಾವಿ ಸಂಪಾದಕರು -  
ಡಾ|| ಹಿ. ಚಿ. ಬೋರಲಿಂಗಯ್ಯ, ಕುಲಪತಿಗಳು ಕನ್ನಡ ವಿಶ್ವವಿದ್ಯಾಲಯ ಹಂಪಿ  
ಡಾ|| ಎಲ್ ತಿಮ್ಮೇಶ, ಪ್ರಾಧ್ಯಾಪಕರು ಸರ್ಕಾರಿ ಇಂಜಿನಿಯರಿಂಗ್ ಕಾಲೇಜ್, ಹಾಸನ

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

| Semester: III/IV  |   |                      |
|---|---|----------------------|
| COURSE: Balake Kannada (For Non-Karnataka students)   |   |                      |
| Course Code: 21KAN1372  | L:T:P:J: 1:0:0:0  | CIE Marks: 50        |
| Credits:  | 1   | SEE Marks: 50        |
| Hours:  | 15 hrs  | SEE Duration: 1.5Hrs |
| <b>Course Learning Objectives: The students will be able to</b>   |   |                      |
| 1   | The course will enable the Non-Karnataka students to understand speak read and write Kannada language and communicate or Converse in Kannada language in their daily life with Kannada speakers |                      |
| <b>Module 1 – SPOKEN KANNADA</b>  |   | <b>RBT</b>           |
|   |   | <b>Hrs</b>           |
| i. Interaction in Hostel / College.<br>ii. Conversation in a Bus.<br>iii. Conversation between friends.<br>iv. Conversation with Teachers.<br>v. Telephonic Conversation.<br>vi. Conversation with shopkeeper.<br>vii. Conversation with Auto and Cab Driver. |   | 1,2,3<br>5           |
| <b>Module 2 – READ AND WRITE</b>  |   | <b>RBT</b>           |
|   |   | <b>Hrs</b>           |
| Vowels, Initial forms & Secondary forms Yogavahas<br>Classified consonants, Un-classified consonants.   |   | 1,2,3<br>4           |
| <b>Module 3 – HISTORY OF KARNATAKA</b>  |   | <b>RBT</b>           |
|   |   | <b>Hrs</b>           |
| Royal Dynasties of Karnataka  |   | 1,2,3<br>2           |
| <b>Module 4 – LITERATURE AND TOURIST PLACES OF KARNATAKA</b>  |   | <b>RBT</b>           |
|   |   | <b>Hrs</b>           |
| The Birds view of Kannada Literature<br>Karnataka's Tourist Paradise  |   | 1,2,3<br>2           |
| <b>Module 5 – KANNADA LANGUAGE</b>  |   | <b>RBT</b>           |
|   |   | <b>Hrs</b>           |
| History of Kannada Language   |   | 1,2,3<br>2           |

## Reference Books

1. “ಬಳಕೆ ಕನ್ನಡ” ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯದ ಕನ್ನಡ ಮಾತೃಭಾಷೆ ಅಲ್ಲದ ವಿದ್ಯಾರ್ಥಿಗಳಿಗೆ ನಿಗದಿಪಡಿಸಿದ ಪಠ್ಯಪುಸ್ತಕ.  
ಪ್ರಧಾನ ಸಂಪಾದಕರು - ಕುಲಪತಿಗಳು ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ ಬೆಳಗಾವಿ ಸಂಪಾದಕರು –  
ಡಾ|| ಎಲ್ ತಿಮ್ಮೇಶ, ಪ್ರಾಧ್ಯಾಪಕರು ಸರ್ಕಾರಿ ಇಂಜಿನಿಯರಿಂಗ್ ಕಾಲೇಜ್, ಹಾಸನ

### **Class Internal Assessment**

|              |                  |   |
|--------------|------------------|---|
| IA1          | 30 Marks         | Average of 2 IA will be taken<br>30 Marks |
| IA2          | 30 Marks         |   |
| Assignment 1 | 10 Marks         | 10 Marks                                  |
| Assignment 2 | 10 Marks         | 10 Marks                                  |
|              | <b>Total CIA</b> | <b>50 Marks</b>                           |

### **Semester End Assessment**

|                   |                          |                 |
|-------------------|--------------------------|-----------------|
| Semester end Exam | Objective Type Questions | 50 Marks        |
|                   | <b>Total SEA</b>         | <b>50 Marks</b> |

**Final Marks = CIA + SEA = 50+50 = 100 Marks**

#### **Faculties:**

1. Sri. Chandrashekar
2. Dr. Chandravathi

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

| Semester: III   |   |                      |
|---|---|----------------------|
| COURSE: Soft Skill-1                                  |   |                      |
| Course Code: 21SFT138                                 | L:T:P:J: 0:0:2:0  | CIA Marks: 50        |
| Credits:  | 1   | SEA Marks: 50        |
| Hours:  | 15 hrs  | SEE Duration: 1.5Hrs |
| Course Learning Objectives: The students will be able |   |                      |
| 1   | To help students understand their strengths and weakness.                               |                      |
| 2   | To develop analytical and creative ability to solve problems individually or as a team. |                      |
| 3   | To make students industry ready through practice of corporate etiquettes.               |                      |
| 4   | To enhance public speaking and presentation skills.                                     |                      |

| Module No. | Contents of the Module  | Hours    | Cos              |
|------------|---|----------|------------------|
| <b>1</b>   | <b>Module-1 Understanding and Managing Self</b><br>Self-Awareness, Self-Management, Anger Management, Time management, Change management.<br>Vision and goal setting - Diff between vision and goal, smart, stretched goal concept, case studies<br>Knowledge, Skill, Attitude<br>Personality analysis using Big 5 personality test<br>Critical Thinking, Problem solving, Creativity and innovation<br>Integrity, ethics, values | 8        | <b>1 &amp; 2</b> |
| <b>2</b>   | <b>Module -2 Corporate etiquettes and Mannerism</b><br>Introduction to Etiquette and Mannerism, Personal Etiquette, Grooming etiquettes- professional styling, Body & personality styling, Video Interview Etiquettes, Personal Interview Etiquettes<br>Effective meeting skills.<br>Workplace behavior, Personal interview   | 6        | <b>3</b>         |
| <b>3</b>   | <b>Module -3 Public Speaking and presentation skills</b><br>Introduction to public speaking, making ideas, illustrating and delivering ideas, overcoming fear of public speaking and developing great delivery.<br>Advanced Business presentation skills, PowerPoint presentation, Group discussion   | <b>6</b> | <b>4</b>         |
| <b>4</b>   | <b>Module -4 Team Work</b><br>Interpersonal skills, group work vs team work   | <b>4</b> | <b>5</b>         |

**Course Outcomes: At the end of the course the student will be able to:**

|            |   |
|------------|---|
| <b>CO1</b> | Understand their strength and weaknesses.   |
| <b>CO2</b> | Develop analytical and creative ability to solve problems.                          |
| <b>CO3</b> | Identify themselves as industry ready through the practice of corporate etiquettes. |
| <b>CO4</b> | Enhance public speaking and presentation skills.                                    |
| <b>CO5</b> | Build team collaboration by working towards shared goals.                           |

**Mapping of Course Outcomes with Programme Outcomes:**

| <b>COs</b> | <b>PO8</b> | <b>PO9</b> | <b>PO10</b> | <b>PO11</b> |
|------------|------------|------------|-------------|-------------|
| CO1        | 3          | 3          |             |             |
| CO2        |            | 3          |             | 3           |
| CO3        | 3          | 3          |             | 3           |
| CO4        |            | 3          | 3           |             |
| CO5        |            |            | 3           | 3           |

**MOOC Course:**

**Communicate with impact** - <https://www.coursera.org/learn/communicate-with-impact>

**Leading Diverse Teams** - <https://www.coursera.org/learn/leading-diverse-teams>

**Practical component:**

1. Mock GD and interview may be conducted at the end of the course to check their confidence. Students can prepare their SWOT analysis and present the same.
2. The students are to be involved in various activities and games such as Just a Minute or Pick and speak to demonstrate each topic.

**Class Internal Assessment – 50 Marks**

**1. Video Assignment        -30Marks**

**2. Weekly Assessment       -20Marks**

**Rubrics for evaluation: (TOTAL - 30 Marks)**

| <b>Sl. No.</b> | <b>Assessment</b>  | <b>COs</b>  | <b>Marks</b> |
|----------------|--|-------------|--------------|
| 1              | Creativity   | CO 2        | 5M           |
| 2              | Approach and flow  | CO 2        | 5M           |
| 3              | Time Management (duration of video and deadline)               | CO 1        | 5M           |
| 4              | Individual presentation in the video                           | CO 4        | 5M           |
| 5              | Report- Brief about the topic and Contribution of team members | CO 5        | 5M           |
| 6              | Report- Reflections (learnings from the activity)              | CO 2 & CO 5 | 5M           |

### **Semester End Assessment – 50 Marks**

|  |                   |
|--|-------------------|
| <b>PPT</b>                                 | <b>- 10 Marks</b> |
| <b>Communication (Clarity and English)</b> | <b>- 10 Marks</b> |
| <b>Body Language</b>                       | <b>- 10 Marks</b> |
| <b>Viva (Q and A)</b>                      | <b>- 10 Marks</b> |
| <b>Project Report</b>                      | <b>- 10 Marks</b> |

**Final Marks = CIA + SEA = 50+50 = 100 Marks**

#### **Faculties:**

1. Ms. Jasmine Basumatary, Assistant Professor, Dept. of Humanities
2. Mrs. Rohini T., Assistant Professor, Dept. of ECE

# B.N.M. Institute of Technology

An Autonomous Institution under VTU, Approved by AICTE

## Department of Mathematics

### Syllabus

| Semester: III  |                     |                                |
|--|---------------------|--------------------------------|
| <b>Course: BRIDGE MATHEMATICS – I</b>  |                     |                                |
| <b>Course Code: 21MATDIP131</b>  |                     |                                |
| (Mandatory Learning Course : Common to all Programmes)   |                     |                                |
| (A bridge course for Lateral Entry students under Diploma quota to BE programmes)  |                     |                                |
| <b>L:T:P:J</b>   | <b>3:0:0:0</b>      | <b>CIA : 100</b>               |
| <b>Credits:</b>  | <b>0</b>            | <b>SEA : --</b>                |
| <b>Hours:</b>  | <b>30</b>           | <b>SEA Duration : --</b>       |
| <b>Course Learning Objectives:</b> The students will be able to  |                     |                                |
| 1 Provide basic concepts of Laplace transform differential and integral calculus.  |                     |                                |
| 2 Provide an insight in to vector differentiation and first order OD E's.  |                     |                                |
| <b>Module-1: Laplace Transform</b>   | <b>No. of hours</b> | <b>Blooms cognitive Levels</b> |
| Introduction to the Laplace transform, Laplace transforms of elementary functions (statements only). Laplace transforms of $e^{at}f(t)$ , $t^n f(t)$ and $\frac{f(t)}{t}$ (without proofs) and unit-step function– problems.   | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-2: Inverse Laplace Transform</b>   |                     |                                |
| Definition and problems, Inverse Laplace transform by partial fractions. Solution of second order linear differential equations using Laplace transforms.  | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-3: Differential Calculus &amp; Partial differentiation</b>   |                     |                                |
| <b>Differential Calculus:</b> Review of successive differentiation-illustrative examples. Taylor's and Maclaurin's series expansions, problems on Maclaurin's series expansion.<br><b>Partial differentiation:</b> Introduction to partial differentiation: Euler's theorem-problems on first order derivatives only. Total derivatives-differentiation of composite functions. Jacobians of order two-problems. | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-4: Integral Calculus and Vector Differentiation</b>  |                     |                                |
| <b>Integral Calculus:</b> Introduction to double and triple integrals and problems.<br><b>Vector Differentiation:</b> Review of vector algebra-illustrative examples. Scalar and vector point functions. Gradient, Divergence, Curl-simple, Solenoidal and irrotational vector fields.   | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-5: Ordinary differential equations</b>   |                     |                                |
| Introduction-solutions of first order and first-degree differential equations: exact and reducible to exact differential equations-Integrating factors on $\frac{1}{N}\left(\frac{\partial M}{\partial y} - \frac{\partial N}{\partial x}\right)$ and $\frac{1}{M}\left(\frac{\partial M}{\partial y} - \frac{\partial N}{\partial x}\right)$ , linear and reducible to linear differential equations.           | <b>06</b>           | <b>Apply</b>                   |

#### Reference Books:

1. E. Kreyszig: "Advanced Engineering Mathematics", John Wiley & Sons, 10<sup>th</sup> Edition(Reprint), 2016.
2. B. S. Grewal: "Higher Engineering Mathematics", Khanna Publishers, 44<sup>th</sup> Edition, 2017.
3. B.V. Ramana: "Higher Engineering Mathematics" 11<sup>th</sup> Edition, Tata McGraw-Hill, 2010.
4. Srimanta Pal & Subobh C. Bhunia: "Engineering Mathematics", Oxford University Press, 3rd Reprint, 2016.



**Course Outcomes:** After completing the course, the students will be able to

- CO 1: Apply the knowledge of Laplace transform in solving integral equations.
- CO 2: Use Laplace transform and inverse Laplace transform in solving differential equations.
- CO 3: Use derivatives and partial derivatives to calculate rate of change of multivariate functions.
- CO 4: Solve double and triple integrals and illustrate the applications of multivariate calculus to understand the solenoidal and irrotational vectors.
- CO 5: Identify and solve first order ordinary differential equations.

**Department of Mathematics**

**Syllabus**

**Semester: IV**

**Course: Complex Analysis, Probability and Random Process**

**Course Code: 21MAC141 (Common to ECE, EEE & ME)**

|                 |                |                               |
|-----------------|----------------|-------------------------------|
| <b>L:T:P:J</b>  | <b>2:2:0:0</b> | <b>CIA: 50</b>                |
| <b>Credits:</b> | <b>03</b>      | <b>SEA: 50</b>                |
| <b>Hours:</b>   | <b>40</b>      | <b>SEA Duration: 03 Hours</b> |

**Course Learning Objectives:** The students will be able to

- 1 Provide an insight into applications of complex variables and conformal mapping arising in potential theory, quantum mechanics, heat conduction and field theory.
- 2 Develop the knowledge of probability, joint probability distribution and Random process occurring in digital signal processing, design engineering and microwave engineering.

| <b>Module-1: Complex Analysis</b>  | <b>No. of hours</b>    | <b>Blooms cognitive Levels</b> |
|--|------------------------|--------------------------------|
| Review of function of a complex variable, limits, continuity and differentiability. Analytic functions. Cauchy-Riemann equations in Cartesian and polar forms. Consequences of Cauchy-Riemann equations (only statement), construction of analytic function using Milne-Thomson method.<br><b>Self study:</b> Applications of Complex function in Engineering.   | <b>L: 04<br/>T: 04</b> | <b>Apply</b>                   |
| <b>Module-2: Conformal Mapping &amp; Complex Integration</b>   |                        |                                |
| <b>Conformal mapping:</b> Introduction, discussion of transformations: $w = e^z$ , $w = z^2$ , $w = z + \frac{1}{z}$ ( $z \neq 0$ ) and bilinear transformations.<br><b>Complex integration:</b> Introduction to complex integration, Cauchy's theorem and Cauchy's integral formula.<br><b>Self study:</b> Problems on Complex line integration.  | <b>L: 04<br/>T: 04</b> | <b>Apply</b>                   |
| <b>Module-3: Probability Distributions &amp; Joint probability distribution</b>  |                        |                                |
| <b>Probability Distributions:</b> Review of basic probability theory. Discrete and continuous Random variables, probability mass/density functions (definitions only). Binomial, Poisson, exponential and normal distributions (without proof).<br><b>Joint probability distribution:</b> Joint Probability distribution for two discrete random variables, expectation, covariance and correlation.<br><b>Self study:</b> Applications of probability distribution in Engineering.  | <b>L: 04<br/>T: 04</b> | <b>Apply</b>                   |
| <b>Module-4: Markov Chain &amp; Sampling Theory</b>  |                        |                                |
| <b>Markov Chain:</b> Introduction to Stochastic process, Probability vectors, Stochastic matrices, Regular stochastic matrices, Markov Chains, Higher transition probabilities, Stationary distribution of Regular Markov chains and absorbing states, Markovian processes.<br><b>Sampling Theory:</b> Introduction to sampling theory, Testing of hypothesis, level of significance, confidence limits, test of significance of mean and difference of means for large samples-z-test, test of significance of small samples-Student's t- distribution, Goodness of fit-Chi-square test.<br><b>Self study:</b> Applications of Markov Chain in Engineering. | <b>L: 04<br/>T: 04</b> | <b>Apply</b>                   |
| <b>Module-5: Random Process</b>  |                        |                                |
| Introduction, classification of random process, methods of description of a random process, stationary, auto-correlation function, Ergodicity, Spectral representation, Weiner-Kinchine theorem, Poisson process, pure birth process, birth and death process with a constant rate, death process with a linear rate.<br><b>Self study:</b> Applications of Random process in Engineering.   | <b>L: 04<br/>T: 04</b> | <b>Apply</b>                   |

**Course Outcomes:** After completing the course, the students will be able to

- CO1: Use the concepts of analytic function and complex potentials to solve the problems arising in electromagnetic field theory.
- CO2: Utilize conformal mapping and complex integral arising in aerofoil theory, fluid flow visualization and image processing.
- CO3: Apply discrete and continuous probability and joint probability distributions in analyzing the probability models arising in engineering field.
- CO4: Use Markov chain in prediction of future events and demonstrate the validity of testing the hypothesis.
- CO5: Use the concepts of random process in dealing with signals in engineering problems.

**Reference Books:**

1. E. Kreyszig: "Advanced Engineering Mathematics", John Wiley & Sons, 10<sup>th</sup> Edition(Reprint), 2016.
2. B. S. Grewal: Higher Engineering Mathematics, Khanna Publishers, 44<sup>th</sup> Edition, 2017.
3. S. D. Sharma : "Operations Research", KedarNath Ram Nath & Co. Meerut, 2014.
4. T. Veerarajan : "Probability, Statistics and Random processes", McGraw Hill Education (India) Private Limited, Third edition, Nineteenth reprint 2017.
5. C. Ray Wylie, Louis C. Barrett : "Advanced Engineering Mathematics", 6<sup>th</sup> Edition, 2. McGraw-Hill Book Co., New York, 1995.
6. James Stewart : Calculus —Early Transcendental, Cengage Learning India Private Ltd., 2017.
7. B. V. Ramana: "Higher Engineering Mathematics" 11<sup>th</sup> Edition, Tata McGraw-Hill, 2010.
8. Srimanta Pal & Subobh C. Bhunia: "Engineering Mathematics", Oxford University Press, 3<sup>rd</sup> Reprint, 2016.

**Web links and Video Lectures:**

1. <https://nptel.ac.in/courses/111106141>
2. <https://www.digimat.in/nptel/courses/video/111107119/L29.html>
3. <https://archive.nptel.ac.in/courses/122/107/122107036/>
4. <https://archive.nptel.ac.in/courses/105/105/105105045/>
5. <https://archive.nptel.ac.in/courses/111/102/111102014/>
6. <https://archive.nptel.ac.in/courses/111/103/111103159/>

| Dept. of Electronics and Communication Engineering  |  |                        |                         |
|---|--|------------------------|-------------------------|
| Choice Based Credit System (CBCS and Outcome Based Education (OBE))   |  |                        |                         |
| Semester: IV  |  |                        |                         |
| Course Name: Digital Signal Processing)   |  | Course Code: 21ECE142  |                         |
| L: T: P: J  | 3: 2: 0: 0   | CIA Marks: 50          |                         |
| Credits:  | 4  | SEA Marks: 50          |                         |
| Hours/Week (Total)  | 5hrs/week (50)   | SEA Duration: 03 Hours |                         |
| Pre-Requisites: Math fundamentals   |  |                        |                         |
| Course Learning Objectives: The students will be able to  |  |                        |                         |
| 1   | To discuss continuous and discrete-time signals and systems, their properties, representations, and methods that are necessary for the analysis of continuous and discrete-time signals and systems. |                        |                         |
| 2   | To develop the mathematical and computational skills needed in application areas like communication, signal processing, and control, which will be taught in other courses.                          |                        |                         |
| 3   | Understand the concept of Z-transforms, frequency domain sampling, and Discrete Fourier Transform (DFT).   |                        |                         |
| 4   | Design digital FIR filters and IIR filters.  |                        |                         |
|   |  |                        |                         |
| Module-1:   |  | No. of Hours           | Blooms Cognitive Levels |
| Introduction and Classification of Signals: Definition of signal and Classification of signals<br>Basic Operations on signals: Amplitude scaling, addition, multiplication, time scaling, time shift, and time reversal.<br>Elementary signals/functions: Exponential, sinusoidal, step, impulse, ramp functions, triangular, and rectangular pulse. Differentiation, Integration of signals  |  | 10                     | Apply CO1               |
| Module-2:   |  |                        |                         |
| System and its properties: Definition of system, Linear-nonlinear, Time variant-invariant, causal-noncausal, static-dynamic, Stable and Unstable Systems. Impulse response representation of LTI Systems: Convolution Sum (combination of Unit Step and Exponential). Convolution Integral  |  | 10                     | Apply CO2               |
| Module-3:   |  |                        |                         |
| Z-Transforms: Definition, Basic problems, Region of Convergence.<br>Fourier Representation of aperiodic Signals: Introduction to DTFT, Definition, and basic problems.<br>Discrete Fourier Transform (DFT): Frequency domain sampling, The Discrete Fourier Transform, DFT as a linear transformation, Properties of the DFT: Periodicity, Linearity, Multiplication of two DFTs and Circular Convolution. Necessity for efficient computation of DFT, Radix-2 Fast Fourier Transform (FFT) algorithm for DFT computation. Radix-2 FFT algorithm for computation of Inverse Discrete Fourier Transform (IDFT) |  | 10                     | Apply CO3               |
| Module-4:   |  |                        |                         |
| IIR Filters: Introduction to IIR filters, Bilinear Transformations, Design of Analog and Digital Butterworth filters (low-pass and high-pass). Realization of IIR filter structure (Direct form I & form II, Cascade, Parallel). Design of Bandpass Analog Butterworth filter.  |  | 10                     | Apply CO4               |
| Module-5:   |  |                        |                         |
| FIR Filters: Introduction to FIR filters, Frequency response of ideal digital low pass filter, high pass filter, Windowing design of FIR filters using Rectangular, Hamming & Bartlett windows.<br>FIR filter realization using Direct form and Lattice structure.  |  | 10                     | Apply CO5               |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE142.1</b>   | Classify the signals as continuous/discrete, periodic/apperiodic, even/odd, energy/power, and deterministic/random signals.                  |
| <b>21ECE142.2</b>   | Determine the linearity, causality, time-invariance, and stability properties of continuous & discrete-time systems and compute convolution. |
| <b>21ECE142.3</b>   | Represent signals in the frequency domain using Z-Transforms, DTFT, and compute the DFT of signals.  |
| <b>21ECE142.4</b>   | Develop and realize the transfer function of IIR filters   |
| <b>21ECE142.5</b>   | Develop and realize the transfer function of FIR filters.  |
| <b>21ECE142.6</b>   | Interpret the signals and systems used in the different areas of application.  |

| <b>Reference Books</b>   |  |
|--|--|
| <ol style="list-style-type: none"> <li>1. Simon Haykins and Barry Van Veen, "Signals and Systems", 2nd Edition, 2018, Wiley India. ISBN 9971-51-239-4.</li> <li>2. Proakis &amp; Monalakis, "Digital signal processing – Principles Algorithms &amp; Applications", 4th Edition, Pearson Education, New Delhi, 2007. ISBN: 81-317-1000-9.</li> <li>3. Michael Roberts, "Fundamentals of Signals &amp; Systems", 2nd edition, Tata McGraw-Hill, 2010, ISBN 978-0-07-070221-9.</li> <li>4. Li Tan, Jean Jiang, "Digital Signal processing - Fundamentals and Applications", Academic Press, 2013, ISBN: 978-0-12-415893.</li> <li>5. Sanjit K Mitra, "Digital Signal Processing, A Computer Based Approach", 4th Edition, McGraw Hill Education, 2013.</li> <li>6. Dr. D. Ganesh Rao and Satish Tunga, "Signals and Systems", Cengage India Private Limited, 2017, ISBN: 978-81-315-3362-8</li> <li>7. Dr. D. Ganesh Rao and Vineeth P Gejji, "Digital Signal Processing" Cengage India Private Limited, 2017, ISBN: 9386858231</li> </ol> |  |

#### **Marks Distribution for Assessment:**

| <b>CIA<br/>(50)</b>               | <b>Component</b> | <b>Description</b>  | <b>Marks</b> |
|-----------------------------------|------------------|---|--------------|
|                                   | Written Test     | <ul style="list-style-type: none"> <li>● Total Number of Test: 3</li> <li>● Each Theory test will be conducted for 30 marks.</li> <li>● Average of 3 tests = 30 Marks</li> </ul>    | 30           |
|                                   | Assignment       | Average of 2 Assignments for 10 marks each  | 10           |
|                                   | AAT              | Open book test  | 10           |
| <b>Total Marks</b>                |                  |   | <b>50</b>    |
| <b>SEA<br/>(50)</b>               | <b>Component</b> | <b>Description</b>  | <b>Marks</b> |
|                                   | Written Exam     | Theory exam will be conducted for 100 marks and scaled down to 50 Marks.<br>The question paper will have 10 full questions each of 20 marks. Students must answer 5 full questions. | <b>50</b>    |
| <b>Total marks for the Course</b> |                  |   | <b>100</b>   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses.

| Dept. of Electronics and Communication Engineering   |   |                        |                         |
|--|---|------------------------|-------------------------|
| Choice Based Credit System (CBCS and Outcome Based Education (OBE))  |   |                        |                         |
| Semester: IV   |   |                        |                         |
| Course Name: ARM Microcontroller & Its Application   |   | Course Code: 21ECE143  |                         |
| L: T: P: J   | 3: 0: 2: 0  | CIA Marks: 50          |                         |
| Credits:   | 4   | SEA Marks: 50          |                         |
| Hours/Week (Total)   | 3 Hours/ Week (40 Hours)  | SEA Duration: 03 Hours |                         |
| Pre-Requisites: Basic knowledge of Microcontroller/Microprocessor  |   |                        |                         |
| Course Learning Objectives: The students will be able to   |   |                        |                         |
| 1  | Understand the architectural features of 32-bit microcontroller ARM Cortex M3.              |                        |                         |
| 2  | Program ARM Cortex M3 using the instructions set and C language for different applications. |                        |                         |
| 3  | Describe the memory systems, bus interface unit, exceptions of ARM Cortex M3.               |                        |                         |
|  |   |                        |                         |
| Module-1: ARM-32-bit Microcontroller   |   | No. of Hours           | Blooms Cognitive Levels |
| Overview of the Cortex-M3, Architecture of ARM Cortex M3, Various Units in the architecture, Debugging support, General Purpose Registers, Special Registers, Exceptions/ Interrupts, The Built-In Nested Vectored Interrupt Controller, Stack operation, Operation Modes. |   | 8                      | Understand CO1          |
| Module-2: ARM Cortex M3 Instruction Sets and Programming-Part 1  |   |                        |                         |
| ARM Cortex M3 Instruction, Assembly basics, General Data-Processing Instructions, Bit Field instructions, IF THEN instructions, Saturation Operations.   |   | 8                      | Apply CO2               |
| Module-3: ARM Cortex M3 Instruction Sets and Programming-Part 2  |   |                        |                         |
| Memory Access instructions, Branch control instructions, Combined Compare and Conditional Branch, Typical Development Flow, CMSIS, Programming in C, Programming in assembly   |   | 8                      | Apply CO3               |
| Module-4: Memory Systems of Cortex-M3  |   |                        |                         |
| Memory System Features Overview, Memory Maps, Memory Access Attributes, Bit-Band Operations, The Pipeline, A Detailed Block Diagram, Bus Interfaces on the Cortex-M3: The I-Code Bus, The D-Code Bus, The System Bus, The External PPB, The DAP Bus                        |   | 8                      | Understand CO4          |
| Module-5: Exceptions in Cortex M3  |   |                        |                         |
| Exception Types, Definitions of Priority, Vector Tables, Interrupt Inputs and Pending Behaviour, Fault Exceptions Bus Faults, Memory Management Faults, Usage Faults, Hard Faults, Dealing with Faults, Supervisor Call and Pend able Service Call                         |   | 8                      | Understand CO5          |

| Lab Experiments (12 Lab sessions +1 Revision session+ 1 Lab Test)                                     |  |
|---|--|
| Experiments   |  |
| 1. ALP to find the sum of first 10 integer numbers.   |  |
| 2. ALP to multiply two 16-bit binary numbers.   |  |
| 3. ALP to find the number of 0's and 1's in a 32-bit data   |  |
| 4. ALP to find determine whether the given 16 bit is even or odd                                      |  |
| 5. ALP to store data in the RAM   |  |
| 6. ALP to reverse the string  |  |
| 7. Interface a simple Switch and display its status through Relay, Buzzer and LED.                    |  |
| 8. Interface a Stepper motor and rotate it in clockwise and anti-clockwise direction.                 |  |
| 9. Interface a DAC and generate Triangular and Square waveforms.                                      |  |
| 10. Display the Hex digits 0 to F on a 7-segment LED interface, with an appropriate delay in between. |  |
| 11. Interface keypad & display the Key Pressed on LCD   |  |
| 12. Toggle the LED when an external interrupt occurs  |  |
| Revision  |  |
| Lab Assessment & evaluation   |  |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE143.1</b>   | Describe the architectural features of 32-bit microcontroller ARM Cortex M3.                   |
| <b>21ECE143.2</b>   | Apply the knowledge of instruction set of ARM Cortex M3 for programming.                       |
| <b>21ECE143.3</b>   | Apply the knowledge of embedded C Programming for ARM Cortex M3 for different applications.    |
| <b>21ECE143.4</b>   | Understand the memory map & Bus interface unit of ARM Cortex M3.                               |
| <b>21ECE143.5</b>   | Describe the exceptions of ARM Cortex M3.  |
| <b>21ECE143.6</b>   | Design a Embedded system using ARM CortexM3 for Societal needs, Health care, Home application. |

| <b>Reference Books</b>  |  |
|---|--|
| <ol style="list-style-type: none"> <li>1. The Definitive Guide to the ARM® Cortex-M3, Second Edition, Joseph You.</li> <li>2. Discovering the STM32 Microcontroller by Geoffrey Brown, Publisher: Indiana University, Published: 2016.</li> </ol> |  |

#### **Marks Distribution for Assessment:**

| <b>CIA<br/>(50)</b>           | <b>Component</b> | <b>Description</b>   | <b>Marks</b> |
|-------------------------------|------------------|--|--------------|
|                               | Written Test     | <ul style="list-style-type: none"> <li>● Total Number of Test: 3</li> <li>● Each Theory test will be conducted for 30 marks</li> <li>● Average of 3 tests = 30 Marks</li> </ul>      | 30           |
|                               | Lab Exam         | Average of two Lab Internals, Record and Observation   | 20           |
| <b>Total Marks</b>            |                  |  | <b>50</b>    |
| <b>SEA<br/>(50)</b>           | <b>Component</b> | <b>Description</b>   | <b>Marks</b> |
|                               | Written Exam     | Theory exam will be conducted for 100 marks and scaled down to 50 Marks<br>The question paper will have 10 full questions each of 20 marks. Students have to answer 5 full questions | <b>50</b>    |
| <b>Total marks(CIA + SEA)</b> |                  |  | <b>100</b>   |

| Dept. of Electronics and Communication Engineering<br>Choice Based Credit System (CBCS and Outcome Based Education (OBE))  |   |                                |
|--|---|--------------------------------|
| Semester: IV   |   |                                |
| <b>Course Name: Analog and Digital Communication</b>   |   | <b>Course Code: 21ECE144</b>   |
| <b>L: T: P: J</b>  | <b>3:0:2:0</b>  | <b>CIA Marks: 50</b>           |
| <b>Credits:</b>  | <b>4</b>  | <b>SEA Marks: 50</b>           |
| <b>Hours/Week (Total)</b>  | <b>5 / 50 hours</b>   | <b>SEA Duration: 03 Hours</b>  |
| <b>Pre-Requisites:</b> Fourier Transform, Basics of Signals and systems  |   |                                |
| <b>Course Learning Objectives: The students will be able to</b>  |   |                                |
| 1  | Understand and analyze concepts of Analog Modulation schemes viz; AM, FM techniques.  |                                |
| 2  | Understand and analyze concepts digitization of signals viz; sampling, quantizing and encoding.                               |                                |
| 3  | Understand the performance of the analog modulation scheme in the presence of the AWGN channel.                               |                                |
| 4  | Understand the concept of signal processing of digital data and signal conversion to symbols at the transmitter and receiver. |                                |
| 5  | Understand and analyze concepts of Digital Modulation schemes and Compute performance metrics of bandlimited channel.         |                                |
| <b>Module-1: AMPLITUDE MODULATION</b>  |   | <b>No. of Hours</b>            |
|  |   | <b>Blooms Cognitive Levels</b> |
| <b>AMPLITUDE MODULATION:</b> Introduction, Amplitude Modulation: Time & Frequency Domain description, switching modulator, Envelop detector.<br><b>DOUBLE SIDE BAND-SUPPRESSED CARRIER MODULATION:</b> Time and Frequency Domain description, Ring modulator Coherent detection, Costas Receiver, Frequency Translation. Frequency- Division Multiplexing, VSB Transmission of Analog and Digital Television |   | <b>8</b>                       |
|  |   | <b>Apply CO1</b>               |
| <b>Module-2: ANGLE MODULATION</b>  |   |                                |
| <b>ANGLE MODULATION:</b> Basic definitions, Frequency Modulation: Narrow Band FM, Wide Band FM, the Transmission bandwidth of FM Signals, Generation of FM Signals, Demodulation of FM Signals, FM Stereo Multiplexing, Phase-Locked Loop: Linear model of PLL. The Superhetrodyne Receiver.   |   | <b>8</b>                       |
|  |   | <b>Apply CO2</b>               |
| <b>Module-3: NOISE</b>   |   |                                |
| <b>NOISE</b> - Shot Noise, Thermal noise, White Noise, Noise Equivalent Bandwidth <b>Text 1</b><br><b>NOISE IN ANALOG MODULATION:</b> Introduction, Receiver Model, Noise in DSB-SC receivers. Noise in AM receivers, Threshold effect, Noise in FM receivers, Capture effect, FM threshold effect, FM threshold reduction, Pre-emphasis and De-emphasis in FM. Correlation receiver.                        |   | <b>8</b>                       |
|  |   | <b>Apply CO3</b>               |
| <b>Module-4: SAMPLING AND QUANTIZATION</b>   |   |                                |
| Introduction, Why Digitize Analog Sources? The Low pass Sampling process Pulse Amplitude Modulation, Time Division Multiplexing, Pulse-Position Modulation, Generation of PPM Waves, Detection of PPM Waves, Quantization Random Process, Quantization Noise. Robust quantization, Compander.  |   | <b>8</b>                       |
|  |   | <b>Apply CO4</b>               |
| <b>Module-5: DIGITAL MODULATION TECHNIQUES</b>   |   |                                |
| Pulse-Code Modulation: Sampling, Quantization, Encoding, Regeneration, Decoding, Filtering, Multiplexing; Delta Modulation. Digital Modulation schemes: Amplitude shift keying, Frequency shift keying, Binary Phase shift keying. Quadrature amplitude modulation, Differential phase shift keying.   |   | <b>8</b>                       |
|  |   | <b>Apply CO5</b>               |



| Lab Experiments |   |
|-----------------|---|
| Sl. No.         | Experiment  |
| 1               | Pulse sampling, Verification of sampling theorem.   |
| 2               | Time Division Multiplexing and Demultiplexing of two bandlimited signals.   |
| 3               | BASK generation and detection   |
| 4               | BFSK generation and detection   |
| 5               | Simulate Amplitude Modulation and Demodulation: Standard AM using MATLAB. (One hour session to be engaged for concept discussion) |
| 6               | Simulate Amplitude Modulation and Demodulation: DSBSC using MATLAB. (One hour session to be engaged for concept discussion)       |
| 7               | Simulate Frequency modulation and demodulation using MATLAB   |
| 8               | Simulate Pulse Width modulation and demodulation using MATLAB. (One hour session to be engaged for concept discussion)            |
| 9               | Simulate Pulse Position modulation and demodulation using MATLAB. (One hour session to be engaged for concept discussion)         |
| 10              | Simulate Pulse code modulation and demodulation using MATLAB. (One hour session to be engaged for concept discussion)             |

**Course Outcomes: After completing the course, the students will be able to**

|            |  |
|------------|--|
| 21ECE144.1 | Analyze and compute the performance of Amplitude modulation.   |
| 21ECE144.2 | Analyze and compute the performance of Frequency modulation.   |
| 21ECE144.3 | Compute the performance of Analog modulation schemes in presence of an AWGN channel.                 |
| 21ECE144.4 | Analyze and compute the performance of pulse modulation schemes with and without quantization noise. |
| 21ECE145.5 | Analyze the performance of digital modulation schemes.   |
| 21ECE146.6 | Explain functional blocks of signal processing and communication applications.                       |

**Reference Books**

1. "Communication Systems", Simon Haykins & Moher, 5th Edition, John Wiley, India Pvt. Ltd, 2010, ISBN 978 – 81 – 265 – 2151 – 7.
2. **Digital communications**, Simon Haykin, John Wiley India Pvt. Ltd, 2008.
3. Simon Haykin, "Digital Communication Systems", John Wiley & Sons, First Edition, 2014, ISBN 978-0-471-64735-5.
4. John G Proakis and Masoud Salehi, "Fundamentals of Communication Systems", 2014 Edition, Pearson Education, ISBN 978-8-131-70573-5.
5. Modern Digital and Analog Communication Systems, B. P. Lathi, Oxford University Press., 4th edition.
6. An Introduction to Analog and Digital Communication, Simon Haykins, John Wiley India Pvt. Ltd., 2008, ISBN 978–81–265–3653–5.
7. Bernard Sklar and Ray, "Digital Communications - Fundamentals and Applications", Pearson Education, Third Edition, 2014, ISBN: 978-81-317-2092-9.

**Marks Distribution for Assessment:**

| CIA<br>(50)                                 | Component    | Description  | Marks |
|---|--------------|--|-------|
|   | Written Test | ● Total Number of Test: 3  | 30    |
|   |              | ● Each Theory test will be conducted for 30 marks  |       |
|   |              | ● Average of 3 tests = 30 Marks  |       |
|   | Laboratory   | Record and Observation-10 Marks  | 10    |
| One Laboratory Internal Assessment-10 Marks |              | 10   |       |
| Total Marks                                 |              |  | 50    |
| SEA<br>(50)                                 | Component    | Description  | Marks |
|   | Written Exam | Theory exam will be conducted for 100 marks and scaled down to 50 Marks<br>The question paper will have 10 full questions each of 20 marks. Students have to answer 5 full questions | 50    |
|   |              | Total marks (CIA+ SEA)   | 100   |

**Optional/ Not Compulsory:**

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses.

| Dept. of Electronics and Communication Engineering<br>Choice Based Credit System (CBCS and Outcome Based Education (OBE))  |   |                        |
|--|---|------------------------|
| Semester: IV   |   |                        |
| Course Name: Signal Processing Applications of MATLAB  |   | Course Code: 21ECE145  |
| L: T: P: J   | 0: 0: 2 :2  | CIA Marks: 50          |
| Credits:   | 2   | SEA Marks: 50          |
| Hours/Week (Total)   | 4 / 25 hours  | SEA Duration: 03 Hours |
| Pre-Requisites: Signals and Systems and DSP Fundamentals   |   |                        |
| Course Learning Objectives: The students will be able to   |   |                        |
| 1  | Simulate continuous time, discrete time signals and verify sampling theorem using MATLAB.   |                        |
| 2  | Perform computation of DFT and convolution along with the verification of their properties. |                        |
| 3  | Perform operations and transformations on Images.   |                        |
| 4  | Compute and display the filtering operations and compare with the theoretical values.       |                        |
| 5  | Able to use Simulink platform to verify the properties of a system.                         |                        |
|  |   |                        |
| List of Programs   |   |                        |
| 1. Plot discrete and continuous time waveforms like rectangular pulse, square wave, triangular pulse, triangular wave, impulse, step, and ramp signal.   |   |                        |
| 2. Verification of sampling theorem (use interpolation function).  |   |                        |
| 3. Computation of Linear convolution of two given sequences. Prove commutative, distributive, and associative property of convolution.   |   |                        |
| 4. Introduction to Image processing toolbox. Perform basic image processing operations like add, subtract, complement, and crop.   |   |                        |
| 5. Computation of N point DFT of a given sequence and to plot magnitude and phase spectrum.  |   |                        |
| 6. Perform the following operations on images: image enhancement, and thresholding on a given gray scale image.  |   |                        |
| 7. Perform the following operations on images: smoothening and sharpening using different filters.   |   |                        |
| 8. Design and implementation of Low pass FIR/IIR filter to meet the desired specifications and test the filter with a speech/audio file. Plot the spectrum of audio signal before and after filtering. |   |                        |
| 9. Checking Linearity/Non-Linearity of a system using SIMULINK   |   |                        |
| 10. Checking Time variance/invariance of a system using SIMULINK   |   |                        |
| Mini Project : One mini project to be completed in 12 lab sessions including its evaluation.   |   |                        |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE145.1</b>   | Demonstrate sampling theorem and plot elementary waveforms in continuous and discrete time domains. |
| <b>21ECE145.2</b>   | Analyze the signals using DFT and convolution.  |
| <b>21ECE145.3</b>   | Perform basic operations on images.   |
| <b>21ECE145.4</b>   | Apply filtering techniques on audio/speech signals.   |
| <b>21ECE145.5</b>   | Build a system to verify the properties of a given system using SIMULINK.                           |
| <b>21ECE145.6</b>   | Develop a real time application in speech/audio/image processing.                                   |

| <b>Reference Books</b>   |
|--|
| 1. Vinay K Ingle, John G Proakis, Digital Signal Processing using MATLAB, Fourth Edition, Cengage India Private Limited, 2017. |
| 2. John W. Leis, Digital Signal Processing Using MATLAB for Students and Researchers, Wiley, August 2011.                      |

**Marks Distribution for Assessment:**

|             |                          |   |       |
|-------------|--------------------------|---|-------|
| CIA<br>(50) | Component                | Description   | Marks |
|             | Practical                | Lab records   | 5     |
|             |                          | Performance day wise  | 5     |
|             | Internal Laboratory Test | Conduction  | 5     |
|             |                          | Viva  | 5     |
|             | Project                  | Demonstration   | 10    |
|             |                          | Presentation  | 10    |
|             |                          | Report  | 10    |
| Total Marks |                          |   | 50    |
| SEA<br>(50) | Component                | Description   | Marks |
|             | External Laboratory Exam | External Lab exam will be conducted for 100 marks and scaled down to 50 Marks. The marks allocated is as follows:<br>Write up – 20<br>Conduction – 70<br>Viva-voce – 10 | 50    |
|             |                          | Total marks (CIA + SEA)   | 100   |

**Additional Assessment Tools (AAT)** – Presentations, Open ended experiments, Mini Projects, MATLAB courses.

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

| Semester: IV  |   |               |
|---|---|---------------|
| COURSE: CONSTITUTION OF INDIA AND PROFESSIONAL ETHICS   |   |               |
| Course Code: 21CIP146   | L:T:P:J: 1:0:0:0  | CIE Marks: 50 |
| Credits:  | 1   | SEE Marks: 50 |
| Hours:  | 15 Hrs  | SEE Duration: |
| <b>Course Learning Objectives: The students will be able to</b>   |   |               |
| 1   | know the fundamental political codes, structure, procedures, powers, and duties of Indian government institutions, fundamental rights, directive principles, and the duties of citizens |               |
| 2   | know the Indian top civil service positions and the exams conducted by UPSC and SPSC for the same   |               |
| 3   | Understand engineering ethics and their responsibilities; identify their individual roles and ethical responsibilities towards society.   |               |
| <b>MODULE 1: Introduction to Indian Constitution</b>  |   | <b>RBT</b>    |
|   |   | <b>Hrs</b>    |
| The Necessity of the Constitution, Introduction to Indian Constitution, The Making of the Constitution, Role of Constituent Assembly, Preamble and Salient features of the Constitution of India, Fundamental Rights and its Restriction and limitations in different complex situations, Directive Principles of State Policy, Fundamental Duties.   |   | 1,2,3         |
|   |   | 3             |
| <b>MODULE 2: System of Government, Central Government, State Government</b>   |   | <b>RBT</b>    |
|   |   | <b>Hrs</b>    |
| System of Government-Parliamentary System, Federal System. Central Government-Basic details, Powers and Functions of Union Executive. Parliament- LS and RS (Composition, Duration, Membership and Presiding officers of Parliament and their functions). Leaders in Parliament (Leader of the House and Leader of the Opposition). Sessions of Parliament (Summoning, Adjournment, Adjournment Sine Die, Prorogation, Dissolution). Quorum of House, Language in Parliament, Joint sitting of two Houses. State Government-Basic details, Powers and Functions of State Executive. State Legislature (Composition, Duration, Membership and Presiding officers of Parliament and their functions). |   | 1,2,3         |
|   |   | 3             |
| <b>MODULE 3: Judiciary, Amendments and Emergency Provisions</b>   |   | <b>RBT</b>    |
|   |   | <b>Hrs</b>    |
| Supreme Court, High Court, Judicial Review, Judicial Activism. Methods in Constitutional Amendments (How and Why). Types of Emergencies and its Consequences, Recent Amendments to the Constitution.  |   | 1,2,3         |
|   |   | 3             |
| <b>MODULE 4: Elections, Constitutional and Non Constitutional Bodies</b>  |   | <b>RBT</b>    |
|   |   | <b>Hrs</b>    |
| Elections- Election Commission of India, Electoral Process. Constitutional Bodies- Election Commission, Union Public Service Commission, State Public Service Commission, Goods and Service Tax Council. Non-Constitutional Bodies- Central Information Commission, State Information Commission.   |   | 1,2,3         |
|   |   | 3             |

| <b>MODULE 5: Professional Ethics</b>   | <b>RBT</b>     | <b>Hrs</b> |
|--|----------------|------------|
| Scope & Aims of Engineering & Professional Ethics, Positive and Negative Faces of Engineering Ethics, Responsibilities in Engineering, the impediments to Responsibility. Trust and Reliability in Engineering, Risks, Safety and liability in Engineering, Clash of Ethics, IPRs (Intellectual Property Rights) | <b>1, 2, 3</b> | <b>3</b>   |

**Course outcome:** On completion of this course, students will be able to,  
CO1: Have constitutional knowledge and legal literacy.  
CO2: Have knowledge on All India Services and State Civil Services.  
CO3: Understand Engineering and Professional Ethics and responsibilities of Engineers.

### Reference Books

#### Suggested Learning Resources:

- Title of the Book - Indian Polity**  
Name of the Author - M Lakshmikanth  
Name of the Publisher-Mc Graw Hill Education  
Edition and Year- 2019
- Title of the Book - Engineering Ethics**  
Name of the Authors - M. Govindarajan, S.Natarajan, V. S. Senthil kumar  
Name of the Publisher- Prentice-Hall  
Edition and Year-2004
- Durga Das Basu (DD Basu):** “Introduction to the Constitution on India”, (Students Edition.)Prentice –Hall EEE, 19th / 20th Edn., (Latest Edition) or 2008.
- Shubham Singles, Charles E. Haries, and Et al:** “Constitution of India and Professional Ethics” byCengage Learning India Private Limited, Latest Edition – 2018.
- M.Govindarajan, S.Natarajan, V.S.Senthilkumar,** “Engineering Ethics”, Prentice –Hall of IndiaPvt. Ltd. New Delhi, 2004
- M.V.Pylee,** “An Introduction to Constitution of India”, Vikas Publishing, 2002.
- Latest Publications of **NHRC - Indian Institute of Human Rights**, New Delhi.

### Web Links and Video Lectures

[www.unacademy.com/lesson/future-perfect-tense/YQ9NSNQZ](http://www.unacademy.com/lesson/future-perfect-tense/YQ9NSNQZ)  
<https://successesacademy>

### Question paper pattern for SEA and CIA.

- The SEA question paper will be set for 50 marks and the pattern of the question paper will be objective type (MCQ).
- The CIA question paper will be set for 50 marks and the pattern of the question paper will be objective type (MCQ).

**Final Marks = CIA + SEA = 50 + 50 = 100 Marks**

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

| Semester: III  |   |                       |
|--|---|-----------------------|
| COURSE: Environmental Science  |   |                       |
| Course Code: 21EVS147  | L: T: P: J: 0:2:0:0   | CIA Marks: 50         |
| Credits:   | 1   | SEA Marks: 50         |
| Hours:   | 15 sessions   | SEE Duration: 1.5 Hrs |
| <b>Course Learning Objectives: The students will be able to</b>  |   |                       |
| 1  | To identify the major challenges in environmental issues and evaluate possible solutions.                       |                       |
| 2  | Develop analytical skills, critical thinking and demonstrate socio-economic skills for sustainable development. |                       |
| 3  | To analyse an overall impact of specific issues and develop environmental management plan.                      |                       |
| <b>Module 1 – Environment</b>  |   | <b>RBT</b>            |
| <b>a) Environment:</b> Definition,<br><b>b) Ecology and Ecosystems:</b><br>(i) Biomes (ii) Ecosystems & Sustainable Ecosystem (iv) Human Activities & Environment.<br><b>c) Human activities and their Impact on Environment:</b><br>(i) Agriculture (ii) Industry (iii) Transport (iv) mining.<br>(i) Environmental Impact Assessment (EIA) (ii) Sustainable Development  |   | <b>Hrs</b>            |
|  |   | 1,2,3                 |
|  |   | <b>6</b>              |
| <b>Module 2 – Natural Resources</b>  |   | <b>RBT</b>            |
| <b>Natural Resources</b><br><b>a) Forest Resources:</b><br>(i) Forest wealth and its conservation (ii) Wood–Major renewable resources (iii) Biodiversity<br><b>b) Water resources and its uses:</b><br>(i) Quality (ii) Impurities – Fluoride etc<br><b>c) Water borne diseases</b><br><b>d) Energy:</b><br>(i) Conventional (ii) Non-conventional (iii) Wind, Solar, Tidal, Hydro Electric, Biomass & Biogas (iv) Alternate source – Hydrogen, Bio fuel, Hybrid & semi hybrid vehicles, etc<br><b>e) Life on Earth:</b><br>(i) Wild life management, Nature, Genetically Modified (GM Crops), Balance of Nature – Nature pyramid, Floods and droughts |   | <b>Hrs</b>            |
|  |   | 1,2,3                 |
|  |   | <b>6</b>              |
| <b>Module 3 – Pollution and Current Global issues</b>  |   | <b>RBT</b>            |
| <b>a) Pollution</b><br>i. Types of pollutions, Environmental, Air, Water, Noise, land, Effluents Public Health<br>ii. Carbon foot print, Climate change, Ozone depletion (Chloro Floro carbon) Global warming, Greenhouse effect, Acid Rain.<br>iii. Ground water pollution, (Earth summits for balancing effect on environment).  |   | <b>Hrs</b>            |
|  |   | 1,2,3                 |
|  |   | <b>6</b>              |

|  |              |            |
|--|--------------|------------|
| <b>b) Current Global Environmental issues:</b><br>(i) Population<br>(ii) Local urbanization - concrete jungles.<br>(iii) waste water management.<br>(iv) Effect on natural drainage in cities, encroachment on lakes, etc.   |              |            |
| <b>Module 4 – Sustainable development</b>  | <b>RBT</b>   | <b>Hrs</b> |
| <b>Sustainable development:</b><br>i. Solid waste, E-waste and Bio Medical waste management.<br>ii. Waste Water treatment, Encouraging Green buildings.<br>iii. Vermi compost, organic farming, adopting Subhash Palekar farming methods.  | <b>1,2,3</b> | 6          |
| <b>Module 5 – Environmental policies, Protection &amp; Laws</b>  | <b>RBT</b>   | <b>Hrs</b> |
| <b>Environmental policies, Protection &amp; Laws</b><br><b>Regulations &amp; Laws</b><br>i. Forest, Wildlife, Water and Air.<br>ii. Environmental movements, NGO's – Chipko, Silent valley, Narmada<br>iii. Environmental Ethics.<br>iv. Resource needs for future generations – for mankind other life forms on this planet.<br>v. Role of individual in sustainable development. | <b>1,2,3</b> | 6          |

| <b>Textbook/s</b> |  |                               |   |                                |
|-------------------|--|-------------------------------|---|--------------------------------|
| <b>Sl. No.</b>    | <b>Title of the Book</b>                     | <b>Name of the Author/s</b>   | <b>Name of the Publisher</b>                  | <b>Edition and Year</b>        |
| 1                 | Environmental Studies                        | Anil Kumar De, Arnab Kumar De | New Age International (P) Limited, Publishers | 2018                           |
| 2.                | Environmental Studies                        | Benny Joseph                  | Tata Mc Graw – Hill.                          | 2 <sup>nd</sup> Edition, 2012  |
| 3.                | Environmental Science working with the Earth | G.Tyler Miller Jr.            | Thomson Brooks /Cole,                         | 11 <sup>th</sup> Edition, 2006 |



| <b>Course Outcomes: After completing the course, the students shall</b> |  |
|---|--|
| 21EVS147.1  | CO1: Understand the concepts of ecology, environment and biodiversity and the consequences of their destruction. |
| 21EVS147.2  | CO2: Gain awareness about the advances in energy systems as well ways to manage natural resources.               |
| 21EVS147.3  | CO3: Understand the different kinds of pollution, their impact and manage waste through recycling.               |
| 21EVS147.4  | CO4: Gain awareness about the current environmental issues and their global impact on various aspects.           |
| 21EVS147.5  | CO5: Develop critical thinking and apply them to analyse a problem or question related to the environment.       |

### **Class Internal Assessment**

|            |                  |  |
|------------|------------------|--|
| IA1        | 30marks          | Average of 2 IA will be taken 30 Marks |
| IA2        | 30Marks          |  |
| Assignment | 20 Marks         | 20 Marks                               |
|            | <b>Total CIA</b> | <b>50 Marks</b>                        |

### **Semester End Assessment**

|                   |                          |                 |
|-------------------|--------------------------|-----------------|
| Semester end Exam | Objective Type Questions | 50 Marks        |
|                   | <b>Total SEA</b>         | <b>50 Marks</b> |

**Final Marks = CIA + SEA = 50+50 = 100 Marks**

### **Faculties:**

1. Sri. Narayan Rao R Maanay, Secretary, BNMIT
2. Dr. Prathibha B S, HoD, Chemistry Dept.

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU, Approved by AICTE

## Department of Mathematics

### Syllabus

| Semester: IV   |                     |                                |
|--|---------------------|--------------------------------|
| <b>Course: BRIDGE MATHEMATICS – II</b>   |                     |                                |
| <b>Course Code: 21MATDIP141</b>  |                     |                                |
| (Mandatory Learning Course: Common to all Programmes)  |                     |                                |
| (Abridge course for Lateral Entry students under Diploma quota to BE programmes)   |                     |                                |
| <b>L:T:P:J</b>   | <b>3:0:0:0</b>      | <b>CIA : 100</b>               |
| <b>Credits:</b>  | <b>0</b>            | <b>SEA : ---</b>               |
| <b>Hours:</b>  | <b>30</b>           | <b>SEA Duration : ---</b>      |
| <b>Course Learning Objectives:</b> The students will be able   |                     |                                |
| 1 To provide essential concepts of linear algebra, second & higher order differential equations along with methods to solve them.  |                     |                                |
| 2 To provide an insight into elementary probability theory and numerical methods.  |                     |                                |
| <b>Module-1: Linear Algebra</b>  | <b>No. of hours</b> | <b>Blooms cognitive Levels</b> |
| Introduction - rank of matrix by elementary row operations - Echelon form. Consistency of system of linear equations - Gauss elimination method. Eigen values and Eigen vectors of a square matrix. Problems.  | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-2: Numerical Methods</b>   |                     |                                |
| Finite differences. Interpolation / extrapolation using Newton's forward and backward difference formulae-problems. Solution of polynomial and transcendental equations-Newton-Raphson method-problems. Numerical integration: Simpson's one third rule and Weddle's rule- problems (All formulas without proof) | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-3: Higher order ordinary differential equations</b>  |                     |                                |
| Linear differential equations of second order equations with constant coefficients. Homogeneous / non-homogeneous equations. Inverse differential operators on $e^{ax}$ , $\sin(ax + b)$ , $\cos(ax + b)$ and a polynomial $P_n(x)$ .  | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-4: Partial Differential Equations (PDE)</b>  |                     |                                |
| Formation of PDE by elimination of arbitrary constants and functions. Solution of non- homogeneous PDE by direct integration. Homogeneous PDE involving derivatives with respect to one independent variable only.   | <b>06</b>           | <b>Apply</b>                   |
| <b>Module-5: Probability</b>   |                     |                                |
| Introduction. Sample space and events. Axioms of probability. Addition & multiplication theorems. Conditional probability, Bayes's theorem, problems   | <b>06</b>           | <b>Apply</b>                   |

#### Reference Books:

1. E. Kreyszig: "Advanced Engineering Mathematics", John Wiley & Sons, 10<sup>th</sup> Edition(Reprint), 2016.
2. B. S. Grewal: "Higher Engineering Mathematics", Khanna Publishers, 44<sup>th</sup> Edition, 2017.
3. B.V. Ramana: "Higher Engineering Mathematics" 11th Edition, Tata McGraw-Hill, 2010.
4. Srimanta Pal & Subobh C. Bhunia: "Engineering Mathematics", Oxford University Press, 3<sup>rd</sup> Reprint, 2016.

**Course Outcomes:** After completing the course, the students will be able to

- CO 1: Solve systems of linear equations using matrix algebra.
- CO 2: Apply the knowledge of numerical methods in modelling and solving engineering problems.
- CO 3: Make use of analytical methods to solve higher order differential equations.
- CO 4: Classify partial differential equations and solve them by exact methods
- CO 5: Apply elementary probability theory and solve related problems.

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))

Semester: V

Course Name: Digital Image Processing

Course Code: 21ECE151

|                    |               |                        |
|--------------------|---------------|------------------------|
| L: T: P: J         | 3 : 0 : 0 : 0 | CIA Marks: 50          |
| Credits:           | 3             | SEA Marks: 50          |
| Hours/Week (Total) | 3             | SEA Duration: 03 Hours |

Prerequisite: Nil

Course Learning Objectives: The students will be able to

|   |   |
|---|---|
| 1 | Understand the fundamentals of Digital Image Processing.                            |
| 2 | Explain the image enhancement techniques both in the Spatial and Frequency Domain.  |
| 3 | Explain the Restoration techniques used in Digital image processing.                |
| 4 | Understand the Color and Morphological Image Processing methods.                    |
| 5 | Understand the techniques for Segmentation and Representation of gray scale Images. |

| Module-1: Digital Image Fundamentals   | No. of Hours | Blooms Cognitive Levels/CO Mapping |
|--|--------------|------------------------------------|
| <b>Digital Image Fundamentals:</b> What is Digital Image Processing? Origins of Digital Image Processing, Examples of fields that use DIP, Fundamental Steps in Digital Image Processing, Components of an Image Processing System, Elements of Visual Perception, Image Sensing and Acquisition, Image Sampling and Quantization, Some Basic Relationships Between Pixels, Linear and Nonlinear Operations. | 8            | Apply<br>CO1                       |
| <b>Module-2: Filtering in the Spatial and Frequency Domain</b>   |              |                                    |
| <b>Spatial Domain:</b> Some Basic Intensity Transformation Functions, Histogram Processing, Fundamentals of Spatial Filtering, Smoothing Spatial Filters, Sharpening Spatial Filters<br><b>Frequency Domain:</b> Preliminary Concepts, The Discrete Fourier Transform (DFT) of Two Variables, Filtering in the Frequency Domain, Image Smoothing and Image Sharpening Using Frequency Domain Filters         | 8            | Apply<br>CO2                       |
| <b>Module-3: Restoration</b>   |              |                                    |
| <b>Restoration:</b> Noise models, Restoration in the Presence of Noise Only using Spatial Filtering and Frequency Domain Filtering, Linear, Position-Invariant Degradations, Estimating the Degradation Function, Inverse Filtering, Minimum Mean Square Error (Wiener) Filtering.   | 8            | Apply<br>CO3                       |
| <b>Module-4: Color and Morphological Image Processing</b>  |              |                                    |
| <b>Color Image Processing:</b> Color Fundamentals, Color Models, Pseudocolor Image Processing.<br><b>Morphological Image Processing:</b> Preliminaries, Erosion and Dilation, Opening and Closing, The Hit-or-Miss Transforms  | 8            | Apply<br>CO4                       |
| <b>Module-5: Segmentation, Representation and Description</b>  |              |                                    |
| <b>Segmentation:</b> Point, Line, and Edge Detection, Thresholding, Region-Based Segmentation<br><b>Representation and Description:</b> Representation, Boundary descriptors, Regional Descriptors   | 8            | Apply<br>CO5                       |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE151.1</b>   | Apply image formation techniques and the role human visual system plays in perception of gray and color image data.               |
| <b>21ECE151.2</b>   | Apply image processing techniques in both the spatial and frequency (Fourier) domains.  |
| <b>21ECE151.3</b>   | Apply image Restoration techniques in the spatial domain.   |
| <b>21ECE151.4</b>   | Apply image processing techniques for Color and Morphological Image Processing.   |
| <b>21ECE151.5</b>   | Design image analysis techniques in the form of image segmentation evaluate the methodologies for Representation and Description. |
| <b>21ECE151.6</b>   | Conduct independent study and analysis of Image Enhancement and Restoration techniques for real time applications.                |

| <b>Reference Books</b>   |
|--|
| 1.Digital Image Processing- Rafael C Gonzalez and Richard E. Woods, PHI 3 <sup>rd</sup> Edition, 2010.         |
| 2. Image Processing- S.Jayaraman, S.Esakkirajan, T.Veerakumar, Tata McGraw Hill 2 <sup>nd</sup> Edition, 2020. |
| 3. Fundamentals of Digital Image Processing-A. K. Jain, Pearson Education, 2 <sup>nd</sup> Edition, 2004.      |

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA   |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     |   |

**i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA : 50%**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks.<br>5 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))

Semester: V

Course Name: Electromagnetic Waves and Transmission Lines Course Code: 21ECE152

|                    |         |                      |
|--------------------|---------|----------------------|
| L: T:P: J          | 2:2:0:0 | CIA Marks:50         |
| Credits:           | 3       | SEA Marks:50         |
| Hours/Week (Total) | 4       | SEA Duration:03Hours |

Pre-Requisites: Vector Calculus

Course Learning Objectives: The students will be able to

|   |  |
|---|--|
| 1 | Understand the applications of Coulomb's law and Gauss law to different charge distributions and the applications of Laplace's and Poisson's Equations to solve real time problems on capacitance of different charge distributions. |
| 2 | Understand the physical significance of Biot-Savart's and Ampere's Law for different current distributions   |
| 3 | Know the physical interpretation of Maxwell's equations and applications for Plane waves for their behavior in different media   |
| 4 | Acquire knowledge of Poynting Theorem and its application of Power flow.   |
| 5 | Understand the parameters of microwave transmission line and waveguides.   |

| Module-1: Laws of Static Electric Field | No. of Hours | Blooms Cognitive Levels/ CO Mapping |
|---|--------------|-------------------------------------|
|---|--------------|-------------------------------------|

**Vector Basics:** Vector Algebra, Rectangular coordinate system, vector components and unit vectors, the dot product, the cross product, circular cylindrical coordinates, the spherical coordinate system.

**Coulomb's Law, Electric Field Intensity and Flux density**

Experimental law of Coulomb, Electric field intensity, Field due to continuous point charge distribution, Field of a line charge, Electric flux density

**Gauss's law and Divergence**

Gauss's law, Divergence. Maxwell's First equation (Electrostatics), Vector Operator  $\nabla$  and divergence theorem [Qualitative Analysis Only]

8

Apply  
CO1

**Module-2: Energy, Potential, Current and Current density, Poisson's, Laplace's Equations**

**Energy, Potential and Conductors:** Energy expended in moving a point charge in an electric field, The line integral, Definition of potential difference and potential, The potential field of point charge, Potential gradient.

**Current, Current density, Continuity of current.**

**Poisson's and Laplace's Equations:** Derivation of Poisson's and Laplace's Equations, Uniqueness theorem, Examples of the solution of Laplace's equation.

8

Apply  
CO2

**Module-3: Laws of Magneto-Static Fields and Time Varying Field**

**Steady Magnetic Field:** Biot-Savart Law, Ampere's circuital law, Curl, Stokes' theorem [Qualitative Analysis Only], Magnetic flux and magnetic flux density, Scalar and Vector Magnetic Potentials

**Faraday's law of Electromagnetic Induction** –Integral form and Point form

**Maxwell's equations:** Inconsistency of Ampere's law with continuity equation, displacement current, Maxwell's equations in point form and integral form.

8

Apply  
CO3

|  |  |                      |
|--|--|----------------------|
| <b>Module-4: Uniform Plane Wave</b>  |  |                      |
| <b>Uniform Plane Wave:</b> Wave Propagation in free space, Derivation of General wave equations from Maxwell's equations, Relation between E and H, Solution of wave equation for free space and good conductor, wave propagation in free space and good conductor ( $\gamma$ , $\alpha$ , $\beta$ , $\eta$ ) Skin effect or Depth of penetration, Poynting theorem. | <b>8</b>   | <b>Apply<br/>CO4</b> |
| <b>Module-5: Transmission lines</b>  |  |                      |
| Transmission Line equations and solutions, Reflection Coefficient and Transmission Coefficient, Standing Wave and Standing Wave Ratio, Calculation of reflection coefficient and standing wave ratio using Smith Chart.  | <b>8</b>   | <b>Apply<br/>CO5</b> |
| <b>Course Outcomes: After completing the course, the students will be able to</b>  |  |                      |
| <b>21ECE152.1</b>  | Evaluate problems on electrostatic force, electric field due to point, linear, volume charges by applying conventional methods. Understanding Gauss law to evaluate Electric fields due to different charge distributions and Volume Charge distribution by using Divergence Theorem |                      |
| <b>21ECE152.2</b>  | Determine potential and energy with respect to point charge. Apply Laplace's equation to determine voltage function, capacitance.  |                      |
| <b>21ECE152.3</b>  | Apply Biot-Savart's and Ampere's laws for evaluating Magnetic field for different current configurations, Apply Maxwell's equations for time varying fields.   |                      |
| <b>21ECE152.4</b>  | Apply Maxwell's equations for deriving the propagation of EM waves in free space and conductors and Evaluate power associated with EM waves using Poynting theorem.  |                      |
| <b>21ECE152.5</b>  | Explain propagation of RF signals through transmission line and transmission line basics.  |                      |
| <b>21ECE152.6</b>  | Self-learning through listening and comprehension of audio / video lectures related to electro-magnetic fields and waves domain and understand the effects of E.M. waves with respect to Electromagnetic interference (EMI) and Electromagnetic Compatibility (EMC).                 |                      |

|  |  |
|--|--|
| <b>Reference Books</b>   |  |
| 1. Engineering Electromagnetics by W.H. Hayt and J.A. Buck, Tata McGraw Hill, 2014, ISBN-978-93-392-0327-6, 8 <sup>th</sup> Edition.<br>2. Microwave Devices and Circuits by Samuel Y.Liao, PHI, 2003, 3 <sup>rd</sup> Edition.<br>3. Electromagnetic Waves and Radiating systems, E. C. Jordan and K.G. Balmain, PHI, 2006 2 <sup>nd</sup> Edition.<br>4. Elements of Electromagnetics, Matthew N.O., Sadiku, Oxford university press, 2007, 4 <sup>th</sup> Edition.<br>5. Electromagnetics, Joseph Edminister, Schaum Outline Series, McGraw Hill, 1995, 2 <sup>nd</sup> Edition. |  |

#### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

**i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA: 50%**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |



| <b>Dept. of Electronics and Communication Engineering</b><br><b>Choice Based Credit System (CBCS and Outcome Based Education (OBE))</b>  |  |   |
|--|--|---|
| <b>Semester: V</b>   |  |   |
| <b>Course Name: Computer Networks and Security</b>   |  | <b>Course Code: 21ECE153</b>              |
| <b>L: T: P: J</b>  | <b>3: 0: 2 :0</b>  | <b>CIA Marks: 50</b>                      |
| <b>Credits:</b>  | <b>4</b>   | <b>SEA Marks: 50</b>                      |
| <b>Hours/Week (Total)</b>  | <b>5</b>   | <b>SEA Duration: 03 Hours</b>             |
| <b>Pre-Requisites: Basics of Digital Communication</b>   |  |   |
| <b>Course Learning Objectives: The students will be able to</b>  |  |   |
| 1  | Understand the layering architecture of OSI reference model and TCP/IP protocol suite.   |   |
| 2  | Understand the protocols associated with each layer.                                     |   |
| 3  | Learn the different networking architectures and their representations.                  |   |
| 4  | Explain transport layer and application layer protocols.                                 |   |
| 5  | Explain network security services, mechanisms, Transport Level Security and IP Security. |   |
| <b>Module-1: Data communication and Physical Layer</b>   |  | <b>No. of Hours</b>                       |
|  |  | <b>Blooms Cognitive Levels/CO Mapping</b> |
| <b>Data communication:</b> Components, Data representation, Data flow. Networks: Network criteria, Physical Structures, Network types: LAN, WAN, Switching, The Internet. TCP/IP Protocol Suite, Layered Architecture, Layers in the TCP/IP Protocol Suite, Description of each Layer, Encapsulation and De-capsulation, Addressing, Multiplexing and De-multiplexing, OSI versus TCP/IP.<br><b>Physical Layer:</b> Data and Signals, Transmission impairment. |  | <b>10</b><br><br><b>Apply CO1</b>         |
| <b>Module-2: Data-Link Layer</b>   |  |   |
| <b>Data-Link Layer:</b> Nodes and Links, Services, Two Categories of links, Sublayers, Link Layer addressing: Types of addresses, ARP. Data Link Control (DLC) services: Framing, Flow and Error Control, Data Link Layer Protocols: Simple Protocol, Stop and Wait protocol, Wired and Wireless LANs: Ethernet Protocol, Standard Ethernet.   |  | <b>10</b><br><br><b>Apply CO2</b>         |
| <b>Module-3: Network Layer</b>   |  |   |
| <b>Network Layer:</b> Introduction, Network Layer services: Packetizing, Routing and Forwarding, Packet Switching: Datagram Approach, Virtual Circuit Approach. IPV4 Addresses: Address Space, Classful Addressing, Classless Addressing, DHCP, Network Address Resolution, Distance Vector Routing, Link State Routing, Path vector routing.  |  | <b>10</b><br><br><b>Apply CO3</b>         |
| <b>Module-4: Transport Layer and Application Layer</b>   |  |   |
| <b>Transport Layer:</b> Introduction, Transport Layer Services, Connectionless and Connection-oriented Protocols, Transport Layer Protocols: Simple protocol, Stop and wait protocol, Go-Back-N Protocol, Transport-Layer Protocols in the Internet: User Datagram Protocol: User Datagram, UDP Services, Transmission Control Protocol: TCP Services, TCP Features.<br><b>Application Layer:</b> Introduction, Services, Application - layer paradigms.       |  | <b>10</b><br><br><b>Understand CO4</b>    |

| <b>Module-5: Network Security</b>  |           |                           |
|--|-----------|---------------------------|
| <b>Network Security:</b> Need for Security, Security Approaches, Principles of Security, Types of Attacks, Viruses and Related Threats, Need for Firewalls, Firewall Characteristics, Types of Firewalls, overview of IP security. | <b>10</b> | <b>Understand<br/>CO5</b> |
| <b>Transport Level Security:</b> Web security consideration, Transport Layer Security (TLS).   |           |                           |

| <b>Lab Experiments</b>  |
|---|
| 1. Program to implement three nodes point – to – point network with duplex links between them.  |
| 2. Program to implement transmission of ping messages/trace route over a network topology consisting of 6 nodes and find the number of packets dropped due to congestion.   |
| 3. Program to implement an Ethernet LAN using n nodes and set multiple traffic nodes and plot congestion window for different source / destination.   |
| 4. Program to construct simple LAN and understand the concept and operation of Address Resolution Protocol (ARP) using CISCO Packet Tracer.   |
| 5. Program for simulation of distance vector routing algorithm.   |
| 6. Program for simulation of link state routing algorithm.  |
| 7. Program to analyze the performance of various configurations and protocols in LAN using CISCO Packet Tracer.   |
| 8. Program to construct a Wireless LAN and make the PCs communicate wirelessly using CISCO Packet Tracer.   |
| 9. Program to install and configure network interface card. Identify IP address of a workstation, class of the address and configure the IP address on a workstation. To share the hardware resources on a network. |
| Revision  |
| Lab assessment & evaluation   |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE153.1</b>   | Apply the concepts of networking to create networks thoroughly.                   |
| <b>21ECE153.2</b>   | Apply the Data Link layer services and protocols to networks.                     |
| <b>21ECE153.3</b>   | Apply the Network layer services and protocols to networks.                       |
| <b>21ECE153.4</b>   | Explain the Transport layer and Application layer services and protocols.         |
| <b>21ECE153.5</b>   | Explain security concerns in networks, Transport level security and IP security.  |
| <b>21ECE153.6</b>   | Discuss and analyze the various applications that can be implemented on networks. |

| <b>References</b>  |
|--|
| 1. Forouzan, “Data Communications and Networking”, 5th Edition, McGraw Hill, 2013, ISBN: 1-25- 906475-3.   |
| 2. William Stallings, “Cryptography and Network Security Principles and Practice”, Pearson Education Inc., 5th Edition, 2014, ISBN: 978-81-317- 6166-32. |
| 3. Atul Kahate, “Cryptography and Network Security”, TMH, 4th Edition, 2019, ISBN-13: 978-9353163303, ISBN-10: 9353163307.                               |
| 4. Andrew Tannenbaum, “Computer Networks”, Prentice Hall, 2003, ISBN: 0-13-066102-3.   |

| PCL        | CIA | SEA | CIA (50)         |   |    |     | SEA   |
|------------|-----|-----|------------------|---|----|-----|---|
|            |     |     |                  | I   | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30  | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 marks scaled down to 20 marks |    |     |   |
|            |     |     | Assignment       | Average of 2 Assignments – 10M                            |    |     |   |
|            |     |     | Practical        | Weekly Assessment – 10 Marks<br>IA test – 10 Marks        |    |     |   |
|            |     |     | Total – 50 Marks |   |    |     | Total – 50 Marks  |

**i) CIA: 50%**

|        |   |                                |
|--------|---|--------------------------------|
| Theory | <b>IA Test (Theory):</b> 3 IA tests - each of 30 Marks<br><b>Assignment:</b> 2 Assignments – each of 10 marks | Average of 3 tests<br>30 Marks |
| Lab    | <b>Weekly Assessment</b> – 10 Marks<br><b>Practical test (1)</b> - 10 marks                                   | 20 Marks                       |
| Total  |   | 50 Marks                       |

**ii) SEA: 50%**

**Question Paper:**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer, each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| Total              |   | 50 Marks  |

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**Dept. of Electronics and Communication Engineering**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: V**

**Course Name: Embedded Systems and RTOS**

**CourseCode:21ECE154**

|                           |                |                            |
|---------------------------|----------------|----------------------------|
| <b>L:T:P:J</b>            | <b>3:0:2:0</b> | <b>CIAMarks:50</b>         |
| <b>Credits:</b>           | <b>4</b>       | <b>SEAMarks:50</b>         |
| <b>Hours/Week (Total)</b> | <b>5</b>       | <b>SEADuration:03Hours</b> |

**Pre-Requisites: Knowledge of microprocessor/microcontroller hardware, programming concept in assembly and C.**

**Course Learning Objectives: The student will be able to**

- 1 Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system
- 2 Develop an embedded system using hardware software co-design approaches
- 3 Understanding Advanced Architecture and Processor- Memory Organization
- 4 Understanding the ESP32 architecture
- 5 Apply the scheduling techniques for the given real time operating system
- 6 Design a Embedded system for Societal needs, Health care, Home application

| <b>Module-1: Embedded System Components</b>  | <b>No. of Hours</b> | <b>Blooms Cognitive Levels /CO Mapping</b> |
|--|---------------------|--|
| Introduction, Embedded Vs General computing system, Classification of Embedded systems, Major applications and purpose of ES. Elements of an Embedded System, Difference between Harvard and Princeton, Big and Little Endian formats, Memory, Sensors, Actuators, LED, 7 segment LED display, Optocoupler, relay, Piezo buzzer, Push button switch, Communication Interface (onboard, external – RS 232, USB, Blue tooth, Wi-Fi types), Embedded firmware | <b>10</b>           | <b>Understand CO1</b>                      |
| <b>Module-2: Embedded System Design Concepts</b>   |                     |  |
| Introduction, Characteristics, and Quality Attributes of Embedded Systems, Operational and non-operational quality attributes, Embedded Systems – Application and Domain Specific, Hardware Software Co-Design and Program Modelling, Issues in hardware software co design, computational models, hardware software tradeoffs, Embedded firmware design and development: Design approaches, development languages   | <b>10</b>           | <b>Apply CO2</b>                           |
| <b>Module-3: Advanced Architecture and Processor- Memory Organization</b>  |                     |  |
| Processor- Memory Organization, Introduction to Advanced processor Architectures, Processor Organization, Instruction level Parallelism, Intel x86 Architecture, ARM, SHARC, Memory Types & Addresses, Memory Addresses, Memory Hierarchy & Cache, Performance Metrics, Selection of Processor & Memory Devices.   | <b>10</b>           | <b>Understand CO3</b>                      |

|   |           |                           |
|---|-----------|---------------------------|
|   |           |                           |
| <b>Module-4: ESP 32 Architecture</b>  |           |                           |
| Introduction ,Features, Functional Description, Interrupt Matrix (INTERRUPT),Overview, Features, Reset and Clock, IO_MUX and GPIO Matrix (GPIO, IO_MUX) , Overview,Peripheral Input via GPIO Matrix, DPort Registers, DMA Controller (DMA), Overview, Features, Functional Description, Watchdog Timers (WDT) | <b>10</b> | <b>Understand<br/>CO4</b> |
| <b>Module-5: Real Time Operating Systems</b>  |           |                           |
| Introduction, Operating System basics, Types of operating systems, Task, process and threads excluding programs, Thread preemption, Multi-processing and multitasking, Task scheduling excluding programs   | <b>10</b> | <b>Apply<br/>CO5</b>      |

|  |
|--|
| <b>Lab Experiments</b>   |
| 1. ESP32 Basics- Understanding ESP32 Board and Components, Installing and work with Arduino IDE, Program to read the status of push button & control LED & Buzzer. |
| 2. Program to display a message on LCD using ESP32   |
| 3. Program to control LED interfaced to ESP32 using Bluetooth (HC-05)  |
| 4. Program to control LED interfaced to ESP32 using Wifi (Blynk)   |
| 5. Program to control LED interfaced to ESP32 using with Wifi ( Google Firebase Cloud)   |
| 6. Program for creating child threads  |
| 7. Programs to build multithreaded applications  |
| 8. Program for FIFO scheduling   |
| 9. Program for round robin scheduling  |
| 10. Program for Priority Based scheduling  |
| 11. Revision   |
| 12. Lab Assessment   |

|   |   |
|---|---|
| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
| <b>21ECE154.1</b>   | Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system |
| <b>21ECE154.2</b>   | Develop an embedded system using hardware software co-design approaches   |
| <b>21ECE154.3</b>   | Understanding Advanced Architecture and Processor- Memory Organization  |
| <b>21ECE154.4</b>   | Understanding the ESP32 Architecture  |
| <b>21ECE154.5</b>   | Apply the scheduling techniques for the given real time operating system  |
| <b>21ECE154.6</b>   | Design a Embedded system for Societal needs, Health care, Home application  |

| Reference Books  |
|--|
| <ol style="list-style-type: none"> <li>1. “Introduction to Embedded Systems”, Shibu K V, Tata McGraw Hill Education Private Limited, 2<sup>nd</sup> Edition, 2017.</li> <li>2. Embedded System: Architecture, Programming and Design by Raj Kamal, TMH Publication, 3<sup>rd</sup> Edition, 2003.</li> <li>3. ESP32 Technical Reference Manual</li> <li>4. Embedded Software Primer, David Simon, Pearson Education, 2002.</li> <li>5. Real Time Systems Theory and Practice by Rajib Mall, Pearson Education, 2006.</li> <li>6. Embedded Real-time Systems Programming, Sri Ram Iyer and Pankaj Gupta, TMH, 2017.</li> <li>7. The Linux Programming Interface, Michael Kerrisk, No Starch Press, 2010.</li> </ol> |

**Marks Distribution for Assessment:**

| PCL        | CIA | SEA | CIA (50)         |   |    |     | SEA   |
|------------|-----|-----|------------------|---|----|-----|---|
|            |     |     |                  | I   | II | III | Conduction: 100 M<br>Reduced to 50 M  |
| Conduction | 50  | 50  | Written Test     | 30  | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 marks scaled down to 20 marks |    |     |   |
|            |     |     | Assignment       | Average of 2 Assignments – 10M                            |    |     |   |
|            |     |     | Practical        | Weekly Assessment – 10 Marks<br>IA test – 10 Marks        |    |     |   |
|            |     |     | Total – 50 Marks |   |    |     | Total – 50 Marks  |

**i) CIA: 50%**

|        |  |                                |
|--------|--|--------------------------------|
| Theory | <b>Test (Theory):</b> 3 IA tests - each of 30 Marks<br><b>Assignment:</b> 2 Assignments – each of 10 marks | Average of 3 tests<br>30 Marks |
| Lab    | <b>Weekly Assessment</b> – 10 Marks<br><b>Practical test(1)</b> - 10 marks                                 | 20 Marks                       |
| Total  |  | 50 Marks                       |

**ii) SEA: 50%**

**Question Paper:**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer, each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| Total              |   | 50 Marks  |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))

Semester: V

Course Name: Artificial Intelligence and Machine Learning Applications Course Code: 21ECE155

|                    |   |                        |
|--------------------|---|------------------------|
| L: T: P: J         | 0: 0 : 2 : 2                              | CIA Marks: 50          |
| Credits:           | 2   | SEA Marks: 50          |
| Hours/Week (Total) | 12 Lab sessions + 12 sessions for project | SEA Duration: 03 Hours |

Pre-Requisites: Linear Algebra Fundamentals and basics of MATLAB

Course Learning Objectives: The students will be able to

|   |   |
|---|---|
| 1 | Introduce some concepts and techniques that are core to Artificial Intelligence and Machine Learning. |
| 2 | Understand Intelligent Systems, and problem solving.  |
| 3 | Understand K-means clustering algorithms  |
| 4 | Acquire knowledge of Classification and Regression Techniques   |
| 5 | Identify and apply Machine Learning algorithms to solve real world problems                           |

| Module 1 – Artificial Intelligence  | No. of Hours | Blooms Cognitive Levels/CO Mapping |
|---|--------------|------------------------------------|
| <b>Artificial Intelligence:</b> History, Intelligent systems, foundation and sub area of AI, applications, current trend and development of AI, Problem solving state space search and control strategies, introducing machine learning with MATLAB<br><b>Program:</b><br>1. Write a MATLAB script to import an excel file by<br>a.) Manual Method<br>b.) Programmatic Method using in-built command as a table variable and display the summary of table | 5            | Apply CO1                          |

| Module 2: Machine Learning   |   |           |
|--|---|-----------|
| <b>Machine Learning:</b> Introduction to Machine Learning. Different types of learning: Supervised, Unsupervised and Reinforcement learning, Feature Selection<br><b>Program:</b><br>1. Write a MATLAB script to load the titanic dataset (Ref1) and use suitable functions to select the best features for predicting the survival status of a given passenger. | 5 | Apply CO2 |

| Module 3: Clustering Algorithms  |   |           |
|--|---|-----------|
| Introduction to Clustering algorithms, K Means clustering algorithm<br><b>Program:</b><br>1. Write a MATLAB script to perform data clustering.<br>a.) Hard Clustering Algorithm<br>b.) Soft Clustering Algorithm | 5 | Apply CO3 |

| Module 4: Classification |
|--------------------------|
|--------------------------|

|  |   |           |
|--|---|-----------|
| Introduction to Classification, Evaluation Metrics, MATLAB Implementation.<br><b>Program:</b><br>1. Write a MATLAB script to develop a classifier model to predict the survival status of a passenger using titanic dataset  | 5 | Apply CO4 |
| <b>Module 5: Regression</b>  |   |           |
| Introduction to Regression, Evaluation Metrics, MATLAB Implementation.<br><b>Program:</b><br>1. Write a MATLAB script to implement a Regression Model on a given Dataset   | 5 | Apply CO5 |
| <b>Mini Project:</b> One mini project to be completed in 12 lab sessions including its evaluation.   |   |           |
| <b>Sample Mini Projects</b>  |   |           |
| 1. Image Segmentation.<br>2. Sign Language Recognition System.<br>3. Game Playing Project.<br>4. Handwritten Character Recognition.<br>5. Bitcoin Price Predictor.<br>6. Music Genre Classification.<br>7. Wine Quality Test.<br>8. Titanic Survival Prediction Project. |   |           |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE155.1</b>   | Implement data importing and reading using MATLAB                                    |
| <b>21ECE155.2</b>   | Implement Feature Selection and Prediction using MATLAB                              |
| <b>21ECE155.3</b>   | Design Clustering Algorithms for a given Problem Statement and a Dataset             |
| <b>21ECE155.4</b>   | Design suitable Classification Algorithm for a given Problem Statement and a Dataset |
| <b>21ECE155.5</b>   | Design suitable Regression Algorithm for a given Problem Statement and a Dataset     |
| <b>21ECE155.6</b>   | Apply Machine Learning algorithms to solve real world problems.                      |

| <b>Reference Books</b>   |
|--|
| 1. Saroj Kaushik, Artificial Intelligence, Cengage learning, 2014, 1st Edition, Cengage Learning India<br>2. Giuseppe Ciaburro, MATLAB for Machine Learning, Packt Publishing, 2017, ISBN: 978-1-78839-843-5, 2017<br>3. Elaine Rich, Kevin Knight, Artificial Intelligence, Tata McGraw Hill Education, 3rd edition, 2017<br>4. Oliver Theobald, Machine Learning for Absolute Beginners, 2017. |



### Marks Distribution for Assessment:

| PBL        | CIA | SEA | CIA (50)  |  |       | SEA<br>Conduction: 100 M<br>Reduced to: 50 M                 |
|------------|-----|-----|-----------|--|-------|--|
| Conduction | 50  | 50  | Theory    | I IA   | II IA | Project<br>Assessed for 100<br>marks.<br>reduced to 50 Marks |
|            |     |     |           | 30   | 30    |  |
|            |     |     |           | Average of 2 tests – 30 marks  |       |  |
|            |     |     | Practical | Weekly Assessment (Record/Project)<br>– 10 Marks<br>Lab IA test – 10 Marks |       |  |
|            |     |     |           | Total – 50 Marks   |       | Total – 50 Marks   |

#### i) CIA: 50%

|   |             |
|---|-------------|
| <b>Theory - 2 IA tests - Each of 30 Marks</b>   | 30 Marks    |
| <b>Practical</b><br><b>Weekly Assessment</b> - Lab record/Project – 10 Marks<br><b>Lab IA test</b> – 10 Marks | 20 Marks    |
| <b>Total</b>  | <b>50 M</b> |

#### ii) SEA: 50%

|                |  |  |
|----------------|--|--|
| <b>Project</b> | Write up – 10 Marks.<br>Project report – 25 Marks<br>Presentation & Demonstration - 50 Marks<br>Viva-Voce – 15 Marks | <b>100 Marks<br/>reduced to 50<br/>Marks</b> |
| <b>Total</b>   |  | <b>50 Marks</b>                              |

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**Dept. of Electronics and Communication Engineering**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: V**

**Course Name: Smart Sensor Technologies**

**Course Code: 21ECE1561**

|                           |                   |                               |
|---------------------------|-------------------|-------------------------------|
| <b>L: T: P: J</b>         | <b>3: 0: 0: 0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>           | <b>3</b>          | <b>SEA Marks: 50</b>          |
| <b>Hours/Week (Total)</b> | <b>3</b>          | <b>SEA Duration: 03 Hours</b> |

**Pre-Requisites: Basic Engineering Science**

**Course Learning Objectives: The students will be able to**

|   |   |
|---|---|
| 1 | Introducing fundamentals of sensing and exploration of various sensors widely used for real life application. |
| 2 | To familiarize the characteristics, working principle and application of special purpose transducers          |
| 3 | Obtain knowledge on sensors, sensors with microcontrollers and their applications.                            |
| 4 | To develop skillset to implement IoT systems for wearable applications.                                       |

| <b>Module-1: An Introduction to Smart Technologies</b>  | <b>No. of Hours</b> | <b>Blooms cognitive Levels/CO Mapping</b> |
|---|---------------------|---|
| Introduction, Sensor Requirement in Smart Systems, Sensor Technologies for Smart systems, General concepts and terminology of Sensor systems, Transducers classification-sensors and actuators, General input-output configurations, Static and dynamic characteristics of measurement system.                  | 8                   | Understand CO1                            |
| <b>Module-2: Smart Sensors and Applications</b>   |                     |   |
| Integrated and Smart sensors, IEEE 1451 standard & Transducer Electronic Datasheets (TEDs), Overview of various smart sensors: Digital temperature sensor (DS1621, TMP36GZ), Humidity sensor (DHT11, DHT22, FC28), IR sensor (FC51), Gas sensor (MQ2, MQ8), Pressure sensors (BMP180), Accelerometers (ADXL335) | 8                   | Understand CO2                            |
| <b>Module-3: Sensors with Microcontroller</b>   |                     |   |
| Introduction, Separate Vs Integrated Signal Conditioning, Digital Conversion, Online Tool for Evaluating a Sensor Interface Design, MCU Control, MCUs for Sensor Interface, Sensor Integration, Application Examples.   | 8                   | Understand CO3                            |
| <b>Module-4: Bio-Medical and Automotive sensors</b>   |                     |   |
| Electrical Potentials and Propagation of Nerve Signals, Electrodes, EMG, ECG, EEG, Blood pressure, Engine temperature, Airflow, Combustion, Torque, Accelerometers, Gas composition sensors – Liquid level sensors  | 8                   | Understand CO4                            |
| <b>Module-5: Smart Devices Case Study</b>   |                     |   |
| Wearable devices use cases- Smart watches, Android wear, Smart glasses/ Google Glass, fitness trackers, health care devices, sports, smart clothing, defense and security. Wearables: Challenges and Opportunities, Future and Research Roadmap   | 8                   | Understand CO5                            |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE1561.1</b>  | Understand the working principle and behavior of sensors  |
| <b>21ECE1561.2</b>  | Understand the working principle of special purpose sensors and the need for developing smart sensors       |
| <b>21ECE1561.3</b>  | Able to understand how microcontroller is implemented in sensor technologies.                               |
| <b>21ECE1561.4</b>  | Relate and realize the importance automotive sensors and bio medical sensors                                |
| <b>21ECE1561.5</b>  | Design and develop IoT end points for wearable applications.  |
| <b>21ECE1561.6</b>  | Able to design and perform experiments on the sensors and develop the projects based on the customer needs. |

| <b>Reference Books</b>   |  |
|--|--|
| <ol style="list-style-type: none"> <li>1. Jacob Fraden, "Handbook of Modern Sensors: Physics, Designs, and Applications", 5th Edition, Springer, 2016.</li> <li>2. Frank, Randy, "Understanding smart sensors", Artech House integrated microsystems series, 3rd Edition, 2013.</li> <li>3. John Turner, Automotive Sensors, 2012, Momentum Press, USA.</li> <li>4. J. G. Webster, Medical Instrumentation; Application and Design, 2010, 4th Edition, John Wiley, USA.</li> <li>5. John G Webster, Measurement, Instrumentation and Sensors Handbook, 2014, CRC Press, USA.</li> <li>6. M. Mardonova and Y. Choi, "Review of Wearable Device Technology and Its Applications to the Mining Industry," Energies, vol. 11, p. 547, 2018.</li> </ol> |  |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to 50 M   |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

#### **i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test: 3 IA tests - Each of 30 Marks</b>   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT) – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses</b> | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA: 50%**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))

Semester: V

Course Name: Mobile Communication and Processor

Course Code: 21ECE1562

|                    |            |                        |
|--------------------|------------|------------------------|
| L: T: P: J         | 3: 0 :0 :0 | CIA Marks: 50          |
| Credits:           | 3          | SEA Marks: 50          |
| Hours/Week (Total) | 3          | SEA Duration: 03 Hours |

Pre-Requisites: Basics of Communication, Basics of Electronics and Processors

Course Learning Objectives: The students will be able to

- |   |   |
|---|---|
| 1 | Understand the Concepts of Wireless Communication Systems |
| 2 | Understand basic blocks of Mobile Phone                   |
| 3 | Understand Concept of System on Chip                      |

| Module-1: Evolution of Wireless Communications Technology  | No. of Hours | Blooms Cognitive Levels/CO Mapping |
|--|--------------|------------------------------------|
| Introduction to wireless communications: Evolution of mobile radio communications, paging system, cordless telephone system, cellular telephone system, Modern wireless communication systems: 2G networks, 3G networks, Bluetooth and personal area networks.   | 8            | Understand CO1                     |
| <b>Module-2: Mobile Phone Basic Block Diagram</b>  |              |                                    |
| Study of BGA IC's, Block diagram of IC and Installation of software, Flashing, PC based diagnostic tools, mobile sets formatting, used of secret codes. Types of Mobile software, Data cable, Card reader, Mobile display, Remove/replace Component & Mobile phone hardware design of (transmitter filter, microphone, receptor, Antenna, RF power amplifier, local oscillator, Audio IC, speaker, charger etc. ). | 8            | Understand CO2                     |
| <b>Module-3: Hardware and Software Architecture of Mobile Phone</b>  |              |                                    |
| Introduction to Mobile Architecture: Mobile Architecture, Mobile Hardware Architecture, Mobile Software Architecture, Mobile Architecture Vs Computer Architecture, Memory organization, Input and output devices for handled devices and Booting of Mobile devices.   | 8            | Understand CO3                     |
| <b>Module-4: System on Chip Architecture</b>   |              |                                    |
| Hardware Architecture: Introduction to the processors used for Mobile and Handheld devices and SoC architecture like OMAP and Snap Dragon and its case study with reference to protocols, Input and output interfaces, GPU, DSP  | 8            | Understand CO4                     |
| <b>Module-5: Higher Generation Cellular Standards</b>  |              |                                    |
| Higher Generation Cellular Standards: 3G Standards: evolved EDGE, enhancements in 4G standard, Architecture and representative protocols, call flow for LTE, VoLTE, UMTS, introduction to 5G.  | 8            | Understand CO5                     |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1562.1</b>  | Understand the different generation wireless communication technology              |
| <b>21ECE1562.2</b>  | Understand the basic layout of mobile phone  |
| <b>21ECE1562.3</b>  | Understand the difference between the processor used in computers and mobile phone |
| <b>21ECE1562.4</b>  | Understand the standard SoC used in Mobile Phone                                   |
| <b>21ECE1562.5</b>  | Understand the requirements of Next Generation Wireless Communication Technologies |
| <b>21ECE1562.6</b>  | Troubleshoot the hardware and software issue in a basic mobile phone               |

| <b>Reference Books</b>   |  |
|--|--|
| <ol style="list-style-type: none"> <li>1. Rappaport T. S., “Wireless Communication: Principles and Practice”, Second Edition, Pearson Education, 2009</li> <li>2. Mobile Handset Design, Sajal K. Das, Wiley, 2010<br/>(<a href="https://www.oreilly.com/library/view/mobile-handset-design/9780470824672/">https://www.oreilly.com/library/view/mobile-handset-design/9780470824672/</a>)</li> <li>3. Mobile First, Luke Wroblewski, A Book Apart; First Edition , 2011.</li> <li>4. Tommi Mikkonen, “Programming Mobile Devices: An Introduction for Practitioners”, John Wiley &amp; Sons Ltd, 2007.</li> <li>6. J Scheible and Ville Tuulos John, “Mobile Python Rapid Prototyping of Applications on the Mobile Platform” Wiley India Pvt. Ltd, 2008.</li> <li>8. S. Poslad, “Ubiquitous Computing: Smart Devices, Environments and Interactions,” Wiley, 2009.</li> <li>9. Nick Lecrenski, Karli Watson, “Windows Phone 7 Application Development” version 2011</li> <li>10. Jermaine G. Anderson “Flash Lite Mobile Development” version 2010.</li> </ol> |  |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to 50 M   |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

#### **i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test: 3 IA tests - Each of 30 Marks</b>   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT) – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses</b> | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA: 50%**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**Dept. of Electronics and Communication Engineering**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: V**

**Course Name: Satellite Communication** **Course Code: 21ECE1563**

|                           |                  |                               |
|---------------------------|------------------|-------------------------------|
| <b>L: T: P: J</b>         | <b>3:0 :0 :0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>           | <b>3</b>         | <b>SEA Marks: 50</b>          |
| <b>Hours/Week (Total)</b> | <b>3</b>         | <b>SEA Duration: 03 Hours</b> |

**Pre-Requisites: Communication concepts, Mathematical Preliminaries**

**Course Learning Objectives: The students will be able to**

- 1 Understand the basic principle of satellite orbits and trajectories.
- 2 Study of electronic systems associated with a satellite and the earth station.
- 3 Understand the various technologies associated with the satellite communication.
- 4 Focus on a communication satellite and the national satellite system.
- 5 Study of satellite applications focusing various domains services such as remote sensing, weather forecasting and navigation.

| <b>Module-1: Satellite Orbits and Trajectories:</b>  | <b>No. of Hours</b> | <b>Blooms Cognitive Levels/CO Mapping</b> |
|--|---------------------|---|
| Definition, Basic Principles, Orbital parameters, Injection velocity and satellite trajectory, Types of Satellite orbits, Orbital perturbations, Satellite stabilization, Orbital effects on satellite's performance, Eclipses, Look angles: Azimuth angle, Elevation angle.               | 8                   | Understand CO1                            |
| <b>Module-2: Satellite subsystem and Earth Station:</b>  |                     |   |
| Power supply subsystem, Attitude and Orbit control, Tracking, Telemetry and command subsystem, Payload.<br>Types of earth station, Architecture, Design considerations, Testing, Earth station Hardware, Satellite tracking.   | 8                   | Apply CO2                                 |
| <b>Module-3: Multiple Access Techniques and Satellite Link Design Fundamentals:</b>  |                     |   |
| Introduction, FDMA (No derivation), SCPC Systems, MCPC Systems, TDMA, CDMA, SDMA.<br>Transmission Equation, Satellite Link parameters, Propagation considerations.   | 8                   | Apply CO3                                 |
| <b>Module-4: Communication Satellites:</b>   |                     |   |
| Introduction, Related Applications, Frequency Bands, Payloads, Satellite Vs. Terrestrial Networks, Satellite Telephony, Satellite Television, Satellite radio, regional satellite Systems, National Satellite Systems.   | 8                   | Understand CO4                            |
| <b>Module-5: Remote Sensing, Weather Forecasting, and Navigation Satellites:</b>   |                     |   |
| Classification of remote sensing systems, orbits, Payloads, Types of images: Image Classification, Interpretation, Applications.<br>Fundamentals of Weather Forecasting, Images, Orbits, Payloads, Applications.<br>Development of Satellite Navigation Systems, GPS system, Applications. | 8                   | Understand CO5                            |



| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE1563.1</b>  | Describe the satellite orbits and trajectories with the definitions of parameters associated with satellites.                   |
| <b>21ECE1563.2</b>  | Apply the electronic hardware systems associated with the satellite subsystem and earth station.                                |
| <b>21ECE1563.3</b>  | Compute the satellite link parameters under various propagation conditions with the illustration of multiple access techniques. |
| <b>21ECE1563.4</b>  | Describe the various applications of satellites with the focus on national satellite system.                                    |
| <b>21ECE1563.5</b>  | Describe the fundamentals and applications of remote sensing, weather forecasting and navigation satellites.                    |
| <b>21ECE1563.6</b>  | Relate contextual knowledge to assess the solutions for real life applications of communication systems.                        |

| <b>Reference Books</b>   |  |
|--|--|
| 1. Anil K. Maini, Varsha Agrawal, Satellite Communications, Wiley India Pvt. Ltd., 2015, ISBN: 978-81-265-2071-8.<br>2. Dennis Roddy, Satellite Communications, 4 <sup>th</sup> Edition, McGraw- Hill International edition, 2006<br>3. Timothy Pratt, Charles Bostian, Jeremy Allnutt, Satellite Communications, 2 <sup>nd</sup> Edition, Wiley India Pvt. Ltd, 2017, ISBN: 978-81-265-0833-4 |  |

#### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to 50 M   |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

#### i) CIA: 50%

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

#### ii) SEA: 50%

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))

Semester: V

Course Name: Embedded System Design Using Raspberry Pi Course Code: 21ECE1564

|                    |            |                        |
|--------------------|------------|------------------------|
| L: T: P: J         | 3: 0: 0 :0 | CIA Marks: 50          |
| Credits:           | 3          | SEA Marks: 50          |
| Hours/Week (Total) | 3          | SEA Duration: 03 Hours |

Pre-Requisites: Microprocessor/Microcontroller, Python Basics.

Course Learning Objectives: The students will be able to

|   |  |
|---|--|
| 1 | Understand the basic hardware components and their selection method based on the characteristics and attributes of an embedded system. |
| 2 | Gain the knowledge of knowledge of hardware software co-design and firmware approaches.  |
| 3 | Understand the basics of python programming for Raspberry Pi board.  |
| 4 | Understand the working principle of Raspberry Pi board and interfacing peripherals.  |
| 5 | Understand the fundamental aspects of Raspberry Pi interfacing with different cloud services.  |

| Module-1: Embedded System Components   | No. of Hours | Blooms Cognitive Levels/CO Mapping |
|--|--------------|------------------------------------|
| Introduction, Embedded vs General computing system, Classification of Embedded systems, Major applications and purpose of ES, Elements of an Embedded system (Block diagram and explanation), Differences between RISC and CISC, Harvard and Princeton, Big and Little Endian formats, Memory (ROM and RAM types), Sensors, Actuators, Optocoupler, Communication Interfaces (I2C, SPI, IrDA, Bluetooth, Wi-Fi, Zigbee). | 8            | Understand CO1                     |
| <b>Module-2: Embedded System Design Concepts</b>   |              |                                    |
| Characteristics and quality attributes of Embedded Systems, Operational and non-operational quality attributes, Embedded Systems-Application and Domain specific, Hardware Software co-design and Program modeling (excluding UML), Embedded firmware design and development (excluding C language).   | 8            | Apply CO2                          |
| <b>Module-3: Basics of Python Programming</b>  |              |                                    |
| Python Fundamentals, Variables, Data types, Operators, Flow Control Loop statements and Exception Handling in Python, <b>Functions:</b> Creation of functions, passing parameters and return values, <b>Strings:</b> String Manipulation, String methods, Lists, Tuples and Dictionary in Python.  | 8            | Apply CO3                          |
| <b>Module-4: Introduction to Raspberry Pi and Interfacing Peripherals</b>  |              |                                    |
| Introduction to Raspberry Pi architecture, Pin details, technical specifications, Interfacing Raspberry Pi to sensors and output devices: LED, Buzzer, LDR, IR/PIR, DHT11 sensors, Ultrasonic sensors, Interfacing LCD display.  | 8            | Apply CO4                          |
| <b>Module-5: Raspberry Pi Cloud Interface</b>  |              |                                    |

|   |   |              |
|---|---|--------------|
| Introduction to Thingspeak, Communication using HTTP, Communication using MQTT protocol, Communication using SMTP protocol, Controlling Raspberry Pi peripherals with Flask Programming, Cloud data visualization and analysis. | 8 | Apply<br>CO5 |
|---|---|--------------|

**Course Outcomes: After completing the course, the students will be able to**

|                    |   |
|--------------------|---|
| <b>21ECE1564.1</b> | Classify and analyze the different hardware components of Embedded systems. |
| <b>21ECE1564.2</b> | Develop the hardware software co-design and firmware design approaches.     |
| <b>21ECE1564.3</b> | Apply the fundamentals of python programming for Raspberry Pi board.        |
| <b>21ECE1564.4</b> | Design and Development of Raspberry Pi based Embedded applications.         |
| <b>21ECE1564.5</b> | Development of Raspberry Pi based cloud services.                           |
| <b>21ECE1564.6</b> | Apply and analyze the various applications of Embedded systems.             |

**Reference Books**

1. Shibu K V, "Introduction to Embedded Systems", Tata McGraw Hill Education, 2<sup>nd</sup> Edition, 2000.
2. Charles R. Severance, "Python for Everybody: Exploring data using Python 3", Shroff publishers, 2017.
3. Simon Monk, "Raspberry Pi Cookbook", O'Reilly Media, Inc, 2014.
4. Volker Ziemann, "A Hands-on course in sensors using Arduino and Raspberry Pi, CRC Press, 2018.
5. Colin Dow, "Internet of Thing: Programming Projects-Build modern IoT solutions with Raspberry Pi3 and Python", Packtpub, 2018.

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to 50 M   |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

**i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA: 50%**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks.<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI   |  |                        |                                    |
|--|--|------------------------|------------------------------------|
| Course Name: Engineering Project Management and Finance  |  |                        | Course Code: 21ECE161              |
| L: T: P:   | 2 : 0 : 0 : 0  | CIA Marks: 50          |                                    |
| Credits:   | 2  | SEA Marks: 50          |                                    |
| Hours/Week (Total)   | 2 (25)   | SEA Duration: 03 Hours |                                    |
| Pre-Requisites:  |  |                        |                                    |
| Course Learning Objectives: The students will be able to   |  |                        |                                    |
| 1  | To familiarize the students with basic concepts of project management.             |                        |                                    |
| 2  | To understand risk management and perform technical analysis of market and demand. |                        |                                    |
| 3  | To evaluate the multiple project with constraints and project financing.           |                        |                                    |
| 4  | To familiarize students with the concept of cost of capital and its relevance.     |                        |                                    |
| 5  | To provide a basic understanding of financial analysis.                            |                        |                                    |
|  |  |                        |                                    |
| Module-1: Project Management   |  | No. of Hours           | Blooms Cognitive Levels/CO Mapping |
| Structure of projects, phases of project management-planning, scheduling, controlling phase, work breakdown structure, project control charts, network planning, PERT & CPM, Network components & precedence relationships, critical path analysis, probability in PERT analysis, Theory of crashing (Theory Only), Theory of Constraints (Theory only). |  | 5                      | Apply CO1                          |
| Module-2: Project Risk Management  |  |                        |                                    |
| Risk Management: Definition, classification of Risk factors, Risk identification process, qualitative and quantitative risk analysis, quantitative risk analysis tools<br>Case study: Challenging Engineering and Technology Projects  |  | 5                      | Analyse CO2                        |
| Module-3: Project financing  |  |                        |                                    |
| Multiple projects and constraints: Constraints, methods of ranking, mathematical programming approach, linear programming model<br><br>Qualitative Analysis: Qualitative factors in capital budgeting, strategic aspects, strategic planning and financial analysis, informational asymmetry and capital budgeting, and organizational considerations.   |  | 5                      | Analyse CO3                        |
| Module-4: Cost of Capital  |  |                        |                                    |
| Cost of Capital: Cost of debenture capital; Cost of preferential capital; Cost of term loans; cost of equity capital - Dividend discounting and CAPM   |  | 5                      | Apply CO4                          |

|  |          |                            |
|--|----------|----------------------------|
| model; Cost of retained earnings; Determination of Weighted average cost of capital (WACC) and Marginal cost of capital (Problems on WACC)   |          |                            |
| <b>Module-5: Financial Analysis</b>  |          |                            |
| <b>Financial Analysis:</b> Estimation of cost of project and means of financing, estimates of sales and production, cost of production, working capital requirement and its financing, estimates of working results, breakeven points, projected cash flow statement, projected balance sheet, make or buy decision. | <b>5</b> | <b>Analyse CO5 and CO6</b> |

**Course Outcomes: After completing the course, the students will be able to**

|                   |  |
|-------------------|--|
| <b>21ECE161.1</b> | Apply basic concepts of project management                                   |
| <b>21ECE161.2</b> | Understand risk management and perform market and demand analysis            |
| <b>21ECE161.3</b> | Understand project financing and evaluate multiple projects with constraints |
| <b>21ECE161.4</b> | Appreciate different sources of financing and understand the cost of capital |
| <b>21ECE161.5</b> | Understand the basic concepts of financial analysis                          |
| <b>21ECE161.6</b> | Understand and analyze project cash flow                                     |

**Reference Books**

1. Project Management for Engineering and Technology, David L. Goetsch, Pearson, 2015.
2. Project Planning: Analysis, Selection, Implementation and Review – Prasanna Chandra, 7/e, TMH, 2011.
3. Financial Management: Text, Problems and Cases, Khan M. Y. & Jain P. K, TMH, 8/e, 2019.
4. Financial Management, Prasanna Chandra, TMH, 9/e, 2017.
5. Project Management for Business and Technology: Principles and Practice – Nicholas, John M., 2/e, Pearson.
6. Project Management and Control – Narendra Singh, HPH, 2003.
7. Principles of Corporate Finance, Brealey, Myers, Allen & Mohanty, McGraw Hill Education, 11/e, 2014.
8. Cases in Financial Management, I. M. Pandey & Ramesh Bhat, McGraw Hill Education, 3/e, 2015
9. Project Management: The Managerial Process – Gray & Larson, 4/e, TMH, 2011.

### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

#### i) CIA: 50%

|  |                           |
|--|---------------------------|
| IA Test: 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| Assignment   | 10 Marks                  |
| Additional Assessment Tools (AAT) – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>   | <b>50 M</b>               |

#### ii) SEA : 50%

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI   |  |                        |                                    |
|--|--|------------------------|------------------------------------|
| Course Name: Microwave and Antennas  |  | Course Code: 21ECE162  |                                    |
| L: T:P: J  | 3:0:2:0  | CIA Marks: 50          |                                    |
| Credits:   | 4  | SEA Marks: 50          |                                    |
| Hours/week (Total)   | 5 (50)   | SEA Duration: 03 Hours |                                    |
| Pre-Requisites: Electromagnetic waves and transmission lines fundamentals  |  |                        |                                    |
| Course Learning Objectives: The students will be able to   |  |                        |                                    |
| 1  | Apply the knowledge of fields and waves to develop concepts of transmission line theory.                             |                        |                                    |
| 2  | Describe the basic operation of microwave devices.   |                        |                                    |
| 3  | Describe the radiation from isolated, linear wire antennas and from linear elements near or on a conducting surface. |                        |                                    |
| 4  | Calculate the fundamental parameters for antennas and the radiation field from an antenna.                           |                        |                                    |
| Module-1: Microwave Waveguides & Sources   |  |                        |                                    |
| <b>Microwave Waveguides:</b> Introduction, TE, TM waves Rectangular waveguides (qualitative analysis TE, TM modes), group velocity phase velocity, and wave impedance, Microwave cavities, resonant frequency.   |  | No. of Hrs             | Blooms Cognitive Levels/CO Mapping |
| <b>Microwave Sources:</b> Klystron Oscillator, Magnetron, TWT amplifiers.  |  |                        |                                    |
|  |  | 10                     | Apply CO1                          |
| Module-2: S- Parameters & Microwave Passive Devices  |  |                        |                                    |
| <b>S-parameters:</b> Introduction, properties of S matrix  |  | 10                     | Apply CO2                          |
| <b>Microwave Passive Devices:</b> Waveguide Tee's, Directional couplers, circulators, power divider, Faraday Isolator, Phase Shifters (Rotatory type), Attenuators (Rotatory type).  |  |                        |                                    |
| Module-3: Antenna Basics & Electric Dipoles  |  |                        |                                    |
| <b>Antenna Basics:</b> Introduction, antenna radiation mechanism, basic Antenna parameters, patterns, beam area, radiation intensity, beam efficiency, Directivity and gain, antenna apertures, effective height, bandwidth, radiation, efficiency, antenna temperature and antenna field zones. |  | 10                     | Apply CO3                          |
| <b>Electric dipoles:</b> Introduction, short electric dipole (Directivity, radiation resistance).  |  |                        |                                    |
| Module-4: Point Sources & Thin linear Antenna  |  |                        |                                    |



|   |           |              |
|---|-----------|--------------|
| <b>Point Sources:</b> Introduction, Point Sources, Power Theorem, Arrays of two isotropic point sources, Linear Arrays of n Isotropic Point Sources of equal Amplitude and Spacing.<br><b>Thin Linear Antenna:</b> Directivity and Radiation Resistance | <b>10</b> | Apply<br>CO4 |
| <b>Module-5: Antenna Types</b>  |           |              |
| Loop Antenna, Horn Antenna, Parabolic Antenna, Helical Antenna, Yagi- Uda Antenna, Log Periodic Antenna, Reflector antenna, Microstrip Patch Antenna.   | <b>10</b> | Apply<br>CO5 |

| <b>Practical Experiments</b> |  |
|------------------------------|--|
| <b>Sl. No</b>                | <b>Experiments</b>   |
| <b>1</b>                     | Measurement of frequency, guide wavelength, power, VSWR and attenuation in microwave test bench.                                 |
| <b>2</b>                     | Obtain the Radiation Pattern and Measurement of directivity and gain of microstrip dipole and Yagi antennas.                     |
| <b>3</b>                     | Determination of Coupling and isolation characteristics of microstrip directional coupler.                                       |
| <b>4</b>                     | Determination of Resonance characteristics of microstrip ring resonator and computation of dielectric constant of the substrate. |
| <b>5</b>                     | Determination of Power division and isolation of microstrip power divider.   |
| <b>6</b>                     | Simulate Broadside array, End-Fired array of Dipole Antenna and to plot the Radiation pattern.                                   |
| <b>7</b>                     | Simulate Linear array (Uniform) Antenna and plot the Radiation pattern   |
| <b>8</b>                     | Simulate Dipole Antenna and plot the Radiation pattern   |
| <b>9</b>                     | Simulate and calculate Phase and group velocity (X- band) waveguide at 9GHz  |
| <b>10</b>                    | Simulate Rectangular Waveguide propagation modes.  |

**Course Outcomes: After completing the course, the students will be able to**

|                   |   |
|-------------------|---|
| <b>21ECE162.1</b> | Develop generation and propagation of RF signals using Microwave oscillators through transmission line.                         |
| <b>21ECE162.2</b> | Compute the performance parameters and S-Matrix of microwave passive devices by applying the network/field concepts.            |
| <b>21ECE162.3</b> | Determine various antenna parameters for building an RF system  |
| <b>21ECE162.4</b> | Develop expressions for field intensity of a given antenna / an array of antennas. (Point sources, dipole, thin linear antenna) |
| <b>21ECE162.5</b> | Select suitable antenna configuration according to specific applications.   |
| <b>21ECE162.6</b> | Illustrate the benefits and hazards of microwave radiation to human health, environment, and society.                           |

**Reference Books**

1. Microwave Engineering, David M Pozar, 4th Edition, 2011, John Wiley, ISBN: 978-0-470-63155-3
2. Antenna Theory and Design, C A Balanis, 3rd Edition, 2005, John Wiley & sons, Inc. publication, ISBN-13: 978-0471667827
3. Foundations of Microwave Engineering, R E Collin, 2009, 2nd Edition, IEEE Press on Electromagnetic and Wave Theory, ISBN-13: 978-0-7803-6031-0
4. Computational Electromagnetics with MATLAB, Matthew N.O. Sadiku, 2019, Taylor & Francis Group, ISBN: 13: 978-1-138-55815-1

**Marks Distribution for Assessment:**

| PCL        | CIA | SEA | CIA (50)         |   |    |     | SEA   |
|------------|-----|-----|------------------|---|----|-----|---|
|            |     |     |                  | I   | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30  | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 marks scaled down to 20 marks |    |     |   |
|            |     |     | Assignment       | Average of 2 Assignments – 10M                            |    |     |   |
|            |     |     | Practical        | Weekly Assessment – 10 Marks<br>IA test – 10 Marks        |    |     |   |
|            |     |     | Total – 50 Marks |   |    |     | Total – 50 Marks  |

**i) CIA: 50%**

|        |  |                                |
|--------|--|--------------------------------|
| Theory | <b>IA Test</b> (Theory): 3 IA tests - each of 30 Marks<br><b>Assignment</b> : 2 Assignments – each of 10 marks | Average of 3 tests<br>30 Marks |
| Lab    | <b>Weekly Assessment</b> – 10 Marks<br><b>Practical test (1)</b> - 10 marks                                    | 20 Marks                       |
| Total  |  | <b>50 Marks</b>                |

**ii) SEA : 50%**  
**Question Paper:**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer, each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| Total              |  | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| <b>Semester: VI</b>  |   |   |
|--|---|---|
| <b>Course Name: VLSI Design</b>  |   | <b>Course Code: 21ECE163</b>              |
| <b>L: T: P: J</b>  | <b>3:0:2:0</b>  | <b>CIA Marks: 50</b>                      |
| <b>Credits:</b>  | <b>4</b>  | <b>SEA Marks: 50</b>                      |
| <b>Hours/Week (Total)</b>  | <b>5 (50)</b>   | <b>SEA Duration: 03 Hours</b>             |
| <b>Pre-Requisites: KVL &amp; KCL, MOSFET fundamentals, Digital electronics</b>   |   |   |
| <b>Course Learning Objectives: The students will be able to</b>  |   |   |
| 1  | Learn MOS transistor theory and CMOS technologies                                   |   |
| 2  | Learn the operation principles and analysis of inverter and logic circuits          |   |
| 3  | Design combinational, sequential and dynamic logic circuits as per the requirements |   |
| 4  | Design memory – SRAM, DRAM, ROM   |   |
| 5  | Demonstrate the concepts of Static Timing Analysis and CMOS testing                 |   |
| <b>Module-1: CMOS Logic Fundamentals</b>   |   | <b>No. of Hours</b>                       |
| Brief History, VLSI Design Flow, MOS Transistors – V-I Characteristics, Non-Ideal characteristics, CMOS Logic – Inverter DC Characteristics. Different Logic gates by truth table  |   | <b>Blooms Cognitive Levels/CO Mapping</b> |
|  |   | <b>Understand CO1</b>                     |
| <b>Module-2: CMOS Fabrication and CMOS Delays</b>  |   |   |
| CMOS Fabrication and lay out, Layout design rules, Scaling - Constant voltage, Constant field, MOSFET Capacitances without derivations, Transient Characteristics of Inverter, RC Delay, Linear Delay model.   |   | <b>10</b>                                 |
|  |   | <b>Apply CO2</b>                          |
| <b>Module-3: Combinational Logic circuits</b>  |   |   |
| Logical effort of paths and transistor sizing<br>Combinational logic design – Circuit families, - Static, Ratioed, CVSL, Dynamic logic, - Comparison of Performance parameters   |   | <b>10</b>                                 |
|  |   | <b>Apply CO3</b>                          |
| <b>Module-4: Sequential logic circuits and Semiconductor memories</b>  |   |   |
| <b>Sequential logic circuits</b> – Sequencing methods and timing, Latches and flipflops<br><b>Semiconductor Memories</b> – Memory architecture, SRAM – 6T and 8T and 10T SRAM, DRAM – 1T and 3T  |   | <b>10</b>                                 |
|  |   | <b>Apply CO4</b>                          |
| <b>Module-5: STA And Verification</b>  |   |   |
| <b>STA Concepts</b> – Timing arcs, Maximum and minimum timing path, Critical path, Clock domain crossing.<br><b>Verification</b> – Logic Verification principles, Testing – Manufacturing Test Principles, Design for Testability, Built in Self-test, MBIST |   | <b>10</b>                                 |
|  |   | <b>Analyse CO5</b>                        |

| <b>Lab Experiments</b> |   |
|------------------------|---|
| <b>Sl. No.</b>         | <b>NOTE: EDA tools with Custom circuit design flow and RTL Design flow to be used</b> |
| <b>1.</b>              | I- V Characteristics of n- MOSFET and p – MOSFET                                      |
| <b>2.</b>              | Inverter Characteristics – Pre-layout   |
| <b>3.</b>              | Inverter – Post layout simulation   |
| <b>4.</b>              | CMOS NAND gate – Design, Pre and Post layout simulation                               |

|     |  |
|-----|--|
| 5.  | 4 Bit adder – Timing analysis, Slack calculation   |
| 6.  | 4 Bit ALU - Timing analysis, Slack calculation   |
| 7.  | 4 Bit Up- down counter - Timing analysis, Slack calculation  |
| 8.  | 6T SRAM – Characterization   |
| 9.  | Estimation of Path delay and Setup and Hold time analysis for any RTL with predefined clock frequency. |
| 10. | Insert Scan chain for a given RTL and analyze.   |

| Course Outcomes: After completing the course, the students will be able to |   |
|--|---|
| 21ECE163.1   | Demonstrate understanding of MOS transistor theory, CMOS fabrication flow and technology scaling                            |
| 21ECE163.2   | Design the basic gates using the stick and layout diagrams for physical design and estimate sheet resistance and delays.    |
| 21ECE163.3   | Analyze logic delay and path delay based on logic effort and path effort.   |
| 21ECE163.4   | Analyze timing issues with latches and flipflops  |
| 21ECE163.5   | Analyze timing consideration in Memory elements, Verification methodologies and Testing issues in VLSI Design.              |
| 21ECE163.6   | Analyze an RTL design with timing and power constraints and bring up the physical design for the chosen RTL with EDA tools. |

| Reference Books |  |
|-----------------|--|
| 1.              | <b>CMOS VLSI Design-</b> A Circuits and Systems Perspective, Neil H.E.& Weste, David Harris, Ayan Banerjee, Pearson Education, 4 <sup>th</sup> Edition, 2011                             |
| 2.              | <b>CMOS Digital Integrated Circuits: Analysis and Design</b> - Sung Mo Kang & Yosuf Leblebici, Third Edition, Tata McGraw-Hill. 2003   |
| 3.              | <b>Static Timing Analysis for Nanometer Designs:</b> A Practical Approach, J. Bhasker, R Chadha, Springer, 2009  |
| 4.              | <b>Microelectronics Circuits Theory and Applications</b> , Adel Sedra and K. C. Smith, 6 <sup>th</sup> or 7 <sup>th</sup> Edition, Oxford University Press, International Version, 2009. |
| 5.              | <b>Basic VLSI Design</b> , Douglas A Pucknell & Kamran Eshragian,, PHI 3rd Edition, (original Edition – 1994).   |

#### Marks Distribution for Assessment:

| PCL        | CIA | SEA | CIA (50)         |   |    |     | SEA   |
|------------|-----|-----|------------------|---|----|-----|---|
|            |     |     |                  | I   | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30  | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 marks scaled down to 20 marks |    |     |   |
|            |     |     | Assignment       | Average of 2 Assignments – 10M                            |    |     |   |
|            |     |     | Practical        | Weekly Assessment – 10 Marks<br>IA test – 10 Marks        |    |     |   |
|            |     |     | Total – 50 Marks |   |    |     | Total – 50 Marks  |

i) **CIA: 50%**

|              |  |                                |
|--------------|--|--------------------------------|
| Theory       | <b>IA Test (Theory):</b> 3 IA tests - each of 30 Marks<br><b>Assignment :</b> 2 Assignments – each of 10 marks | Average of 3 tests<br>30 Marks |
| Lab          | <b>Weekly Assessment</b> – 10 Marks<br><b>Practical test (1)</b> - 10 marks                                    | 20 Marks                       |
| <b>Total</b> |  | <b>50 Marks</b>                |

ii) **SEA : 50%**

**Question Paper:**

|                    |  |   |
|--------------------|--|---|
| <b>Theory Exam</b> | 5 questions to answer, each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |  | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |  |   |
|---|--|---|
| <b>Course Name: Java Programming and its Applications</b>   |  | <b>Course Code: 21ECE164</b>              |
| <b>L: T: P: J</b>   | <b>0: 0 : 2 : 2</b>  | <b>CIA Marks: 50</b>                      |
| <b>Credits:</b>   | <b>2</b>   | <b>SEA Marks: 50</b>                      |
| <b>Hours/Week (Total)</b>   | <b>4 (25)</b>  | <b>SEA Duration: 03 Hours</b>             |
| <b>Pre-Requisites:</b> Basics of C and C++ language, Students should be familiarized about java installation and setting the java environment, Usage of IDEs like Eclipse/ Netbeans should be introduced.   |  |   |
| <b>Course Learning Objectives: The students will be able to</b>   |  |   |
| 1   | To introduce the use of Eclipse/Netbeans IDE to create Java Applications.        |   |
| 2   | Reinforce the understanding of basic object-oriented programming concepts.       |   |
| 3   | Create multi-threaded programs and event handling mechanism.                     |   |
| 4   | To make the students understand life cycle of the applets and its functionality. |   |
| 5   | Using java programming to develop programs for solving real-world problems.      |   |
|   |  | <b>No. of Hours</b>                       |
|   |  | <b>Blooms Cognitive Levels/CO Mapping</b> |
| <b>Module-1: Introduction to Java</b>   |  |   |
| Introduction to Java: Features of OOP, Characteristics/Buzz words of Java, Java Environment: JDK, JVM, JRE, Fundamental Programming Structure in Java, Variables, Data Types, Operators & Expressions, Control Statements, Iteration Statements, Command Line Arguments.<br><b>Programs:</b><br>1. Write a java program that prints all real solutions to the quadratic equation $ax^2+bx+c=0$ . Read in a, b, c and use the quadratic formula.<br>2. Write a program to check prime number<br>3. Write a program for Arithmetic calculator using switch case menu  |  | <b>5</b>                                  |
|   |  | <b>Apply CO1</b>                          |
| <b>Module-2: Classes &amp; Objects</b>  |  |   |
| Classes & Objects: Defining Classes & Objects, Access Specifiers, Constructors, Overloading Constructor, Method Overloading, Passing and Returning object form Method, new operator, finalize() method, this keyword, Static Keyword, Encapsulation, Polymorphism.<br>Array and String: Single and Multidimensional Array, Definition of String, String Literals, String Class, String Inbuilt Methods, StringBuffer & StringBuilder Class, Use of Wrapper class.<br><b>Programs:</b><br>4. Create a Java class called Student with the following details as variables within it. USN Name Branch Phone Write a Java program to create n Student objects and print the USN, Name, Branch, and Phone of these objects with suitable headings.<br>5. Design a super class called <b>Staff</b> with details as StaffId, Name, Phone, Salary. Extend this class by writing three subclasses namely Teaching (domain, publications), Technical (skills), and Contract (period). Write a Java program to read and display at least 3 staff objects of all three categories.<br>6. Write a java program demonstrating Method overloading and Constructor |  | <b>5</b>                                  |
|   |  | <b>Apply CO2</b>                          |

|   |   |           |
|---|---|-----------|
| overloading.  |   |           |
| <b>Module-3: Inheritance, Interfaces &amp; Packages.</b>  |   |           |
| <p>Inheritance: Defining an Inheritance, Types of Inheritance, Constructor in subclass, Method Overriding, super keyword, abstract keyword, final keyword.</p> <p>Interfaces &amp; Packages: Defining an Interface, Implementing an Interface, Difference between Interface &amp; Classes, Extending a Interface, Usage of Package, Classpath, Importing a Package.</p> <p><b>Programs:</b></p> <p>7. Write a program to generate the resume. Create 2 Java classes Teacher (data: personal information, qualification, experience, achievements) and Student (data: personal information, result, discipline) which implements the java interface Resume with the method biodata ().</p> <p>8. Develop a java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, miles to KM and vice versa), time converter (hours to minutes, seconds and viceversa) using packages</p> | 5 | Apply CO3 |
| <b>Module-4: Multithreading &amp; IO Programming</b>  |   |           |
| <p>Multithreading: Multi-Threaded Programming: What are threads? How to make the classes threadable; Extending threads; Implementing runnable; Synchronization.</p> <p>IO Programming: Introduction to Stream, Byte Stream, Character stream, Readers and Writers, File Class, File InputStream, File Output Stream, InputStreamReader.</p> <p><b>Programs:</b></p> <p>9. Write a Java program that implements a multi-thread application that has three threads. First thread generates a random integer for every 1 second; second thread computes the square of the number and prints; third thread will print the value of cube of the number.</p>  | 5 | Apply CO4 |
| <b>Module-5: Exceptions, Collections</b>  |   |           |
| <p>Exceptions: Definition of Exception, Classification of Exception, Structure of Try &amp; catch block, Error Vs Exception, Throw Keyword, Throws Keyword, Finally Keyword, Custom Exception.</p> <p>Collections: Collections Overview, Iterators, Collection Interfaces: List: ArrayList, Linked List &amp; Vector, Set: Hashset, Linked Hashset, Map: Hashmap, Linked Hashmap, &amp; Hash table. Comparator &amp; Comparable Interface.</p> <p><b>Programs:</b></p> <p>10. Write a Java program to read two integers a and b. Compute a/b and print, when b is not zero. Raise an exception when b is equal to zero.</p> <p>11. Write functions for the following</p> <ol style="list-style-type: none"> <li>Append - add at end</li> <li>Insert – add at particular index</li> <li>Search</li> <li>List all string starts with given letter</li> </ol>  | 5 | Apply CO5 |
| <b>List of Sample Projects</b>  |   |           |
| <ol style="list-style-type: none"> <li>Airline Reservation System</li> <li>Electricity Billing System</li> <li>Library Management System</li> <li>Online Bank Management System</li> <li>e-Healthcare Management System</li> <li>Online Quiz Management System</li> <li>Stock Management System</li> <li>Weather Report Application</li> <li>Telephone Billing System</li> </ol>  |   |           |

## 10. Currency Converter

### Course Outcomes: After completing the course, the students will be able to

|                   |  |
|-------------------|--|
| <b>21ECE164.1</b> | Use Eclipse/NetBeans IDE to design, develop, debug Java Projects   |
| <b>21ECE164.2</b> | Analyze the necessity for Object Oriented Programming paradigm over structured programming and become familiar with the fundamental concepts in OOP. |
| <b>21ECE164.3</b> | Demonstrate the ability to design and develop java programs, analyze, and interpret object oriented data and document results                        |
| <b>21ECE164.4</b> | Apply the concepts of exception/event handling, abstraction to develop robust programs.  |
| <b>21ECE164.5</b> | Develop user friendly applications using GUI concepts  |
| <b>21ECE164.6</b> | Develop a Project using JAVA using the concepts  |

### Reference Books

1. E Balagurusamy, Programming with Java, Graw Hill, 6th Edition, 2019.
2. Herbert Schildt, C: Java the Complete Reference, McGraw Hill, 11th Edition, 2020
3. Core Java Volume-I Fundamentals Horstmann & Cornell, - Pearson Education. - Eight Edition
4. Head First Java: A Brain-Friendly Guide, 2nd Edition- Kathy Sierra, Bert Bates

### Marks Distribution for Assessment:

| PBL        | CIA | SEA | CIA(50)   |  |       | SEA<br>Conduction: 100 M<br>Reduced to 50 M              |
|------------|-----|-----|-----------|--|-------|--|
| Conduction | 50  | 50  | Theory    | I IA   | II IA | Project<br>Assessed for 100 marks<br>reduced to 50 marks |
|            |     |     |           | 30   | 30    |  |
|            |     |     |           | Average of 2 Tests-30 marks  |       |  |
|            |     |     | Practical | Weekly Assessment<br>(Record/Project)-10 Marks<br>Lab IA test-10 Marks |       |  |
|            |     |     |           | Total- 50 marks  |       |  |

i) CIA : 50 %

|   |                 |
|---|-----------------|
| <b>Theory – 2 IA tests- Each of 30 Marks</b>    | <b>30 Marks</b> |
| <b>Practical</b>                                |                 |
| Weekly Assessment- Lab Record/Project- 10 Marks | 20 Marks        |
| Lab IA Test-10 Marks                            |                 |
| <b>Total</b>                                    | <b>50 M</b>     |

i) SEA : 50 %

|                |   |  |
|----------------|---|--|
| <b>Project</b> | <b>Write up- 10 Marks</b><br><b>Project Report- 25 Marks</b><br><b>Presentation &amp; Demonstration- 50 Marks</b><br><b>Viva-Voce- 15 Marks</b> | <b>100 Marks</b><br><b>reduced to 50</b><br><b>Marks</b> |
| <b>Total</b>   |   | <b>50 Marks</b>  |



# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI   |  |                        |                                    |
|--|--|------------------------|------------------------------------|
| Course Name: Information Theory and Coding   |  | Course Code: 21ECE1651 |                                    |
| L: T: P: J   | 3 : 0 : 0 : 0  | CIA Marks: 50          |                                    |
| Credits:   | 3  | SEA Marks: 50          |                                    |
| Hours/Week (Total)   | 3 hours/week (40)  | SEA Duration: 03 Hours |                                    |
| Pre-Requisites: Set theory, Discrete mathematics, Probability theory and Statistics  |  |                        |                                    |
| Course Learning Objectives: The students will be able to   |  |                        |                                    |
| 1  | Understand the concept of Entropy, Rate of information and order of the source with reference to dependent and independent source. |                        |                                    |
| 2  | Study various source encoding algorithms.  |                        |                                    |
| 3  | Model discrete & continuous communication channels.  |                        |                                    |
| 4  | Study Various Error Control Coding Algorithms  |                        |                                    |
|  |  |                        |                                    |
| Module 1: Information Theory   |  | No. of Hours           | Blooms Cognitive Levels/CO Mapping |
| Introduction: Block Diagram for Digital Communication, Measure of information, Information content of message, Average Information content of symbols in Long Independent sequences, Markov Statistical Model of Information Sources, Average Information content of symbols in Long dependent sequences, Entropy of Markoff Sources, Information rate of Markoff Sources  |  | 08                     | Apply CO1                          |
| Module 2: Source Coding  |  |                        |                                    |
| Encoding of the Source Output, Shannon's Encoding Algorithm, Shannon Fano Encoding Algorithm, Source coding theorem, Prefix codes, Kraft McMillan Inequality property – KMI, Huffman Codes & Extended Huffman coding   |  | 08                     | Apply CO2                          |
| Module 3: Discrete Information Channels  |  |                        |                                    |
| Introduction to Discrete Communication Channels, Channel Matrix, Joint probability Matrix, Binary Symmetric Channel, System Entropies, Mutual Information, Channel Capacity, Channel Capacity of Binary Symmetric Channel and Binary Erasure Channel   |  | 08                     | Apply CO3                          |
| Module 4: Error Control Coding   |  |                        |                                    |
| Introduction to Error Control Coding, Examples, Methods of Controlling Errors, Types of Errors, Types of Codes, Linear Block Codes: matrix description of Linear Block Codes, Error Detection and Error Correction Capabilities of Linear Block Codes, Single Error Correcting Hamming Codes. Binary Cyclic Codes: Algebraic Structure of Cyclic Codes, Systematic and Non Systematic form, Encoding using an (n-k) Bit Shift register, Syndrome Calculation, Error Detection and Correction |  | 08                     | Apply CO4                          |
| Module 5: Convolutional Codes  |  |                        |                                    |
| Convolution Encoder, Time domain approach, Transform domain approach, State Diagram, Code Tree, Trellis Diagram, The Viterbi Algorithm.  |  | 08                     | Apply CO5                          |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1651.1</b>  | Calculate Symbol rate, Self-Information, Entropy and Information Rate as a measure of Information for memory less and dependent sources. |
| <b>21ECE1651.2</b>  | Develop efficient representation of data generated by discrete information source.   |
| <b>21ECE1651.3</b>  | Analyze discrete channels using joint, conditional, and mutual entropies of variables in terms of their coupled probabilities.           |
| <b>21ECE1651.4</b>  | Develop reliable codes for data on imperfect communication channels.   |
| <b>21ECE1651.5</b>  | Apply concept of convolutional codes to carry out encoding and decoding operations.  |
| <b>21ECE1651.6</b>  | Relate the basics of Information Theory & coding to find solutions for practical problems in terms of storage and secured communication  |

| <b>Reference Books</b>   |
|--|
| 1. Digital and Analog communication systems, K. Sam Shanmugam, John Wiley India Pvt. Ltd, 1996.<br>2. Digital communication, Simon Haykin, John Wiley India Pvt. Ltd, 2008.<br>3. ITC and Cryptography, Ranjan Bose, TMH, II Edition, 2007.<br>4. Principles of digital communication, J. Das, S. K. Mullick, P. K. Chatterjee Wiley Technology & Engineering, 1986.<br>5. Digital Communications – Fundamentals and Applications, Bernard Sklar, Pearson Education, Second Edition, 2016, ISBN:9780134724058.<br>6. Information Theory and Coding, Hari Bhat, Ganesh Rao, Cengage, 2017.<br>7. Error Correction Coding Todd K Moon Wiley Std., Edition, 2006. |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |                  | SEA<br>Conduction: 100 M<br>Reduced to 50 M   |
|------------|-----|-----|------------------|-----------------------------------|----|------------------|---|
|            |     |     |                  | I                                 | II | III              |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30               | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |                  |   |
|            |     |     | Assignment       | 10                                |    |                  |   |
|            |     |     | AAT              | 10                                |    |                  |   |
|            |     |     | Total – 50 marks |                                   |    | Total – 50 marks |   |

i) **CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |  |   |
|---|--|---|
| Course Name: Nanoelectronics  |  | Course Code: 21ECE1652                    |
| L: T: P: J  | 3: 0:0:0   | CIA Marks: 50                             |
| Credits:  | 3  | SEA Marks: 50                             |
| Hours/Week (Total)  | 3 (40)   | SEA Duration: 03 Hours                    |
| Prerequisites:  |  |   |
| Course Learning Objectives: The students will be able to  |  |   |
| 1   | Describe nanotechnology with basic fabrication methods for nanostructures. |   |
| 2   | Describe the classification of characterization methods.                   |   |
| 3   | Describe the various fabrication techniques and physical processes.        |   |
| 4   | Discuss the applications of semiconductor nanostructures                   |   |
| <b>Module-1: Introduction</b>   |  | <b>No. of Hours</b>                       |
|   |  | <b>Blooms Cognitive Levels/CO Mapping</b> |
| <b>Introduction:</b> Overview of nanoscience and engineering. Development milestones in microfabrication and electronic industry. Moore's law and continued miniaturization, Classification of Nanostructures, Electronic properties of atoms and solids: Isolated atom, Bonding between atoms, Giant molecular solids, Free electron models and energy bands, crystalline solids, Periodicity of crystal lattices, electronic conduction.  |  | 8   |
|   |  | <b>Understand CO1</b>                     |
| <b>Module-2: Fabrication methods and techniques</b>   |  |   |
| <b>Fabrication methods:</b> Top-down processes, Bottom up processes methods for templating the growth of nanomaterials, ordering of nanosystems.<br><b>Fabrication techniques:</b> requirements of ideal semiconductor, epitaxial growth of quantum wells, lithography and etching, cleaved-edge over growth, growth of vicinal substrates, strain induced dots and wires, electrostatically induced dots and wires, Quantum well width fluctuations, thermally annealed quantum wells, semiconductor nanocrystals, colloidal quantum dots, self-assembly techniques. (Text 1). |  | 8   |
|   |  | <b>Understand CO2</b>                     |
| <b>Module-3: Characterization</b>   |  |   |
| <b>Characterization:</b> General considerations for imaging, Image magnification and resolution, other considerations for imaging, Light microscopy, Classification, Microscopic techniques, Field ion microscopy, scanning probe techniques, diffraction techniques: bulk and surface diffraction techniques.<br>The characterization of semiconductor nanostructures-Optical and electrical characterization, Structural characterization.  |  | 8   |
|   |  | <b>Understand CO3</b>                     |
| <b>Module-4: Inorganic semiconductor nanostructures</b>   |  |   |

|   |   |          |                           |
|---|---|----------|---------------------------|
| <b>Inorganic semiconductor nanostructures:</b> overview of semiconductor physics. Quantum confinement in semiconductor nanostructures: quantum wells, quantum wires, quantum dots, super-lattices, band offsets, electronic density of states, Modulation doping, The quantum Hall effect, Resonant tunnelling, Charging effects. |   | <b>8</b> | <b>Understand<br/>CO4</b> |
| <b>Module-5: Applications of semiconductor nanostructures</b>   |   |          |                           |
| Applications of semiconductor nanostructures: Injection lasers, quantum cascade lasers, single-photon sources, biological tagging, optical memories, coulomb blockade devices, photonic structures.   |   | <b>8</b> | <b>Understand<br/>CO5</b> |
| <b>Course Outcomes: After completing the course, the students will be able to</b>   |   |          |                           |
| <b>21ECE1652.1</b>  | Explain the overview and classification of nanostructures.                                  |          |                           |
| <b>21ECE1652.2</b>  | Explain the top-down and bottom-up fabrication methods and fabrication techniques involved. |          |                           |
| <b>21ECE1652.3</b>  | Explain Image magnification and microscopic techniques used in characterization.            |          |                           |
| <b>21ECE1652.4</b>  | Explain the Inorganic semiconductor nanostructures with doping and charge effects.          |          |                           |
| <b>21ECE1652.5</b>  | Explain the applications of nano sensors, injection lasers                                  |          |                           |
| <b>21ECE1652.6</b>  | Analyze the effects of nanotechnology applications  |          |                           |

|   |  |
|---|--|
| <b>Reference Books</b>  |  |
| <ol style="list-style-type: none"> <li>1. Ed Robert Kelsall, Ian Hamley, Mark Geoghegan, "Nanoscale Science and Technology", John Wiley, 2007.</li> <li>2. Charles P Poole, Jr, Frank J Owens, "Introduction to Nanotechnology", John Wiley, Copyright 2006, Reprint 2011.</li> <li>3. T Pradeep, "Nano: The Essentials-Understanding Nanoscience and Nanotechnology", TMH.</li> <li>4. Ed William A Goddard III, Donald W Brenner, Sergey E. Lyshevski, Gerald J Iafrate, "Hand Book of Nanoscience Engineering and Technology", CRC press, 2003.</li> </ol> |  |

#### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

i) **CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |   |                        |                                    |
|---|---|------------------------|------------------------------------|
| Course Name: Wearable Technology  |   | Course Code: 21ECE1653 |                                    |
| L: T: P: J  | 3 :0 :0 :0  | CIA Marks: 50          |                                    |
| Credits:  | 3   | SEA Marks: 50          |                                    |
| Hours/Week (Total)  | 3 (40)  | SEA Duration: 03 Hours |                                    |
| Prerequisites:  |   |                        |                                    |
| Course Learning Objectives: The students will be able to  |   |                        |                                    |
| 1   | Identify and understand the need for development of wearable devices and its influence on various sectors.      |                        |                                    |
| 2   | To familiarize the characteristics, working principle and application of special purpose transducers            |                        |                                    |
| 3   | To develop skillset to implement IoT systems for wearable applications.   |                        |                                    |
| 4   | To introduce the concept of the reactive sensors and self-generating sensors and its applications in real life. |                        |                                    |
| 5   | To provide a basic understanding of evolution of IoT and its functional modules.                                |                        |                                    |
|   |   |                        |                                    |
| Module-1: Wearables: Fundamentals, advancements and roadmap for the future  |   | No. of Hours           | Blooms Cognitive Levels/CO Mapping |
| World of Wearables, Role of Wearables, Attributes of Wearables, Textiles and clothing: The meta-wearable, Challenges and opportunities.<br>Wearing sensors for disease detection: introduction, cardiovascular diseases, neurological diseases, gastrointestinal diseases.  |   | 8                      | Understand CO1                     |
| Module-2: Smart Fabrics   |   |                        |                                    |
| Introduction. Sensor design, physiological basis and sensor placement, electrical contacts and interconnections for smart garments. Textile integration and design of functional garments, functional evaluation<br>Wearables for Life in Space: Introduction, life aboard the ISS, wearables for life in a protected environment, the extra vehicular activity in the space, life on Moon and Mars |   | 8                      | Understand CO2                     |
| Module-3: Pressure and Flow Sensors   |   |                        |                                    |
| Concepts of Pressure, Units of Pressure, Mercury Pressure sensors, Bellows, membranes and thin plates, Piezoresistive sensors, capacitance sensors, VRP sensors, optoelectronic pressure sensors, indirect pressure sensor, vacuum sensors.<br>Basics of flow dynamics, thermal transport sensors, ultrasonic sensors, electromagnetic sensors, breeze sensor, Dust and smoke detectors             |   | 8                      | Understand CO3                     |
| Module-4: Power and Communication   |   |                        |                                    |
| Powering and data communication<br>RF energy harvesting fundamentals and practical limitations, impedance mismatch, losses, efficiency, charge pump rectifier topologies.   |   | 8                      | Understand CO4                     |
| Module-5: Wearables to THINKables: Data Analytics and Machine Learning  |   |                        |                                    |
| Remote health monitoring using wearable sensors, AI enabled sensors, challenges of AI-enabled sensors in health, future directions<br>Data analytics for wearable IoT based telemedicine: introduction, need and  |   | 8                      | Understand CO5                     |

|   |  |  |
|---|--|--|
| demand of wearables technologies in the society, smart glove design, signal processing pipeline: from sensor signals to classifications |  |  |
| <b>Course Outcomes: After completing the course, the students will be able to</b>   |  |  |
| <b>21ECE1653.1</b>  | Identify and understand the need for development of wearable devices and its influence on various sectors.                                     |  |
| <b>21ECE1653.2</b>  | Understand the working principle of special purpose sensors and the need for developing smart sensors  |  |
| <b>21ECE1653.3</b>  | To identify the real-world problem and give IoT solutions and to analyze and select appropriate protocols, wireless techniques for the problem |  |
| <b>21ECE1653.4</b>  | Demonstrate the concept of resistive sensors which can be employed for real life applications  |  |
| <b>21ECE1653.5</b>  | Design and develop IoT end points for wearable applications.   |  |
| <b>21ECE1653.6</b>  | Able to design and perform experiments on the sensors and develop the projects based on the customer needs.                                    |  |

| Reference Books   |  |
|---|--|
| <ol style="list-style-type: none"> <li>1. Jacob Fraden, "Hand Book of Modern Sensors: physics, Designs and Applications", 3rd ed., Springer, 2010.</li> <li>2. Edward Sazonov, Michael R Neuman, "Wearable Sensors: Fundamentals, Implementation and Applications" Elsevier, 2014</li> <li>3. Toshiyo Tamura, Wenxi Chen, "Seamless Healthcare Monitoring Advancements in Wearable, Attachable, and Invisible Devices". Springer International Publishing, 2017. Daniel J. Inman, Shashank Priya "Energy Harvesting Technologies", Springer US, 2008</li> <li>4. Simone Cirani, Gianluigi Ferrari, Marco Picone, Luca Veltri "Internet of Things: Architectures, Protocols and Standards", Wiley, 2018</li> <li>5. "Environmental, Chemical and Medical Sensors", by Shantanu Bhattacharya, A K Agarwal, Nripen Chanda, Ashok Pandey and Ashis Kumar Sen, Springer Nature Singapore Pte Ltd. 2018</li> <li>6. M. Mardonova and Y. Choi, "Review of Wearable Device Technology and Its Applications to the Mining Industry," Energies, vol. 11, p. 547, 2018.</li> </ol> |  |

#### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |



i) **CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| <b>Semester: VI</b>   |   |   |
|---|---|---|
| <b>Course Name: Artificial Neural Network</b>   |   | <b>Course Code: 21ECE1654</b>             |
| <b>L: T: P: J</b>   | <b>3 : 0 : 0 : 0</b>  | <b>CIA Marks: 50</b>                      |
| <b>Credits:</b>   | <b>3</b>  | <b>SEA Marks: 50</b>                      |
| <b>Hours/Week (Total)</b>   | <b>3 (40)</b>   | <b>SEA Duration: 03 Hours</b>             |
| <b>Pre-Requisites:</b> Basic knowledge of calculus, linear algebra, probability theory and programming  |   |   |
| <b>Course Learning Objectives: The students will be able to</b>   |   |   |
| 1   | Understand the basics of ANN and comparison with Human brain  |   |
| 2   | Demonstrate knowledge on Generalization and function approximation and various architectures of building an ANN |   |
| 3   | Get knowledge of supervised, unsupervised and reinforcement learning using neural networks                      |   |
| <b>Module-1: Introduction to Neural Networks</b>  |   | <b>Blooms Cognitive Levels/CO Mapping</b> |
| <b>Introduction:</b> Biological Neuron – Artificial Neural Model - Types of activation functions – Architecture: Feedforward and Feedback, Convex Sets, Convex Hull and Linear Separability, Non-Linear Separable Problem. Xor Problem, Multilayer Networks.<br><br><b>Learning:</b> Learning Algorithms, Error correction and Gradient Descent Rules, Learning objective of TLNs, Perceptron Learning Algorithm, Perceptron Convergence Theorem. |   | <b>8</b><br><br><b>Apply CO1</b>          |
| <b>Module-2: Supervised Learning</b>  |   |   |
| <b>Supervised Learning:</b> Perceptron learning and Non Separable sets, $\alpha$ -Least Mean Square Learning, MSE Error surface, Steepest Descent Search, $\mu$ -LMS approximate to gradient descent, Application of LMS to Noise Cancelling, Multi-layered Network Architecture, Backpropagation Learning Algorithm, Practical consideration of BP algorithm.  |   | <b>8</b><br><br><b>Apply CO2</b>          |
| <b>Module-3: Support Vector Machines</b>  |   |   |
| <b>Support Vector Machines and Radial Basis Function:</b> Learning from Examples, Statistical Learning Theory, Support Vector Machines, SVM application to Image Classification, Radial Basis Function Regularization theory, Generalized RBF Networks, Learning in RBFNs, RBF application to face recognition.   |   | <b>8</b><br><br><b>Apply CO3</b>          |
| <b>Module-4: Attractor Neural Networks</b>  |   |   |
| <b>Attractor Neural Networks:</b> Associative Learning Attractor Associative Memory, Linear Associative memory, Hopfield Network, application of Hopfield Network, Brain State in a Box neural Network, Simulated Annealing, Boltzmann Machine, Bidirectional Associative Memory.   |   | <b>8</b><br><br><b>Apply CO4</b>          |
| <b>Module-5: Self-Organisation of Feature Maps</b>  |   |   |
| <b>Self-organization Feature Map:</b> Maximal Eigenvector Filtering, Extracting Principal Components, Generalized Learning Laws, Vector   |   | <b>8</b><br><br><b>Apply CO5</b>          |

|   |  |  |
|---|--|--|
| Quantization, Self-organization Feature Maps, Application of SOM, Growing Neural Gas. |  |  |
|---|--|--|

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1654.1</b>  | Understand artificial neural model and its architectures.  |
| <b>21ECE1654.2</b>  | Apply steepest descent, LMS algorithm and Backpropagation algorithm  |
| <b>21ECE1654.3</b>  | Apply support vector machines to classify images.  |
| <b>21ECE1654.4</b>  | Understand attractor neural networks and its applications.   |
| <b>21ECE1654.5</b>  | Apply self-organization feature maps.  |
| <b>21ECE1654.6</b>  | Understand the role of neural networks in engineering, artificial intelligence, and cognitive modelling and be able to apply the concepts of ANN to real world applications. |

| <b>Reference Books</b>   |  |
|--|--|
| 1. <b>Neural Networks A Classroom Approach</b> – Satish Kumar, McGraw Hill Education (India) Pvt. Ltd, Second Edition.<br>2. <b>Introduction to Artificial Neural Systems</b> -J.M. Zurada, Jaico Publications, 1994.<br>3. <b>Artificial Neural Networks</b> -B. Yegnanarayana, PHI, New Delhi 1998 |  |

#### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

#### i) CIA: 50%

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |  |                                    |
|---|--|------------------------------------|
| Course Name: Computer Architecture and Organization   |  | Course Code:21ECE1655              |
| L: T: P: J  | 3 : 0 : 0 : 0  | CIA Marks: 50                      |
| Credits:  | 3  | SEA Marks: 50                      |
| Hours/Week (Total)  | 3 (40)   | SEA Duration: 03 Hours             |
| Pre-Requisites: Digital Logic solving, Number System  |  |                                    |
| Course Learning Objectives: The students will be able to  |  |                                    |
| 1   | Explain the basic sub systems of a computer, their organization, structure and operation |                                    |
| 2   | Illustrate the concept of programs as sequences of machine instructions                  |                                    |
| 3   | Demonstrate different ways of communicating with I/O devices                             |                                    |
| 4   | Describe memory hierarchy and concept of virtual memory                                  |                                    |
| 5   | Illustrate organization of simple pipelined processor and other computing systems        |                                    |
|   |  |                                    |
| Module 1: Introduction  |  | No. of Hours                       |
|   |  | Blooms Cognitive Levels/CO Mapping |
| Basic Structure of Computers: Computer Types, Functional Units, Basic, Operational Concepts, Bus Structures, Software, Performance – Processor Clock, Basic Performance Equation<br>Machine Instructions and Programs: Numbers, Arithmetic Operations and Characters, IEEE standard for Floating point Numbers, Memory Location and Addresses, Memory Operations, Instructions and Instruction Sequencing |  | 8                                  |
|   |  | Understand CO1                     |
| Module-2: Addressing Modes  |  |                                    |
| Addressing Modes, Assembly Language, Basic Input and Output Operations, Stacks and Queues, Subroutines, Additional Instructions.  |  | 8                                  |
|   |  | Apply CO2                          |
| Module-3: IO Organisation   |  |                                    |
| Input/Output Organization: Accessing I/O Devices, Interrupts – Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Controlling Device Requests, Direct Memory Access  |  | 8                                  |
|   |  | Apply CO3                          |
| Module-4: Memory System   |  |                                    |
| Memory System: Basic Concepts, Semiconductor RAM Memories- Internal organization of memory chips, Static memories, Asynchronous DRAMS, Read Only Memories, Cash Memories, Virtual Memories, Secondary Storage-Magnetic Hard Disks   |  | 8                                  |
|   |  | Apply CO4                          |
| Module-5: Basic Processing Unit   |  |                                    |
| Basic Processing Unit: Some Fundamental Concepts, Execution of a Complete Instruction, Multiple Bus Organization, Hardwired Control, Microprogrammed Control  |  | 8                                  |
|   |  | Understand CO5                     |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE1655.1</b>  | Explain the basic organization of a computer system.  |
| <b>21ECE1655.2</b>  | Explain the different addressing modes and assembly language instructions.                            |
| <b>21ECE1655.3</b>  | Explain different ways of accessing an input / output device including interrupts.                    |
| <b>21ECE1655.4</b>  | Illustrate the organization of different types of semiconductor and other secondary storage memories. |
| <b>21ECE1655.5</b>  | Illustrate simple processor organization based on hardwired control and micro programmed control.     |
| <b>21ECE1655.6</b>  | Analyze the architecture and performance issues in different processor families.                      |

| <b>Reference Books</b>  |  |
|---|--|
| <ol style="list-style-type: none"> <li>1. Carl Hamacher, Zvonko Vranesic, Safwat Zaky: Computer Organization, 5th Edition, Tata McGraw Hill, 2002.</li> <li>2. David A. Patterson, John L. Hennessy: Computer Organization and Design – The Hardware / Software Interface ARM Edition, 4th Edition, Elsevier, 2009.</li> <li>3. William Stallings: Computer Organization &amp; Architecture, 7th Edition, PHI, 2006.</li> <li>4. Vincent P. Heuring &amp; Harry F. Jordan: Computer Systems Design and Architecture, 2nd Edition, Pearson Education, 2004.</li> </ol> |  |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

i) **CIA: 50%**

|   |                                  |
|---|----------------------------------|
| <b>IA Test: 3 IA tests - Each of 30 Marks</b>   | <b>Average of 3 tests – 30 M</b> |
| <b>Assignment</b>   | <b>10 Marks</b>                  |
| <b>Additional Assessment Tools (AAT) – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses</b> | <b>10 Marks</b>                  |
| <b>Total</b>  | <b>50 M</b>                      |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI   |  |                                    |
|--|--|------------------------------------|
| Course Name : Strategic Management   |  | Course Code: 21ECE1656             |
| L: T: P:   | 3 : 0 : 0 : 0  | CIA Marks: 50                      |
| Credits:   | 3  | SEA Marks: 50                      |
| Hours/Week (Total)   | 3 (40)   | SEA Duration: 03 Hours             |
| Pre-Requisites:  |  |                                    |
| Course Learning Objectives: The students will be able to   |  |                                    |
| 1  | To provide a framework for students to understand strategic management concepts and conduct external analysis for competitive advantage. |                                    |
| 2  | To help students develop a thorough understanding of principles and models related to an organization's internal analysis.               |                                    |
| 3  | To help students understand the different strategy options available for organizations in a complex and dynamic environment.             |                                    |
| 4  | To acquaint students with essential factors in strategy implementation.  |                                    |
| 5  | To provide a basic understanding of how to establish and exert strategic control.  |                                    |
|  |  |                                    |
| Module-1: Introduction to Strategic Management and External Analysis   |  | No. of Hours                       |
| Meaning and Characteristics of Strategic Management; The Strategic Management Process.<br><b>External Analysis</b><br>Strategically Relevant Components of a Company's External Environment – PESTLE analysis, Environment Threat and Opportunity Profile (ETOP); Industry Analysis –Porter's Dominant Economic Features, Porter's Five Forces Model, Entry and Exit Barriers, Strategic Group Mapping; Industry Key Success Factors, Key Performance Indicators and Key Result Areas. |  | 8                                  |
| Module-2: Internal Analysis  |  | Blooms Cognitive Levels/CO Mapping |
| Strategic Vision, Mission, Goals, Long-Term and Short-Term Objectives and their Value to the Strategic Management Process; Organizational Capability Profile –Resource Based View of the firm (RBV) and VRIN; Business Portfolio Analysis – BCG / Growth Share Matrix, GE 9 Cell Model; Balanced Score Card, SWOC Analysis, Value Chain Analysis, Benchmarking.  |  | 8                                  |
| Module-3: Strategy Formulation   |  |                                    |
| Business Strategies: Porter's Generic Strategies – Low Cost, Differentiation, Best Cost, Focused Low Cost and Focused Differentiation<br>Corporate Strategies: Growth Strategies – Internal Growth, External Growth (Integration, Diversification, Mergers, Joint Ventures, Strategic Alliances), Product/Market Expansion grid / Ansoff's Matrix; Stability Strategies – No-Change, Profit and Proceed with Caution; Retrenchment   |  | 8                                  |



|   |   |                              |
|---|---|------------------------------|
| Strategies – Turnaround, Divestment and Liquidation; International Business Level Strategies.   |   |                              |
| <b>Module-4: Strategy Implementation</b>  |   |                              |
| Facilitators for implementation of strategy: Organisational Structures – matching structure to strategy, McKinsey's 7S, Changing structure and processes (Business Process Reengineering, Six Sigma); Strategic Leadership; Organisational Culture – Learning organisations, MBO, TQM; Barriers to implementation of strategy.<br><br>Strategy and Innovation: Introduction to Innovation – Process, Product and Platform; Creative Destruction and Disruptive Technologies; Open Innovation and Open Strategy. | 8 | Apply<br>CO4                 |
| <b>Module-5: Strategic Control</b>  |   |                              |
| Focus of Strategic Control, Establishing Strategic Controls (Premise Control, Strategic Surveillance, Special Alert Control, Implementation Control), Exerting Strategic Control (through Competitive Benchmarking, Performance and Formal and Informal Organisations).<br><br>Blue Ocean Strategy: Difference between blue & red ocean strategies, principles of blue ocean strategy.  | 8 | Understand<br>CO5 and<br>CO6 |

**Course Outcomes: After completing the course, the students will be able to**

|                    |   |
|--------------------|---|
| <b>21ECE1656.1</b> | Understand strategic management concepts and how to conduct external analysis for competitive advantage.                |
| <b>21ECE1656.2</b> | Apply selected models of internal analysis to evaluate an organization.   |
| <b>21ECE1656.3</b> | Understand and analyze the different strategy options available for organizations in a complex and dynamic environment. |
| <b>21ECE1656.4</b> | Appreciate the essential factors in strategy implementation.  |
| <b>21ECE1656.5</b> | Understand how to establish and exert strategic control.  |
| <b>21ECE1656.6</b> | Understand and analyze blue and red ocean strategies crafted and executed by organizations.                             |

**Reference Books**

1. Arthur A. Thompson Jr., Margaret A. Peteraf, John E. Gamble, A. J. Strickland III, Arun K. Jain, Crafting and Executing Strategy: The Quest for Competitive Advantage – Concepts and Cases, McGraw Hill Education, 19<sup>th</sup> Edition, 2017.
2. Robert M Grant, Contemporary Strategy Analysis, Wiley, 11<sup>th</sup> Edition, 2021.
3. Michael A. Hitt, R. Duane Ireland, Robert E. Hoskisson, S. Manikuttu, Strategic Management: A South-Asian Perspective, Cengage Learning, 9<sup>th</sup> Edition, 2016.
4. Stewart Clegg, Chris Carter, Marting Kornberger, Jochen Schweitzer, Strategy: Theory & Practice, Sage Publications, 3<sup>rd</sup> Edition, 2020.
5. John Parnell, Strategy Management: Theory & Practice, Biztantra, 2004.
6. John A. Pearce, Richard B. Robinson, Strategic Management: Planning for Domestic and Global Competition, McGraw Hill Education, 14<sup>th</sup> Edition, 2015.

### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

#### i) CIA: 50%

|  |                           |
|--|---------------------------|
| IA Test: 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| Assignment   | 10 Marks                  |
| Additional Assessment Tools (AAT) – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>   | <b>50 M</b>               |

#### ii) SEA : 50%

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| <b>Semester: VI</b>  |  |   |
|--|--|---|
| <b>Course Name:</b>  | <b>Nanotechnology</b>  | <b>Course Code: 21ECE1671</b>             |
| <b>L: T: P: J</b>  | <b>3:0:0:0</b>   | <b>CIA Marks: 50</b>                      |
| <b>Credits:</b>  | <b>3</b>   | <b>SEA Marks: 50</b>                      |
| <b>Hours/Week (Total)</b>  | <b>3 (40)</b>  | <b>SEA Duration: 03 Hours</b>             |
| <b>Prerequisites:</b>  |  |   |
| <b>Course Learning Objectives: The students will be able to</b>  |  |   |
| 1  | Understand basics of nanomaterials and their properties.                           |   |
| 2  | Describe synthesis of nanomaterials by chemical techniques.                        |   |
| 3  | Learn to analyze and assess parameters involved in synthesis and characterization. |   |
| 4  | Compare models involved in synthesis of nanostructures.                            |   |
| <b>Module-1: Introduction</b>  |  | <b>No. of Hours</b>                       |
|  |  | <b>Blooms Cognitive Levels/CO Mapping</b> |
| <b>Introduction:</b> introduction to nanoscience and nanotechnologies, importance and scope of nanotechnology, Development milestones in microfabrication and electronic industry. Moore's law and continued miniaturization, natural nanomaterials, properties at nanoscale (physical, chemical, surface, electrical, magnetic, optical, mechanical), Classification of Nanostructures, Kinetics in Nanostructured Materials.                                       |  | <b>8</b>                                  |
|  |  | <b>Understand CO1</b>                     |
| <b>Module-2: Types of Nanomaterials and synthesis</b>  |  |   |
| Types of Nanomaterials (Quantum dots, Nanoparticles, Nanocrystals, Dendrimers, Buckyballs, Nanotubes); Synthesis of Nanomaterials- top down and bottom up approach, Ball Milling, Gas, liquid, and solid –phase synthesis of nanomaterials; Lithography techniques (Photolithography, Dip-pen and Electron beam lithography); Thin film deposition; Electrospinning. Bio-synthesis of nanomaterials.   |  | <b>8</b>                                  |
|  |  | <b>Apply CO2</b>                          |
| <b>Module-3: Characterization of Nano materials</b>  |  |   |
| Microscopy-Scanning tunnelling microscope, Atomic force microscope, scanning electron microscopy, Field Emission Scanning Electron Microscopy, transmission electron microscopy, Environmental Scanning Electron Microscopy (ESEM) High Resolution Transmission Electron Microscope (HRTEM), Surface enhanced Raman Spectroscopy, X-ray diffraction technique, X ray Photoelectron Spectroscopy Surface area analysis, particle size analysis, gravimetric analysis. |  | <b>8</b>                                  |
|  |  | <b>Apply CO3</b>                          |
| <b>Module-4: Nano Structures</b>   |  |   |
| Carbon Nanotubes, Fullerenes, Nanowires, Quantum Dots. Applications of nanostructures. Reinforcement in Ceramics, Drug delivery, Giant magnetoresistance, etc. Cells response to Nanostructures.   |  | <b>8</b>                                  |
|  |  | <b>Apply CO4</b>                          |
| <b>Module-5: Application of Nanotechnology</b>   |  |   |

|  |   |                           |
|--|---|---------------------------|
| Nano electronics, Nano sensors, Nanotechnology in Diagnostics applications, Environmental and Agricultural Applications of nanotechnology, Nano technology for energy systems. | 8 | <b>Understand<br/>CO5</b> |
|--|---|---------------------------|

**Course Outcomes: After completing the course, the students will be able to**

|                    |   |
|--------------------|---|
| <b>21ECE1671.1</b> | Identify various nano materials and describe the basic science behind the properties of materials.  |
| <b>21ECE1671.2</b> | Explain the types and methods of nanomaterial synthesis.  |
| <b>21ECE1671.3</b> | Interpret the creation and characterization of nanoscale materials.                                 |
| <b>21ECE1671.4</b> | Apply principles of nano materials in describing nanostructures.                                    |
| <b>21ECE1671.5</b> | Comprehend the applications of nanotechnology at the leading edge of scientific research            |
| <b>21ECE1671.6</b> | Apply their knowledge of nanotechnology to identify how they can be exploited for new applications. |

**Reference Books**

1. Textbook of Nanoscience and Nanotechnology, Pradeep T, 2012, Tata McGraw Hill Education Pvt. Ltd. ISBN: 9781259007323.
2. Nano-structured Materials and Nanotechnology, Hari Singh Nalwa, 2002, Gulf Professional Publishing, Academic Press, ISBN:0-12-513920-9
3. Nanomaterials, Nanotechnologies and Design: An Introduction to Engineers and Architects, D. Michael Ashby, Paulo Ferreira, Daniel L. Schodek, Butterworth-Heinemann, 2009. Springer
4. Handbook of Nanotechnology by Bharat Bhushan 2004.

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

i) **CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |   |                                    |
|---|---|------------------------------------|
| Course Name: Wearable Devices   |   | Course Code: 21ECE1672             |
| L: T: P: J  | 3 :0 :0 :0  | CIA Marks: 50                      |
| Credits:  | 3   | SEA Marks: 50                      |
| Hours/Week (Total)  | 3 (40)  | SEA Duration: 03 Hours             |
| Pre-Requisites:   |   |                                    |
| Course Learning Objectives: The students will be able to  |   |                                    |
| 1   | Understand and Identify the need for development of wearable devices and its influence on various sectors.  |                                    |
| 2   | To provide the basic understanding of measurement and instrumentation systems and the insight of the resistive sensors and its applications in real life.                                       |                                    |
| 3   | To familiarize the characteristics, working principle and application of special purpose transducers  |                                    |
| 4   | Acquaint the usage of wearable devices as assistive devices, diagnostic devices and other modern applications.  |                                    |
| 5   | To impart the importance of smart sensors, sensor interface standards for wearable device applications and to provide a brief overview of the wearable technology and its impact on social life |                                    |
|   |   |                                    |
| Module-1: Wearables: Fundamentals, advancements, and roadmap for the future   |   | No. of Hours                       |
|   |   | Blooms Cognitive Levels/CO Mapping |
| World of Wearables, Role of Wearables, Attributes of Wearables, Textiles and clothing: The meta-wearable, Challenges and opportunities.<br>Wearing sensors for disease detection: introduction, cardiovascular diseases, neurological diseases, gastrointestinal diseases   |   | 08                                 |
|   |   | Understand CO1                     |
| Module-2: Sensors, Actuators and low-power electronics  |   |                                    |
| Mechanical sensors, Biochemical sensors, tears, saliva, wound and interstitial fluids. Biopotential signals and their characteristics, electrode-body interface and electrode noise, Low-power ADCs for biomedical applications, architectural design for low power biopotential acquisition.   |   | 08                                 |
|   |   | Understand CO2                     |
| Module-3: Pressure and Flow Sensors   |   |                                    |
| Concepts of Pressure, Units of Pressure, Mercury Pressure sensors, Bellows, membranes and thin plates, Piezoresistive sensors, capacitance sensors, VRP sensors, optoelectronic pressure sensors, indirect pressure sensor, vacuum sensors.<br>Basics of flow dynamics, thermal transport sensors, ultrasonic sensors, electromagnetic sensors, breeze sensor, Dust and smoke detectors |   | 08                                 |
|   |   | Understand CO3                     |
| Module-4: Smart Fabrics   |   |                                    |

|  |    |                   |
|--|----|-------------------|
| Introduction. Sensor design, physiological basis and sensor placement, electrical contacts and interconnections for smart garments. Textile integration and design of functional garments, functional evaluation, Woven electronic textile applications  | 08 | Understand<br>CO4 |
| <b>Module-5: Wearables to THINKables: Data Analytics and Machine Learning</b>  |    |                   |
| Remote health monitoring using wearable sensors, AI enabled sensors, challenges of AI-enabled sensors in health, future directions<br>Data analytics for wearable IoT based telemedicine: introduction, need and demand of wearables technologies in the society, smart glove design, signal processing pipeline: from sensor signals to classifications | 08 | Understand<br>CO5 |

**Course Outcomes: After completing the course, the students will be able to**

|                    |   |
|--------------------|---|
| <b>21ECE1672.1</b> | Identify and understand the need for development of wearable devices and its influence on various sectors.  |
| <b>21ECE1672.2</b> | Gain the basic idea of measurements, characteristics and the errors associated with measurements  |
| <b>21ECE1672.3</b> | Understand the working principle of special purpose sensors and the need for developing smart sensors   |
| <b>21ECE1672.4</b> | Acquaint the usage of wearable devices as assistive devices, diagnostic devices and other modern applications.  |
| <b>21ECE1672.5</b> | Design and develop various wearable devices for detection of biochemical and physiological body signals, environmental monitoring, safety and navigational assistive devices. |
| <b>21ECE1672.6</b> | Able to design and perform experiments on the sensors and develop the projects based on the customer needs.   |

**Reference Books**

1. Jacob Fraden, "Hand Book of Modern Sensors: physics, Designs and Applications", 3rd ed., Springer, 2010.
2. Edward Sazonov, Michael R Neuman, "Wearable Sensors: Fundamentals, Implementation and Applications" Elsevier, 2014
3. Toshiyo Tamura, Wenxi Chen, "Seamless Healthcare Monitoring Advancements in Wearable, Attachable, and Invisible Devices". Springer International Publishing, 2017. "Wearable Electronics Sensors - For Safe and Healthy Living", Subhas Chandra Mukhopadhyay, Springer 2015 ECE(BSW) Page 37
4. "Environmental, Chemical and Medical Sensors", by Shantanu Bhattacharya, A K Agarwal, Nripen Chanda, Ashok Pandey and Ashis Kumar Sen, Springer Nature Singapore Pte Ltd. 2018
5. M. Mardonova and Y. Choi, "Review of Wearable Device Technology and Its Applications to the Mining Industry," Energies, vol. 11, p. 547, 2018.
6. N. Luo, W. Dai, C. Li, Z. Zhou, L. Lu, C. C. Y. Poon, et al., "Flexible Piezoresistive Sensor Patch Enabling Ultralow Power Cuffless Blood Pressure Measurement," Advanced Functional Materials, vol. 26, pp. 1178-1187, 2016.

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

**i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses



# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)**  
**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |  |                                    |
|---|--|------------------------------------|
| Course Name: Robotics and Automation  |  | Course Code: 21ECE1673             |
| L: T: P: J  | 3 : 0 : 0 : 0  | CIA Marks: 50                      |
| Credits:  | 3  | SEA Marks: 50                      |
| Hours/Week (Total)  | 3 (40)   | SEA Duration: 03 Hours             |
| Pre-Requisites:   |  |                                    |
| Course Learning Objectives: The students will be able to  |  |                                    |
| 1   | To study the various parts of robots and fields of robotics          |                                    |
| 2   | To study the electronics circuits used in robotic applications       |                                    |
| 3   | To study sensors used in robotics                                    |                                    |
| 4   | To study the programming aspects of robots for specific applications |                                    |
| 5   | To study the control of robots for some specific applications        |                                    |
|   |  |                                    |
| Module-1: Introduction  |  | No. of Hours                       |
|   |  | Blooms Cognitive Levels/CO Mapping |
| History, Robots, Robot Usage, Robot Subsystems, Classification of Robots, Industrial Applications   |  | 8                                  |
|   |  | Understand CO1                     |
| Module-2: Actuators and Grippers  |  |                                    |
| Electric Actuators, Hydraulic Actuators, Pneumatic Actuators, Selection of Motors, Grippers   |  | 8                                  |
|   |  | Understand CO2                     |
| Module-3: Sensors, Vision and Signal Conditioning   |  |                                    |
| Sensor Classification, Internal Sensors, External Sensors, Vision, Signal Conditioning, Sensor Selection  |  | 8                                  |
|   |  | Understand CO3                     |
| Module-4: Programming of Robots   |  |                                    |
| Robot Programming using MATLAB: robot programming workflow, Sensing and Perception, Path Planning and Decision, Control, Programming an Arduino Robot in Simulink, Line Follower Application for Arduino Robot  |  | 8                                  |
|   |  | Apply CO4                          |
| Module-5: Hardware interfacing of Robots  |  |                                    |
| Introduction to Arduino Uno, driver circuits, interfaces used in robotic applications, programming the Arduino for robotic applications. Case studies: Design and Implementation of:<br>1. Human Following Robot Using Arduino and Ultrasonic Sensor<br>2. Obstacle Avoiding Robot using Arduino, Servo Motors and Ultrasonic Sensor<br>3. Bluetooth based Smart Phone Controlled Robot Car<br>4. WiFi Controlled Robot |  | 8                                  |
|   |  | Apply CO5                          |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1673.1</b>  | Understand evolution and basics of robotic system.                     |
| <b>21ECE1673.2</b>  | Understand various actuators used in robotic applications.             |
| <b>21ECE1673.3</b>  | Understand the working of various sensors used in robotic applications |
| <b>21ECE1673.4</b>  | Understand the Robot programming and its languages                     |
| <b>21ECE1673.5</b>  | Interface hardware and software for building robots                    |
| <b>21ECE1673.6</b>  | Develop robots for societal applications                               |

| <b>Reference Books</b>   |
|--|
| <ol style="list-style-type: none"> <li>1. 'Industrial Robotics Technology, Programming and Applications', Mikell P. Groover, Mitchell Weiss, Roger N. Nagel and Nicholas G. Odrey, Mc Graw Hill Book company, 1986</li> <li>2. 'Industrial Robotics', Bernard Hodges, Jaico Publishing House, 1993</li> <li>3. 'Introduction to Robotics', 2e, S K Saha, Tata McGraw Hill Education Private Limited, 2008</li> </ol> |

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

**i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

ii) **SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

*B.N.M. Institute of Technology*

### An Autonomous Institution under VTU

**B.E. (Electronics and Communication Engineering)**

### Choice Based Credit System (CBCS and Outcome Based Education (OBE)

| Semester: VI  |   |                        |                                    |
|---|---|------------------------|------------------------------------|
| Course Name: Automotive Electronics   |   | Course Code: 21ECE1674 |                                    |
| L: T: P: J  | 3 : 0 :0 :0   | CIA Marks: 50          |                                    |
| Credits:  | 3   | SEA Marks: 50          |                                    |
| Hours/Week (Total)  | 3 (40)  | SEA Duration: 03 Hours |                                    |
| Pre-Requisites: Control Systems, Internet of Things, Electronic Circuits, Digital System Design   |   |                        |                                    |
| Course Learning Objectives: The students will be able to  |   |                        |                                    |
| 1   | Understand the basics of automobile dynamics and design electronics to complement those features. .                                       |                        |                                    |
| 2   | Understand principle of working of sensors and actuators used in automobiles for control  |                        |                                    |
| 3   | Design and implement the electronics that attribute the reliability, safety, and smartness to the automobiles, providing add-on comforts. |                        |                                    |
|   |   |                        |                                    |
| Module-1: Automotive Fundamentals Overview  |   | No. of Hours           | Blooms Cognitive Levels/CO Mapping |
| <b>Automotive Fundamentals Overview</b><br>Evolution of Automotive Electronics, Automobile Physical Configuration, Survey of Major Automotive Systems, The Engine - Engine Block, Cylinder Head, Four Stroke Cycle, Engine Control, Ignition System- Spark plug, High voltage circuit and distribution, Spark pulse generation, Ignition Timing, Diesel Engine, Drive Train - Transmission, Drive Shaft, Differential, Suspension, Brakes, Steering System, Starter Battery- Operating principle.<br><b>The Basics of Electronic Engine Control-</b><br>Motivation for Electronic Engine Control- Exhaust Emissions, Fuel Economy, Concept of an Electronic Engine control system, Definition of General terms, Definition of Engine performance terms, Engine mapping, Effect of Air/Fuel ratio, spark timing and EGR on performance, Control Strategy, Electronic Fuel control system, Analysis of intake manifold pressure, Electronic Ignition. |   | 8                      | Understand CO1                     |
| Module-2: Automotive Sensors  |   |                        |                                    |
| <b>Automotive Sensors</b><br>Automotive Control System applications of Sensors and Actuators - Variables to be measured, Airflow rate sensor, Strain Gauge MAP sensor, Engine Crankshaft Angular Position Sensor, Magnetic Reluctance Position Sensor, Hall effect Position Sensor, Shielded Field Sensor, Optical  |   | 8                      | Understand CO2                     |

|  |          |                       |
|--|----------|-----------------------|
| Crankshaft Position Sensor, Throttle Angle Sensor (TAS), Engine Coolant Temperature (ECT) Sensor, Exhaust Gas Oxygen (O <sub>2</sub> /EGO) Lambda Sensors, Piezoelectric Knock Sensor.   |          |                       |
| <b>Module-3: Digital Engine Control Systems</b>  |          |                       |
| <b>Digital Engine Control Systems</b><br>Digital Engine control features, Control modes for fuel Control (Seven Modes), EGR Control, Electronic Ignition Control -Closed loop Ignition timing, Spark Advance Correction Scheme, Integrated Engine Control System- Secondary Air Management, Evaporative Emissions Canister Purge, Automatic System Adjustment, System Diagnostics.   | <b>8</b> | <b>Understand CO3</b> |
| <b>Module-4: Automotive Networking</b>   |          |                       |
| <b>Automotive Networking -</b><br>Bus Systems- Classification, Applications in the vehicle, Coupling of networks, Examples of networked vehicles Buses - CAN Bus, UN Bus, MOST Bus, Bluetooth, Flex Ray, Diagnostic Interfaces.  | <b>8</b> | <b>Understand CO4</b> |
| <b>Module-5: Automotive Diagnostics</b>  |          |                       |
| <b>Automotive Diagnostics -</b><br>Timing Light, Engine Analyser, On-board diagnostics, Off-board diagnostics, Expert Systems, Occupant Protection Systems - Accelerometer based Air Bag systems.<br><b>Future Automotive Electronic Systems -</b><br>Alternative Fuel Engines, Electric and Hybrid vehicles, Fuel cell powered cars, Collision Avoidance Radar warning Systems, Low tire pressure warning system, Heads Up display, Speech Synthesis, Navigation - Navigation Sensors - Radio Navigation, Signpost navigation, dead reckoning navigation, Voice Recognition Cell Phone dialling, Advanced Cruise Control, Stability Augmentation, Automatic driving Control | <b>8</b> | <b>Understand CO5</b> |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1674.1</b>  | Acquire an overview of automotive components, subsystems, and basics of Electronic Engine Control in today's automotive industry                 |
| <b>21ECE1674.2</b>  | Understand the automotive sensors and actuators for interfacing with microcontrollers / microprocessors during automotive system design.         |
| <b>21ECE1674.3</b>  | Understand the fundamentals of digital engine control systems in today's automotive industry.  |
| <b>21ECE1674.4</b>  | Understand the networking of various modules in automotive systems, communication protocols and diagnostics of the sub systems.                  |
| <b>21ECE1674.5</b>  | Understand the importance of automotive diagnostics and get fair idea on future Automotive Electronic Systems                                    |
| <b>21ECE1674.6</b>  | Understanding the design of the electronics that attribute the reliability, safety, and smartness to the automobiles, providing add-on comforts. |

**Reference Books**

1. Understanding Automotive Electronics\_ William B. Ribbens\_ Elsevier Publishing\_6th Edition\_2003
2. Bosch Automotive Electrics and Automotive Electronics Systems and Components, Networking and Hybrid Drive\_ Robert Bosch Gmbh (Ed.)\_ John Wiley& Sons Inc\_5th edition\_2007

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

**i) CIA: 50%**

|   |                           |
|---|---------------------------|
| <b>IA Test:</b> 3 IA tests - Each of 30 Marks   | Average of 3 tests – 30 M |
| <b>Assignment</b>   | 10 Marks                  |
| <b>Additional Assessment Tools (AAT)</b> – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses | 10 Marks                  |
| <b>Total</b>  | <b>50 M</b>               |

**ii) SEA : 50%**

|                    |   |   |
|--------------------|---|---|
| <b>Theory Exam</b> | 5 questions to answer each of 20 Marks<br>2 questions from each module with internal choice<br>Student should answer one full question from each module | 20 M x 5 = <b>100 M</b><br><b>reduced to 50 M</b> |
| <b>Total</b>       |   | <b>50 Marks</b>                                   |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

**B.E. (Electronics and Communication Engineering)  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

| Semester: VI  |  |                        |
|---|--|------------------------|
| Course Name: Employability skills (Technical) -2  |  | Course Code: 21ECE168  |
| L: T: P:  | 0 : 0 : 2 : 0  | CIA Marks: 100         |
| Credits:  | 1  | SEA Marks: --          |
| Hours/Week (Total)  | 2 (25)   | SEA Duration: 03 Hours |
| Course Learning Objectives: The students will be able to  |  |                        |
| 1   | Understand fundamentals of trending technologies currently used in the industry.               |                        |
| 2   | Understand the importance of professional etiquettes.  |                        |
| 3   | Participate in group discussions and various modes of interviews.                              |                        |
| 4   | Solve company simulated aptitude and technical question papers related to campus recruitments. |                        |
|   |  |                        |
| Introductory Courses  |  | No. of Hours           |
| Data Science (Data Analytics & Visualization), Cyber Security, Industrial Automation 4.0, & IOT, AWS, & Cloud Computing   |  | 10                     |
| Personality & Grooming Training<br>Interview Preparation Training   |  |                        |
| Dressing & Group Discussion Etiquettes, Interview Skills, Resume Building(should include introduction to Github, Hackerrank, LeetCode, Codechef), Email & Telephone Etiquettes, Social Media Etiquettes, & LinkedIn Profiling.  |  | 6                      |
| <u>Pre-Preparation Formalities</u>  |  |                        |
| • Training session on Pre-Preparation formalities of Campus Selection should be conducted Job Profiles analysis must be done.   |  |                        |
| • Understanding the salary breakups & other perks, researching about the Company and the work culture through their websites & other digital platforms like Glassdoor & LinkedIn.   |  |                        |
| • Rewriting resumes keeping the job profiles in view.   |  |                        |
| <u>Group Discussion &amp; Personal Interview</u>  |  |                        |
| • Pre-Placement Talk, Mock GD & Personal Interview training sessions for each individual student should be conducted by the Industry Experts and they should brief students on the area of improvements, presentation & behavioral skills required during the campus selection process. |  |                        |
| Assessment Tests  |  |                        |

|   |   |
|---|---|
| Company Specific Aptitude and Technical Tests | 6 |
|---|---|

**Course Outcomes: After completing the course, the students will be able to**

|                   |  |
|-------------------|--|
| <b>21ECE168.1</b> | Analyze the problem and solve it within the allocated time span.                               |
| <b>21ECE168.2</b> | Apply the professional etiquettes during the recruitment drives.                               |
| <b>21ECE168.3</b> | Implement the techniques and skills during the group discussions and various interview skills. |

**Assessment process**

|           | Components                        | Description  | Marks |
|-----------|-----------------------------------|--|-------|
| CIA (100) | Continues Evaluation              | Students to be evaluated on:<br>1. Mock G.D.<br>2. Interview- Offline and Online<br>3. Resume  | 50    |
|           | Written / Online Test             | <ul style="list-style-type: none"> <li><b><u>Total Tests: 03</u></b> <ul style="list-style-type: none"> <li>➤ Assessments with 75 minutes duration &amp; 50 marks each</li> <li>➤ Average score of 50 Marks from 3 tests will be considered for the final score</li> </ul> </li> </ul> | 50    |
|           | <b>Total Marks for the Course</b> |  | 100   |
|           |                                   |  |       |



# *B N M Institute of Technology*

**Autonomous Engineering College Under VTU  
Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: VII**

**Course Name: Wireless Communication Technologies**

**Course Code: 21ECE171**

|                   |                  |                               |
|-------------------|------------------|-------------------------------|
| <b>L: T: P: J</b> | <b>3 :0 :0:0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>   | <b>3</b>         | <b>SEA Marks: 50</b>          |
| <b>Hours</b>      | <b>40</b>        | <b>SEA Duration: 03 Hours</b> |

**Course Learning Objectives: The students will be able to**

|   |  |
|---|--|
| 1 | To apply the concepts of Cellular System in capacity expansion techniques. |
| 2 | To understand the GSM and TDMA Technology .                                |
| 3 | To apply the concepts of OFDM in LTE.                                      |
| 4 | To familiarize the 5G Network architecture and technologies                |
| 5 | To understand network slicing in 5G and evolution towards 6G.              |
| 6 | To analyse the ad-hoc networks for real time wireless applications.        |

| <b>Module-1: Evolution and Cellular System Components</b>  | <b>No. of Hours</b> | <b>Blooms cognitive Levels</b> |
|--|---------------------|--------------------------------|
| Different generation of wireless cellular network, 1G, 2G, 2.5G,3G,4G and beyond, Common cellular network components, The Cellular Concept, Cell Fundamentals, Capacity Expansion techniques, Mobility Management  | <b>8</b>            | <b>Apply CO1</b>               |
| <b>Module-2: GSM ,TDMA and LTE Technology</b>  |                     |                                |
| Introduction to GSM and TDMA, GSM Network and System Architecture, GSM Channel Concept, GSM Identities, GSM System Operations, Key enabling technologies and features of LTE, LTE Network architecture   | <b>8</b>            | <b>Understand CO2</b>          |
| <b>Module-3: Multicarrier Modulation and LTE standard</b>  |                     |                                |
| Multicarrier basics, OFDM Basics, OFDM in LTE, Single carrier frequency domain equalization, Overview and channel structure of LTE: Design principles, Network architecture, Radio Interface protocols, Hierarchical channel structure of LTE, Logical channels, Transport channels, Physical Channels, Channel mapping                    | <b>8</b>            | <b>Understand CO3</b>          |
| <b>Module-4: 5G Overview and Architecture</b>  |                     |                                |
| 5G Overview, Characteristics of 5G, 4G Vs 5G, 5G System Architecture, 5G Deployment architecture, NG core, Network functions in NG core, Communication approach for Core Network Functions, Next Gen Radio Access Networks (NG-RAN), 5G New Radio (5G NR), Technologies accelerating 5G Radio, Small Cells                                 | <b>8</b>            | <b>Apply CO4</b>               |
| <b>Module-5: Network Slicing in 5G and Introduction to 6G</b>  |                     |                                |
| <b>6Network Slicing in 5G</b><br>What is network slicing, Requirements for network slicing, Network slicing management, Benefits of network slicing.<br><b>Introduction to 6G</b><br>Introduction, The societal impact of 6G, Trends and evolution towards 6G, 6G Requirements, The need for a new architecture, Architectural principles. | <b>8</b>            | <b>Understand CO5</b>          |

**Course Outcomes: After completing the course, the students will be able to**

|                   |   |
|-------------------|---|
| <b>21ECE171.1</b> | Apply the concepts of Cellular System in capacity expansion techniques. |
| <b>21ECE171.2</b> | Understand the GSM and TDMA Technology .                                |



# *B N M Institute of Technology*

**Autonomous Engineering College Under VTU  
Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: VII**

**Course Name: Fiber Optics Communication** **Course Code: 21ECE1721**

|                   |                  |                               |
|-------------------|------------------|-------------------------------|
| <b>L: T: P: J</b> | <b>3 :0 :0:0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>   | <b>3</b>         | <b>SEA Marks: 50</b>          |
| <b>Hours</b>      | <b>40</b>        | <b>SEA Duration: 03 Hours</b> |

**Pre-Requisites: Concepts of Analog and Digital Communication**

**Course Learning Objectives: The students will be able to**

- 1 Learn the basic principle of optical fiber communication with different modes of light propagation.
- 2 Understand the transmission characteristics and losses in optical fiber.
- 3 Study of optical components and its applications in optical communication networks.
- 4 Understand the Operational principles of WDM and Optical Components.
- 5 Understand the working of Optical Amplifiers and Optical Networks

| <b>Module-1: Overview of Optical Fiber Communication</b>   | <b>No. of Hours</b> | <b>Blooms Cognitive Levels</b> |
|--|---------------------|--------------------------------|
| Historical development, The general system, Advantages of optical fiber communication, Optical fiber waveguides: Ray theory transmission, Modes in planar guide, Phase and group velocity, cylindrical fiber: Modes, Step index fibers, Graded index fibers, Single mode fibers, Cutoff wavelength, Mode field diameter, effective refractive index. Fiber Materials, Photonic crystal fibers. | <b>8</b>            | <b>Apply CO1</b>               |
| <b>Module-2: Transmission characteristics of optical fiber</b>   |                     |                                |
| Attenuation, Material absorption losses, Linear scattering losses, Nonlinear scattering losses, Fiber bend loss, Dispersion, Chromatic dispersion, Intermodal dispersion: Multimode step index fiber   | <b>8</b>            | <b>Understand CO2</b>          |
| <b>Module-3: Optical sources and Photodetectors</b>  |                     |                                |
| Light Emitting diodes: LED Structures, Light Source Materials, Quantum Efficiency and LED Power, Modulation. Laser Diodes: Modes and Threshold conditions, Rate equation, External Quantum Efficiency, Resonant frequencies, Laser Diode structures and Radiation Patterns: Single mode lasers, Physical principles of Photodiodes, Photodetector noise, Detector response time.               | <b>8</b>            | <b>Apply CO3</b>               |
| <b>Module-4: WDM Concepts and Components</b>   |                     |                                |
| Overview of WDM: Operational principles of WDM, WDM standards, Mach-Zehnder Interferometer Multiplexers, Isolators and Circulators, Fiber grating filters, Dielectric Thin-Film Filters, Diffraction Gratings, Active Optical Components, Tunable light sources, Fiber splices, fiber connectors and fiber couplers.   | <b>8</b>            | <b>Understand CO4</b>          |
| <b>Module-5: Optical Amplifiers and Networks</b>   |                     |                                |
| Optical Amplifiers and Networks – optical amplifiers, basic applications and types, semiconductor optical amplifiers, EDFA.<br>OPTICAL NETWORKS: Introduction, SONET / SDH, Optical Interfaces, SONET/SDH rings, High – speed light – waveguides.  | <b>8</b>            | <b>Understand CO5</b>          |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1721.1</b>  | Classification and working of optical fiber with different modes of signal propagation.        |
| <b>21ECE1721.2</b>  | Describe the transmission characteristics and losses in optical fiber communication.           |
| <b>21ECE1721.3</b>  | Describe the constructional features and the characteristics of optical sources and detectors. |
| <b>21ECE1721.4</b>  | Explain the Operational principles of WDM and Optical Components.                              |
| <b>21ECE1721.5</b>  | Explain the working of Optical Amplifiers and Optical Networks                                 |
| <b>21ECE1721.6</b>  | Analyze impact of optical fiber communication on human health and society.                     |

| <b>Reference Books</b>  |  |
|---|--|
| <ol style="list-style-type: none"> <li>1. Gerd Keiser, Optical Fiber Communication, 5<sup>th</sup> Edition, McGraw Hill Education(India) Private Limited, 2015. ISBN:1-25-900687-5.</li> <li>2. John M Senior, Optical Fiber Communications, Principles and Practice, 3<sup>d</sup> Edition, Pearson Education, 2010, ISBN:978-81-317-3266-3</li> <li>3. Joseph C Palais, Fiber Optic Communication , Pearson Education, 2005, ISBN:0130085103</li> </ol> |  |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |   |    |     | SEA   |
|------------|-----|-----|------------------|---|----|-----|---|
|            |     |     |                  | I   | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30  | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks   |    |     |   |
|            |     |     | Assignment       | Two assignments – Scaled to 10 Marks                                      |    |     |   |
|            |     |     | AAT              | 10 Marks - Presentation on topics related to Optical fiber communication. |    |     |   |
|            |     |     | Total – 50 marks |   |    |     | Total – 50 marks  |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU

B.E. (Electronics and Communication Engineering)

Choice Based Credit System (CBCS and Outcome Based Education (OBE)

Semester: VII

Course Name: SoC Design

Course Code: 21ECE1722

|            |         |                        |
|------------|---------|------------------------|
| L: T: P: J | 3:0:0:0 | CIA Marks: 50          |
| Credits:   | 3       | SEA Marks: 50          |
| Hours      | 40      | SEA Duration: 03 Hours |

**Course Learning Objectives: The students will be able to**

|   |   |
|---|---|
| 1 | The objective of this course is to impart a general understanding of the structure, architecture and operation of systems-on-chip.  |
| 2 | To cover SoC design and modelling techniques with emphasis on architectural exploration, assertion-driven design and the concurrent development of hardware and embedded software used to implement and model inter component communication in SoC. |
| 3 | To provide the overall idea how to integrate various building blocks of a system-on-chip, e.g. processor, on-/off-chip memories are interconnected.   |
| 4 | To apply Implementation methods as well as techniques for low power consumption are addressed.  |

|  | No. of Hours | Blooms Cognitive Levels/CO Mapping |
|--|--------------|------------------------------------|
| <b>Module-1: Introduction to System on Chip</b>  |              |                                    |
| Review of Moore's law and CMOS scaling, benefits of System On Chip integration in terms of cost, power, and performance. Comparison on System on Board, System on Chip, and System-in-Package. Typical goals in SoC design cost reduction, power reduction, design effort reduction, performance maximization. Productivity gap issues and the ways to improve the gap – IP based design and design reuse.   | 8            | Understand CO1                     |
| <b>Module-2: System On Chip Design Process</b>   |              |                                    |
| System On Chip Design Process: A canonical SoC Design, SoC Design flow, waterfall vs spiral, top-down vs bottom-up, Specification requirement, Types of Specification, System Design Process, System level design issues, Soft IP vs Hard IP, IP verification and Integration, Hardware-Software co design, Design for timing closure, Logic design issues, Verification strategy, On chip buses and interfaces, Low Power, Hardware Accelerators in Soc | 8            | Apply CO2                          |
| <b>Module-3: Embedded Memories</b>   |              |                                    |
| Embedded Memories, cache memories, flash memories, embedded DRAM. Topics related to cache memories. Cache coherence. MESI protocol and Directory-based coherence.  | 8            | Apply CO3                          |
| <b>Module-4: Interconnect architectures for SoC</b>  |              |                                    |
| Interconnect architectures for SoC. Bus architecture and its limitations. Network on Chip (NOC) topologies. Mesh-based NoC. Routing in an NoC. Packet switching and wormhole routing   | 8            | Apply CO4                          |
| <b>Module-5: MPSoCs:</b>   |              |                                    |
| MPSoCs: What, Why, How MPSoCs, Techniques for designing MPSoCs, Multichip Packages and chipset based design, Performance and flexibility for MPSoCs design<br>Case Study: A Low Power Open Multimedia Application Platform for LTE.  | 8            | Analyse CO5                        |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| <b>21ECE1722.1</b>  | Learn about the blocks in the system on chip design and its performance.                                    |
| <b>21ECE1722.2</b>  | Analyze the design flow and verification of IPs used in system on chip.                                     |
| <b>21ECE1722.3</b>  | Exposure the concepts of different memory and interconnection methods in SoC                                |
| <b>21ECE1722.4</b>  | Analyze existing Interconnect architectures for SoC and network on chip                                     |
| <b>21ECE1722.5</b>  | Design & develop the algorithms required for the design of IP and SoC and Exposure to the concept of MPSoCs |
| <b>21ECE1722.6</b>  | Understand the complexity of MPSoC design and analyze its usage in real-time applications.                  |

| <b>Reference Books</b> |  |
|------------------------|--|
| 1.                     | SudeepPasricha and NikilDutt, "On-Chip Communication Architectures: System on Chip Interconnect", Morgan Kaufmann Publishers © 2008.   |
| 2.                     | Rao R. Tummala, MadhavanSwaminathan, "Introduction to system on package sopMiniaturization of the Entire Syste", McGraw-Hill, 2008.    |
| 3.                     | James K. Peckol, "Embedded Systems: A Contemporary Design Tool", Wiley Student Edition.  |
| 4.                     | Michael Keating, Pierre Bricaud, "Reuse Methodology Manual for System on Chip designs", Kluwer Academic Publishers, 2nd edition, 2008. |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                      |    |     | SEA   |
|------------|-----|-----|------------------|--------------------------------------|----|-----|---|
|            |     |     |                  | I                                    | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30                                   | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks    |    |     |   |
|            |     |     | Assignment       | Two assignments – Scaled to 10 Marks |    |     |   |
|            |     |     | AAT              | 10 Marks                             |    |     |   |
|            |     |     | Total – 50 marks |                                      |    |     | Total – 50 marks  |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU, Approved by AICTE

**Dept. of Electronics and Communication Engineering**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: VII**

**Course Name: Automotive Electronics**

**Course Code: 21ECE1723**

|                   |                   |                               |
|-------------------|-------------------|-------------------------------|
| <b>L: T: P: J</b> | <b>3: 0: 0: 0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>   | <b>3</b>          | <b>SEA Marks: 50</b>          |
| <b>Hours</b>      | <b>40</b>         | <b>SEA Duration: 03 Hours</b> |

**Pre-Requisites:** Analog and Digital Circuits, Control Systems, Embedded systems, Transducers.

**Course Learning Objectives: The students will be able to**

|   |   |
|---|---|
| 1 | Understand the basics of automobile dynamics and design electronics to complement those features. |
| 2 | Understand the working principle of sensors and actuators used in the automotive electronics.     |
| 3 | Study the principles and functionalities of various automotive communication protocols.           |
| 4 | Explore the future automotive electronic systems.   |

| Module-1: Automotive Fundamentals Overview   | No. of Hours | Blooms Cognitive Levels |
|--|--------------|-------------------------|
| Evolution of Automotive Electronics, Automobile Physical Configuration, Survey of Major Automotive Systems, The Engine - Engine Block, Cylinder Head, Four Stroke Cycle, Engine Control, Ignition System- Spark plug, High voltage circuit and distribution, Spark pulse generation, Ignition Timing, Diesel Engine, Drive Train - Transmission, Drive Shaft, Differential, Suspension, Brakes, Steering System, Starter Battery-Operating principle.                                      | 8            | Understand CO1          |
| Module-2: Automotive Sensors and Actuators   |              |                         |
| <b>Automotive Sensors-</b> Automotive Control System applications of Sensors and Actuators - Variables to be measured, Airflow rate sensor, Strain Gauge MAP sensor, Engine Crankshaft Angular Position Sensor, Magnetic Reluctance Position Sensor, Hall effect Position Sensor, Shielded Field Sensor, Optical Crankshaft Position Sensor, Throttle Angle Sensor (TAS), Engine Coolant Temperature (ECT) Sensor, Exhaust Gas Oxygen (O2/EGO) Lambda Sensors, Piezoelectric Knock Sensor. | 8            | Understand CO2          |
| <b>Automotive Actuators-</b> Solenoid, Fuel Injector, EGR Actuator, Ignition System.   |              |                         |
| Module-3: Digital Engine Control Systems   |              |                         |

|  |   |                |
|--|---|----------------|
| Digital Engine control features, Control modes for fuel Control (Seven Modes), EGR Control, Electronic Ignition Control -Closed loop Ignition timing, Spark Advance Correction Scheme, Integrated Engine Control System- Secondary Air Management, Evaporative Emissions Canister Purge, Automatic System Adjustment, System Diagnostics.  | 8 | Understand CO3 |
| <b>Module-4: Automotive Networking</b>   |   |                |
| Bus Systems- Classification, Applications in the vehicle, Coupling of networks, Examples of networked vehicles Buses - CAN Bus, UN Bus, MOST Bus, Bluetooth, Flex Ray, Diagnostic Interfaces.  | 8 | Understand CO4 |
| <b>Module-5: Automotive Diagnostics and Advanced Automotive Systems</b>  |   |                |
| <b>Automotive Diagnostics</b> -Timing Light, Engine Analyzer, On-board diagnostics, Off-board diagnostics, Expert Systems, Occupant Protection Systems -Accelerometer based Air Bag systems.<br><b>Advanced Automotive Electronic Systems</b> -Alternative Fuel Engines, Electric and Hybrid vehicles, Fuel cell powered cars, Collision Avoidance Radar warning Systems, Low tire pressure warning system, Heads Up display, Speech Synthesis, Navigation - Navigation Sensors - Radio Navigation, Signpost navigation, dead reckoning navigation, Voice Recognition Cell Phone dialling, Advanced Cruise Control, Stability Augmentation, Automatic driving Control. | 8 | Understand CO5 |

**Course Outcomes: After completing the course, the students will be able to**

|             |   |
|-------------|---|
| 21ECE1723.1 | Acquire an overview of automotive components, subsystems, and basics of Electronic Engine Control in today's automotive industry. |
| 21ECE1723.2 | Explore the various automotive sensors and actuators used for the development of automotive systems using microcontrollers.       |
| 21ECE1723.3 | Identify the importance of Control systems in automotive systems.   |
| 21ECE1723.4 | Understand the networking of various modules in automotive systems, communication protocols and diagnostics of the sub systems.   |
| 21ECE1723.5 | Highlight the design of the automotive electronic systems and explore the advanced automotive systems.                            |
| 21ECE1723.6 | Apply the fundamentals of electronics in the development of advanced automotive systems.  |

**Reference Books**

1. William B. Ribbens, "Understanding Automotive Electronics", 8th Edition, Newnes, 2017.
2. Ronald K. Jurgen, "Automotive Electronics Handbook", 2<sup>nd</sup> Edition, Mcgraw-Hill, 2007.
3. Denton, "Automotive Electrical and Electronic systems", MA 01803, Elsevier Butterworth-Heinemann, 2004.
4. Robert Bosch GmbH (Ed.) Bosch Automotive Electrics and Automotive Electronics Systems and Components, Networking and Hybrid Drive, 5th edition, John Wiley & Sons Inc., 2007.



### Marks Distribution for Assessment:

| PEC        | CIA | SEA | CIA (50)         |                                   |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M  |
|------------|-----|-----|------------------|-----------------------------------|----|-----|---|
|            |     |     |                  | I                                 | II | III |   |
| Conduction | 50  | 50  | Written Test     | 30                                | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks |    |     |   |
|            |     |     | Assignment       | 10                                |    |     |   |
|            |     |     | AAT              | 10                                |    |     |   |
|            |     |     | Total – 50 marks |                                   |    |     | Total – 50 marks  |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC course.

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU, Approved by AICTE

**Dept. of Electronics and Communication Engineering**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: VII**

**Course Name: Natural Language Processing**

**Course Code: 21ECE1724**

|                   |                      |                               |
|-------------------|----------------------|-------------------------------|
| <b>L: T: P: J</b> | <b>3 : 0 : 0 : 0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>   | <b>3</b>             | <b>SEA Marks: 50</b>          |
| <b>Hours</b>      | <b>40</b>            | <b>SEA Duration: 03 Hours</b> |

## **Course Learning Objectives: The students will be able to**

- |   |  |
|---|--|
| 1 | Understand, Natural Language Processing Concepts and their Applications. |
| 2 | Analysis of regular expression, parsing.                                 |
| 3 | Semantic Analysis of Meaning Representation.                             |
| 4 | Understand and implement N-gram models.                                  |
| 5 | Design of information retrieval models.                                  |

| <b>Module-1: Introduction</b>  | <b>No. of Hours</b> | <b>Blooms Cognitive Levels</b> |
|--|---------------------|--------------------------------|
| Introduction to Natural Language Processing, Stages in Natural Language Processing, Origins and Challenges of NLP Language and Grammar-Processing Indian Languages, Introduction to the corpus, elements in the balanced corpus. Design a Python program to illustrate corpus.   | 8                   | Apply CO1                      |
| <b>Module-2: Word level Analysis</b>   |                     |                                |
| Word level Analysis: Regular Expressions-Finite-State Automata-Morphological Parsing-Spelling Error Detection and correction. Part-of-Speech Tagging- Rule-based tagger, Stochastic tagger. Design python program to perform part-of-speech tagging on the text scraped from a website. Design a python program to group similar words together based on the nature of the word. | 8                   | Apply CO2                      |
| <b>Module-3: N-Grams</b>   |                     |                                |
| N-Grams: Simple N-grams, Smoothing- Laplace smoothing, Good Turing Discounting, Backoff, Entropy, Morphology: Inflectional morphology, Derivational morphology. Develop a Python program to calculate good Turing frequency.   | 8                   | Apply CO3                      |
| <b>Module-4: Lexical Semantics</b>   |                     |                                |
| Semantic: Meaning Representation, Lexical Semantics, Word Sense Disambiguation –Selectional Restriction-based word sense disambiguation, context-based word sense disambiguation Approaches. Lexical Semantics- Python program to do text classification. Meaning Representation- Python program to represent the meaning of the given text.                                     | 8                   | Apply CO4                      |
| <b>Module-5: Information Retrieval</b>   |                     |                                |

|   |   |              |
|---|---|--------------|
| Information Retrieval-Design features of information retrieval systems- Indexing, eliminating stop words, Stemming, Classical information retrieval Models-Boolean model, Probabilistic model.<br>Applications: Information extraction, Automatic text summarization, topic modelling, Question –Answer System using Python | 8 | Apply<br>CO5 |
|---|---|--------------|

**Course Outcomes: After completing the course, the students will be able to**

|                    |   |
|--------------------|---|
| <b>21ECE1724.1</b> | Apply the fundamental concepts of Natural Language Processing, including its origins, challenges, and applications in processing languages and grammars.                                  |
| <b>21ECE1724.2</b> | Develop skills to analyze text at the word level using regular expressions, morphological parsing, spelling error detection, and part-of-speech tagging.                                  |
| <b>21ECE1724.3</b> | Understand and implement N-gram models and various smoothing techniques, including Laplace smoothing and Good Turing Discounting.   |
| <b>21ECE1724.4</b> | Gain expertise in semantic analysis, including meaning representation, lexical semantics, and word sense disambiguation using Selectional restriction-based and context-based approaches. |
| <b>21ECE1724.5</b> | Design and implement information retrieval systems with features such as indexing, stop word elimination, stemming, and classical models like the Boolean and probabilistic models.       |
| <b>21ECE1724.6</b> | Apply the information retrieval systems to tasks like text summarization, topic modeling, and question-answering systems.   |

**Reference Books**

1. Siddiqui T., Tiwary U. S. Natural language processing and Information retrieval, OUP, 2023.
2. James A., Natural language Understanding 2e, Pearson Education, 2019
3. Daniel Jurafsky and James H Martin, “Speech and Language Processing: An introduction to Natural Language Processing, Computational Linguistics, and 2SpeechRecognition”, 2nd Edition, Prentice Hall, 2013.
4. R. Kibble Introduction to Natural Language Processing CO3354 2013
5. Bharati A., Sangal R., Chaitanya V. Natural language processing: a Paninian perspective, PHI, 2006.

**Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                      |    |     | SEA   |
|------------|-----|-----|------------------|--------------------------------------|----|-----|---|
|            |     |     |                  | I                                    | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30                                   | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks    |    |     |   |
|            |     |     | Assignment       | Two assignments – Scaled to 10 Marks |    |     |   |
|            |     |     | AAT              | 10 Marks                             |    |     |   |
|            |     |     | Total – 50 marks |                                      |    |     | Total – 50 marks  |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU, Approved by AICTE

**Dept. of Electronics and Communication Engineering**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: VII**

**Course Name: Fundamentals of Data Science** **Course Code: 21ECE1725**

|                   |                      |                               |
|-------------------|----------------------|-------------------------------|
| <b>L: T: P: J</b> | <b>3 : 0 : 0 : 0</b> | <b>CIA Marks: 50</b>          |
| <b>Credits:</b>   | <b>3</b>             | <b>SEA Marks: 50</b>          |
| <b>Hours</b>      | <b>3</b>             | <b>SEA Duration: 03 Hours</b> |

**Course Learning Objectives: The students will be able to**

|   |   |
|---|---|
| 1 | Understand the fundamentals of Data Science                       |
| 2 | Analyze the basic tools of EDA and the data science process       |
| 3 | Explore the different algorithms used in data science             |
| 4 | Explore Feature Generation and Feature selection                  |
| 5 | Optimize and solve real life problems with different spam filters |

| <b>Module-1: Fundamentals</b>   | <b>No. of Hours</b> | <b>Blooms Cognitive Levels</b> |
|---|---------------------|--------------------------------|
| <p>Introduction: What is Data Science? Big Data and Data Science hype – and getting past the hype, Why now? – Datafication, Current landscape of perspectives, A data Science Profile, Skill sets. Statistical Inference, Populations and samples, Big Data, new kinds of data, modelling, statistical modeling probability distributions, fitting a model</p> <p><b>Program – 1:</b> Program to find mean, standard deviation and Baye's Theorem Proof</p> | <b>8</b>            | <b>Understand CO1</b>          |
| <b>Module-2: Exploratory Data Analysis</b>  |                     |                                |
| <p>Exploratory Data Analysis and the Data Science Process: Basic tools plots, graphs and summary statistics) of EDA, Philosophy of EDA, The Data Science Process, Machine Learning Algorithms.</p> <p>Three Basic Algorithms: Linear Regression, k-Nearest Neighbours (kNN), k-means</p> <p><b>Program – 2:</b> Given a dataset, perform EDA on it using python</p>   | <b>8</b>            | <b>Apply CO2</b>               |
| <b>Module-3: Spam Filter</b>  |                     |                                |
| <p>Spam Filter, Linear Regression and Spam Filter, K-NN and spam Filter, Naïve Bayes Algorithm, Spam Filter using Naïve Bayes</p> <p><b>Program-3:</b> Implementation of Spam filter using kNN algorithm</p>  | <b>8</b>            | <b>Apply CO3</b>               |
| <b>Module-4: Feature Engineering</b>  |                     |                                |
| <p>Feature Generation and Feature Selection (Extracting Meaning from Data): Motivating application: user (customer) retention. Feature Generation (brainstorming, role of domain expertise, and place for imagination), Feature Selection algorithms. Filters; Wrappers; Decision Trees; Random Forests.</p>  | <b>8</b>            | <b>Apply CO4</b>               |

|   |          |                  |
|---|----------|------------------|
| <b>Program-4:</b> Implementation of Feature Extraction using Random Forest.   |          |                  |
| <b>Module-5: Recommendation Systems</b>   |          |                  |
| Building a User-Facing Data Product, Algorithmic ingredients of a Recommendation Engine, Dimensionality Reduction, Singular Value Decomposition, Principal Component Analysis<br><b>Program-5:</b> Build a recommender system using PCA | <b>8</b> | <b>Apply CO5</b> |

|   |   |
|---|---|
| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
| <b>21ECE1725.1</b>  | Explain the fundamentals of data science                                      |
| <b>21ECE1725.2</b>  | Explore Data Analysis and data science process                                |
| <b>21ECE1725.3</b>  | Understand spam filter implementation using basic Machine Learning algorithms |
| <b>21ECE1725.4</b>  | Understand the working of recommendation systems using ML algorithms          |
| <b>21ECE1725.5</b>  | Explain feature selection and extraction algorithms                           |
| <b>21ECE1725.6</b>  | Conduct independent study and analysis of real-world data science problems    |

|   |
|---|
| <b>Reference Books</b>  |
| <ol style="list-style-type: none"> <li>1. Sanjeev J. Wagh, Manisha S. Bhende, Anuradha D. Thakare “Fundamentals of Data Science”, CRC Press, 2021</li> <li>2. B. Uma Maheswari, R. Sujatha, “Introduction to Data Science - Practical Approach with R and Python”, Wiley, 2021</li> <li>3. Cathy O Neil, Rachel Schutt, “Doing Data Science-Straight Talk from the Frontline”, Orielly, 2014.</li> <li>4. Jure Leskovek, Anand Rajaraman, Jeffrey Ullman, “Mining of Massive Data Sets”, Cambridge University Press, 2014.</li> </ol> |

#### Marks Distribution for Assessment:

| PCC        | CIA | SEA | CIA (50)     |                                      |    |                  | SEA   |
|------------|-----|-----|--------------|--------------------------------------|----|------------------|---|
|            |     |     |              | I                                    | II | III              | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test | 30                                   | 30 | 30               | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |              | Average of three tests – 30 Marks    |    |                  |   |
|            |     |     | Assignment   | Two assignments – Scaled to 10 Marks |    |                  |   |
|            |     |     | AAT          | 10 Marks – Open ended experiments    |    |                  |   |
|            |     |     |              |                                      |    | Total – 50 marks |   |

**Additional Assessment Tools (AAT)** –Open ended experiments.

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

| Semester: B. E   |  |                          |
|--|--|--------------------------|
| Course Name : Digital VLSI Testing   |  | Course Code: 21ECE1732   |
| L: T:P: J  | 3:0:0:0  | CIA Marks: 50            |
| Credits:   | 3  | SEA Marks: 50            |
| Hours:   | 40   | SEA Duration: 03 Hours   |
| Pre-Requisites: The Digital Design / Digital Logic course is a pre-requisite for this course.  |  |                          |
| Course Learning Objectives: The students will be able to   |  |                          |
| 1  | Understand Importance, Challenges, Levels of abstraction, Design for Testability |                          |
| 2  | Understand the Scan design rules, Scan design flow, Fault Simulation             |                          |
| 3  | Understand Test Generation, issues in test generation Built-In-Self-Test         |                          |
| 4  | Understand the Test Compression  |                          |
| 5  | Understand the Memory Testing, Power and Thermal Aware Test                      |                          |
| Module-1: Introduction Digital VLSI Testing  |  |                          |
| Introduction: Importance, Challenges, Levels of abstraction, Fault Models, Advanced issues.<br>Design for Testability: Introduction, Testability Analysis, DFT Basics, Scan cell design, Scan Architecture. Scan design rules, Scan design flow. Fault Simulation: Introduction, Simulation models. Logic simulation, Fault simulation |  | No. of Hrs               |
|  |  | 8                        |
|  |  | Bloom’s Cognitive Levels |
|  |  | Understand CO1           |
| Module–2: Test Generation  |  |                          |
| Introduction, Exhaustive testing, Boolean difference, Basic ATPG algorithms. ATPG for non-stuck-at faults, other issues in test generation.  |  | 8                        |
|  |  | Understand CO2           |
| Module–3: Built-In-Self-Test   |  |                          |
| Built-In-Self-Test: Introduction, BIST design rules. Built-In-Self-Test: Test pattern generation, Output response analysis, Logic BIST architectures   |  | 8                        |
|  |  | Understand CO3           |
| Module–4: Test Compression   |  |                          |
| Introduction, Stimulus compression, Stimulus compression, Response compression   |  | 8                        |
|  |  | Understand CO3           |
| Module–5: Memory Testing & Power and Thermal Aware Test  |  |                          |
| Introduction, RAM fault models, RAM test generation, Memory BIST Power and Thermal Aware Test: Importance, Power models, Low power ATPG. Power and Thermal Aware Test: Low power BIST, Thermal aware techniques  |  | 8                        |
|  |  | Apply CO4                |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE1732.1</b>  | <b>Understanding VLSI Testing Fundamentals:</b> Students will gain a comprehensive understanding of the fundamental concepts of VLSI testing, including the importance of testing, test economics, and the role of testing in the VLSI design flow   |
| <b>21ECE1732.2</b>  | <b>Fault Modeling and Fault Simulation:</b> Students will learn various fault models (e.g., stuck-at faults, transition faults, bridging faults) and how to perform fault simulation to predict circuit behavior in the presence of faults.  |
| <b>21ECE1732.3</b>  | <b>Design for Testability (DFT):</b> Students will understand and apply DFT techniques such as scan design, boundary scan, Built-In Self-Test (BIST), and Logic BIST to enhance the testability of digital circuits.   |
| <b>21ECE1732.4</b>  | <b>Automatic Test Pattern Generation (ATPG):</b> Students will acquire knowledge about ATPG algorithms and tools, learning how to generate test patterns that can effectively detect faults in VLSI circuits.  |
| <b>21ECE1732.5</b>  | <b>Test Compression Techniques:</b> Students will explore methods for reducing the volume of test data through test compression techniques, enabling efficient testing of large-scale VLSI circuits.   |
| <b>21ECE1732.6</b>  | <b>Testing of Memory and Mixed-Signal Circuits:</b> Students will learn specific testing strategies for memory circuits (e.g., SRAM, DRAM) and mixed-signal circuits, including analog and RF components. Students will gain hands-on experience using industry-standard EDA tools for VLSI testing, such as tools for ATPG, fault simulation, and DFT implementation. |

| <b>Reference Books</b>  |
|---|
| <ol style="list-style-type: none"> <li>1. "Digital Systems Testing and Testable Design" by Miron Abramovici, Melvin A. Breuer, and Arthur D. Friedman</li> <li>2. "Essentials of Electronic Testing for Digital, Memory, and Mixed-Signal VLSI Circuits" by Michael L. Bushnell and Vishwani D. Agrawal</li> <li>3. "VLSI Test Principles and Architectures: Design for Testability" by Laung-Terng Wang, Cheng-Wen Wu, and Xiaoqing Wen</li> <li>4. "Introduction to VLSI Testing" by Robert J. Feugate and Steven M. McCoy</li> </ol> |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                      |    |     | SEA   |
|------------|-----|-----|------------------|--------------------------------------|----|-----|---|
|            |     |     |                  | I                                    | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30                                   | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks    |    |     |   |
|            |     |     | Assignment       | Two assignments – Scaled to 10 Marks |    |     |   |
|            |     |     | AAT              | 10 Marks                             |    |     |   |
|            |     |     | Total – 50 marks |                                      |    |     | Total – 50 marks  |

# *B.N.M. Institute of Technology*

**An Autonomous Institution under VTU**

| Semester: B. E  |   |                        |                          |
|---|---|------------------------|--------------------------|
| Course Name : Deep Learning for Computer Vision Course  |   | Code: 21ECE1734        |                          |
| L: T:P: J   | 3:0:0:0   | CIAMarks: 50           |                          |
| Credits:  | 3   | SEAMarks: 50           |                          |
| Hours   | 40  | SEA Duration: 03 Hours |                          |
| Pre-Requisites: Knowledge of basics in probability, linear algebra and calculus and basics of machine learning  |   |                        |                          |
| Course Learning Objectives: The students will be able to  |   |                        |                          |
| 1   | Understand traditional computer vision topics involving Visual Features and Representations.  |                        |                          |
| 2   | Understand building blocks of deep learning for computer vision where they study convolutions neural networks, various architectures and models.                        |                        |                          |
| 3   | Understand applications and use cases and tasks in which CNNs are used like for recognition, verification, retrieval, detection, segmentation                           |                        |                          |
| 4   | Understand computer vision for video, action recognition, activity recognitions,  |                        |                          |
| 5   | Understand deep generative models such as GANS and variational auto encoders, Deep Generative Models: Diffusion Models, Vision-Language Models and Recent Developments. |                        |                          |
| Module-1: Introduction and Overview, Visual Features and Representations  |   |                        |                          |
| Image Formation ,Image Representation, Linear Filtering, Correlation, Convolution, Edge Detection From Edges to Blobs and Corners Scale Space, Image Pyramids and Filter Banks SIFT and Variants  |   | No. of Hrs             | Bloom's Cognitive Levels |
|   |   | 8                      | Understand CO1           |
| Module–2: Deep Learning Basics, Convolutional Neural Networks for Image Classification  |   |                        |                          |
| Neural Networks: A Review , Feedforward Neural Networks and Backpropagation, Gradient Descent and Variants, Regularization in Neural Networks, Improving Training of Neural Networks<br>Convolutional Neural Networks: An Introduction Backpropagation in CNN's ,CNN Architecture for Image Classification          |   | 8                      | Understand CO2           |
| Module–3: Beyond Basic CNNs: Architectures, Fine tuning and Visualization, CNNs for Object Detection and Segmentation, Recurrent Neural Networks and their use in Vision  |   |                        |                          |
| Evolution of CNN Architectures: VGG, Inception, ResNets, ResNet Variants, MobileNet, EfficientNet, Finetuning CNNs, Visualizing CNNs.<br>CNNs for Object Detection: Two-stage Models,Single-stage Models, CNNs for Segmentation<br>Recurrent Neural Networks: Introduction ,Backpropagation in RNNs, LSTMs and GRUs |   | 8                      | Understand CO3           |
| Module–4: Attention Models and Transformers, Vision Transformers and Applications   |   |                        |                          |
| Attention in Vision Models: An Introduction ,Soft and Hard Attention: Image Captioning.<br>Self-Attention and Transformers. From Transformers to Vision   |   |                        | Apply CO4                |



|   |          |              |
|---|----------|--------------|
| Transformers, Transformers for Detection, Transformers for Segmentation   |          |              |
| <b>Module-5: Deep Generative Models: GANs and VAEs, Deep Generative Models: Diffusion Models, Vision-Language Models and Recent Developments</b>  |          |              |
| Deep Generative Models: An Introduction ,Generative Adversarial Networks, GAN Hacks and Improvements, Variational Autoencoders and Disentanglement<br>Introduction to Diffusion Models: DDPMs, Classifier and Classifier-Free Diffusion Guidance,Text-conditioned Diffusion Models,<br>Under the Hood: Sampling, Prediction Space, Noise Schedules, Architectures<br>Self-Supervised Learning: SimCLR, Contrastive Learning, Vision-Language Models | <b>8</b> | Apply<br>CO5 |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| 21ECE1734.1   | Understand traditional computer vision topics involving Visual Features and Representations.  |
| 21ECE1734.2   | Understand building blocks of deep learning for computer vision where they study convolutions neural networks, various architectures and models.                        |
| 21ECE1734.3   | Understand applications and use cases and tasks in which CNNs are used like for recognition, verification, retrieval, detection, segmentation                           |
| 21ECE1734.4   | Understand computer vision for video, action recognition, activity recognitions,  |
| 21ECE1734.5   | Understand deep generative models such as GANS and variational auto encoders, Deep Generative Models: Diffusion Models, Vision-Language Models and Recent Developments. |
| 21ECE1734.6   | Identify and apply algorithms to solve real world problems  |

| <b>Reference Books</b>  |
|---|
| <ol style="list-style-type: none"> <li>1. Deep Learning, Ian Goodfellow, Yoshua Bengio, Aaron Courville, 2016</li> <li>2. Neural Networks and Deep Learning, Michael Nielsen, 2016</li> <li>3. Learning Deep Architectures for AI, Yoshua Bengio, 2009</li> <li>4. Computer Vision: Algorithms and Applications, Richard Szeliski, 2010</li> <li>5. Computer Vision: Models, Learning, and Inference, Simon Prince, 2012</li> <li>6. Computer Vision: A Modern Approach, David Forsyth, Jean Ponce, 2002</li> </ol> |

#### Marks Distribution for Assessment:

| Marks Distribution for Assessment |     |     |                  |                                      |    |     |  |
|-----------------------------------|-----|-----|------------------|--------------------------------------|----|-----|--|
| PCC                               | CIA | SEA | CIA (50)         |                                      |    |     | SEA<br>Conduction: 100 M<br>Reduced to: 50 M   |
|                                   |     |     |                  | I                                    | II | III |  |
| Conduction                        | 50  | 50  | Written Test     | 30                                   | 30 | 30  | Five questions with each of 20 marks (with internal choice).<br>Student should answer one full question from each module |
|                                   |     |     |                  | Average of three tests – 30 Marks    |    |     |  |
|                                   |     |     | Assignment       | Two assignments – Scaled to 10 Marks |    |     |  |
|                                   |     |     | AAT              | 10 Marks                             |    |     |  |
|                                   |     |     | Total – 50 marks |                                      |    |     | Total – 50 marks   |

# *B.N.M. Institute of Technology*

An Autonomous Institution under VTU, Approved by AICTE

**Dept. of Electronics and Communication Engineering**

**Choice Based Credit System (CBCS and Outcome Based Education (OBE))**

**Semester: VII**

**Course Name: Introduction to Industry 4.0 and Industrial Internet of Things**

**Course Code: 21ECE1735**

**L: T:P: J**

**3:0:0:0**

**CIA Marks: 50**

**Credits:**

**SEA Marks: 50**

**Hours**

**40**

**SEA Duration: 03 Hours**

**Pre-Requisites:** Knowledge on IOT

**Course Learning Objectives: The students will be able to**

- |   |   |
|---|---|
| 1 | Understand What is Industry 4.0? and its associated technologies          |
| 2 | Understand the role of IOT in industries                                  |
| 3 | Understand layers of Industrial IOT                                       |
| 4 | Understand role of networking and security in Industrial IOT              |
| 5 | Work on real time case studies based on the application of Industrial IOT |

## **Module-1: Introduction to Industry 4.0**

**Introduction:** Sensing & actuation, Communication-Part I, Part II, Networking-Part I, Part II

**Industry 4.0:** Globalization and Emerging Issues, The Fourth Revolution, LEAN Production Systems, Smart and Connected Business Perspective, Smart Factories, Cyber Physical Systems and Next Generation Sensors, Collaborative Platform and Product Lifecycle Management, Augmented Reality and Virtual Reality, Artificial Intelligence, Big Data and Advanced Analysis, Cybersecurity in Industry 4.0

**No. of Hrs**

**Bloom's Cognitive Levels**

**8**

Understand  
CO1

## **Module-2: Introduction to Industrial IOT**

**Basics of Industrial IoT:** Industrial Processes-Part I, Part II, Industrial Sensing & Actuation, Industrial Internet Systems.

**Industrial IoT Introduction:** Business Model and Reference Architecture: IIoT-Business Models-Part I, Part II, IIoT Reference Architecture-Part I, Part II.

**8**

Understand  
CO2

## **Module-3: Industrial IOT – Layers**

**Industrial IoT- Layers:** IIoT Sensing-Part I, Part II, IIoT Processing-Part I, Part II, IIoT Communication-Part I. IIoT Communication-Part II, Part III, IIoT Networking-Part I, Part II, Part III.

**8**

Understand  
CO3

## **Module 4: Industrial IOT Analytics – Networking and Security**

**Big Data Analytics and Software Defined Networks:** IIoT Analytics - Introduction, Machine Learning and Data Science - Part I, Part II, R and Julia Programming, Data Management with Hadoop, SDN in IIoT-Part I, Part II, Data Center Networks, Industrial IoT: Security and Fog Computing: Cloud Computing in IIoT-Part I, Part II.

**Security and Fog Computing** - Fog Computing in IIoT, Security in IIoT-Part I, Part II, Industrial IoT- Application Domains: Factories and Assembly Line, Food Industry.

**8**

Understand  
CO3

| <b>Module-5: Application domains of Industrial IOT</b>   |          |                  |
|--|----------|------------------|
| <b>Application Domains:</b> Healthcare, Power Plants, Inventory Management & Quality Control, Plant Safety and Security (Including AR and VR safety applications), Facility Management, Oil, chemical and pharmaceutical industry, Applications of UAVs in Industries, Real case studies | <b>8</b> | <b>Apply CO5</b> |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |   |
|---|---|
| 21ECE1735.1   | Understand What is Industry 4.0? and its associated technologies          |
| 21ECE1735.2   | Understand the role of IOT in industries                                  |
| 21ECE1735.3   | Understand layers of Industrial IOT                                       |
| 21ECE1735.4   | Understand role of networking and security in Industrial IOT              |
| 21ECE1735.5   | Work on real time case studies based on the application of Industrial IOT |

| <b>Reference Books</b>   |
|--|
| 1. S. Misra, A. Mukherjee, and A. Roy, 2020. Introduction to IoT. Cambridge University Press.                          |
| 2. S. Misra, C. Roy, and A. Mukherjee, 2020. Introduction to Industrial Internet of Things and Industry 4.0. CRC Press |

#### **Marks Distribution for Assessment:**

| PCC        | CIA | SEA | CIA (50)         |                                      |    |     | SEA   |
|------------|-----|-----|------------------|--------------------------------------|----|-----|---|
|            |     |     |                  | I                                    | II | III | Conduction: 100 M<br>Reduced to: 50 M   |
| Conduction | 50  | 50  | Written Test     | 30                                   | 30 | 30  | Five questions with each of 20 marks (with internal choice). Student should answer one full question from each module |
|            |     |     |                  | Average of three tests – 30 Marks    |    |     |   |
|            |     |     | Assignment       | Two assignments – Scaled to 10 Marks |    |     |   |
|            |     |     | AAT              | 10 Marks                             |    |     |   |
|            |     |     | Total – 50 marks |                                      |    |     | Total – 50 marks  |

# *B N M Institute of Technology*

Autonomous Engineering College Under VTU  
Dept. of Electronics and Communication Engineering  
Choice Based Credit System (CBCS and Outcome Based Education (OBE))

**Semester: VII**

## **RESEARCH METHODOLOGY AND IPR**

|  |  |                         |                                |
|--|--|-------------------------|--------------------------------|
| <b>Course Code: 21ECE174</b>   |  | <b>L:T:P:J: 2:0:0:0</b> | <b>CIE Marks : 50</b>          |
| <b>Credits:</b>  |  | <b>2</b>                | <b>SEE Marks: 50</b>           |
| <b>Hours:</b>  |  | <b>25</b>               | <b>SEE Duration: 03 Hours</b>  |
| <b>Pre-Requisites:</b> Use of internet and online database, clarity on research question/problem and basic of statistics   |  |                         |                                |
| <b>Course Learning Objectives: The students will be able to</b>  |  |                         |                                |
| 1  | To give an overview of the research methodology and explain the technique of defining a research problem   |                         |                                |
| 2  | To explain the functions of literature review, carry out literature search and develop conceptual frameworks   |                         |                                |
| 3  | To explain various experimental designs in research and data handling like data sampling and data collection methods                                       |                         |                                |
| 4  | To interpret the research findings and prepare a research report   |                         |                                |
| 5  | To build awareness on the various forms of IPR and to build the perspectives on the concepts and to develop the linkages in technology innovation and IPR. |                         |                                |
| <b>Module-1: Introduction to Research Methodology</b>  |  |                         |                                |
| <b>Research Methodology:</b> Introduction, Meaning of Research, Objectives of Research, Motivation in Research, Types of Research, Research Approaches, Significance of Research, Research Methods versus Methodology, Research and Scientific Method, Importance of Knowing How Research is Done, Research Process, Criteria of Good Research<br><b>Defining the Research Problem:</b> Research Problem, Selecting the Problem, Necessity of Defining the Problem, Technique Involved in Defining a Problem, An Illustration Problems Encountered by Researchers in India.  |  | <b>No. of Hrs</b>       | <b>Blooms cognitive Levels</b> |
|  |  | <b>05</b>               | <b>Understand CO1</b>          |
| <b>Module-2: Literature Review</b>   |  |                         |                                |
| <b>Reviewing the literature:</b> Place of the literature review in research, Bringing clarity and focus to your research problem, Improving research methodology, Broadening knowledge base in research area, enabling contextual findings, How to review the literature, searching the existing literature, reviewing the selected literature, developing a theoretical framework, Developing a conceptual framework, writing about the literature reviewed.<br><b>Research Design:</b> Meaning of Research Design, Need for Research Design, Features of a Good Design, Important Concepts Relating to Research Design, Different Research Designs, Basic Principles of Experimental Designs, Important Experimental Designs. Use of Endnote or mendeley |  | <b>No. of Hrs</b>       | <b>Blooms cognitive Levels</b> |
|  |  | <b>05</b>               | <b>Apply CO2</b>               |

| <b>Module–3: Data Sampling and Testing of Hypothesis</b>  |                   |                                |
|---|-------------------|--------------------------------|
| <b>Design of Sampling:</b> Introduction, Sample Design, Sampling and Non- sampling Errors, Types of Sampling Designs.<br><b>Data Collection:</b> Qualitative and Quantitative Data, Experimental and Surveys, Collection of Primary Data, Collection of Secondary Data, Selection of Appropriate Method for Data Collection.<br><b>Testing of Hypotheses:</b> Hypothesis, Basic Concepts concerning Testing of Hypotheses, Procedure for Hypothesis Testing, P-Value approach, Limitations of the Tests of Hypothesis. Case Study Method, ANOVA test using excel or similar tools.  | <b>No. of Hrs</b> | <b>Blooms cognitive Levels</b> |
|   | <b>05</b>         | <b>Apply CO3</b>               |
| <b>Module–4: Interpretation and Report Writing</b>  |                   |                                |
| <b>Interpretation:</b> Meaning of Interpretation, Technique of Interpretation, Precaution in Interpretation.<br><b>Report Writing:</b> Significance of Report Writing, Different Steps in Writing Report, Layout of the Research Report, Types of Reports, Oral Presentation, Mechanics of Writing a Research Report, Precautions for Writing Research Reports. Introduction to Latex and various templates for report and paper writing.   | <b>No. of Hrs</b> | <b>Blooms cognitive Levels</b> |
|   | <b>05</b>         | <b>Analyze CO4</b>             |
| <b>Module–5: Intellectual Property Rights</b>   |                   |                                |
| <b>Intellectual Property:</b> The Concept, Intellectual Property System in India, Development of TRIPS Complied, Regime in India, Patents Act, 1970, Trade Mark Act, 1999, The Designs Act, 2000, The Geographical Indications of Goods (Registration and Protection) Act1999, Copyright Act,1957, The Protection of Plant Varieties and Farmers’ Rights Act, 2001, The Semi-Conductor Integrated Circuits Layout Design Act, 2000, Trade Secrets, Utility Models, IPR and Biodiversity, The Convention on Biological Diversity (CBD) 1992, Competing Rationales for Protection of IPRs, Leading International Instruments Concerning IPR, World Intellectual Property, Organisation (WIPO), WIPO and WTO, Paris Convention for the Protection of Industrial Property, National Treatment, Right of Priority, Common Rules, Patents, Marks, Industrial Designs, Trade Names, Indications of Source, Unfair Competition, Introduction to Patents and Copyrights. Case study on company IPR | <b>No. of Hrs</b> | <b>Blooms cognitive Levels</b> |
|   | <b>05</b>         | <b>Understand CO5</b>          |

| <b>Course Outcomes: After completing the course, the students will be able to</b> |  |
|---|--|
| <b>21ECE174.1</b>   | Understand and define research problem   |
| <b>21ECE174.2</b>   | Explain and carry out literature review based on the research problem  |
| <b>21ECE174.3</b>   | Apply sampling and data collection techniques and carry out parametric testsof Hypothesis for the research problem |
| <b>21ECE174.4</b>   | Interpret the research findings and create a report  |
| <b>21ECE174.5</b>   | Explain various forms of IPR and develop the linkages in technology innovation and IPR                             |
| <b>21ECE174.6</b>   | Understand and define research problem   |

| Reference Books   |
|---|
| 1. C.R. Kothari, Gaurav Garg, “Research Methodology: Methods and Techniques”, New Age International 4 th Edition, 2018.   |
| 2. Ranjit Kumar, “Research Methodology a step-by-step guide for beginners” (For the topic Reviewing the literature under module 2), SAGE Publications 3 rd Edition, 2011. |
| 3. Firuza Karmali (Aibara), “ A Short Introduction to LaTeX: A Book for Beginners”, Create space Independent Publishing Platform, 2019.                                   |
| 4. Trochim, “Research Methods: the concise knowledge base”, Atomic Dog Publishing 2005.   |
| 5. Fink A, “Conducting Research Literature Reviews: From the Internet to Paper”, Sage Publications 2009.  |

**Marks Distribution for Assessment:**

| CIA<br>(50)                       | Component    | Description  | Marks      |
|-----------------------------------|--------------|--|------------|
|                                   | Written Test | <ul style="list-style-type: none"> <li>Total Number of Test: 3</li> <li>Each Theory test will be conducted for 30 marks</li> <li>Average of 3 tests = 30 Marks</li> </ul>            | 30         |
|                                   | Assignment   | Review Paper Writing   | 10         |
|                                   | AAT          | Hypothesis testing using Anova   | 10         |
| <b>Total Marks</b>                |              |  | <b>50</b>  |
| SEA<br>(50)                       | Component    | Description  | Marks      |
|                                   | Written Exam | Theory exam will be conducted for 100 marks and scaled down to 50 Marks<br>The question paper will have 10 full questions each of 20 marks.<br>Students must answer 5 full questions | <b>50</b>  |
| <b>Total marks for the Course</b> |              |  | <b>100</b> |

**Additional Assessment Tools (AAT)** – Quiz, Presentations, Term Paper, Open ended experiments, Mini Projects, Two-minute video on latest topic, Short MOOC courses