

# *B.N.M. Institute of Technology*

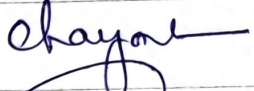







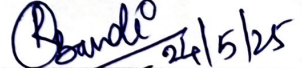
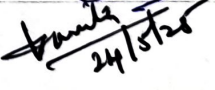
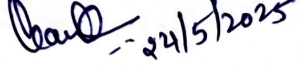
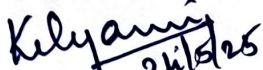
An Autonomous Institution under VTU

For Internal Communication

Department of Computer Science & Engineering

**PG BOS MEMBERS**

**AY- 2025-26**

Sl. No	Name	Category	Nature	Signature
1	Dr. Chayadevi M L	Head of Dept.	Chairperson	
2	DR. Harish Kumar B T Prof. Dept. of CSE Bangalore Institute of Technology	BOS VTU Nominee	Member	 24/5/2025
3	Dr. Mohammed Tajuddin Prof. Cyber security head DSCE, Bengaluru	Subject Expert from Other Institution	Member	 24/5/25
4	Dr. Raghavendra T S Associate Professor, Department of CSE, Presidency University, Bengaluru	Subject Expert from Other University	Member	 24/5/25
5	Mr. Devarajan Application Engg Architect Intel India, Bengaluru	Industry Expert	Member	
6	Ms. Ramya Assistant Prof. Department of CSE DSCE, Bengaluru	Meritorious Aluminous	Member	 24/5/25
7	Dr. Manjunatha S	Professor	Member Secretary	 24/5/25
8	Dr. Chaitra M	Asso. Professor	Member	 24/5/25
9	Dr. Swetha M D	Asso. Professor	Member	
10	Dr. Revanasiddappa Bandi	Asso. Professor	Member	 24/5/25
11	Dr. Kavita V. Horadi	Asso. Professor	Member	 24/5/25
12	Prof. Manikantha K	Assistant Professor	Member	 24/5/2025
13	Prof. Kalyani S Kumar	Assistant Professor	Member	 24/5/25

Additional Director & Principal

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**BNM Institute of Technology**  
Bangalore-560 070

# *B.N.M. Institute of Technology*

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## Department of Computer Science and Engineering

### PG-BOS Meeting-Minutes of the Meeting (MOM)

Date: 24-05-2025

Academic Year: 2025-26

Venue: A-215, Dept. of CSE

Time: 2.30 PM

<b>BOS Members Present</b>	<ol style="list-style-type: none"><li>1. Dr. Chayadevi M. L, HoD-CSE, Chairperson</li><li>2. Dr. Harish Kumar B T, Professor, Dept. of CSE, BIT, Bengaluru. (VTU Nominee)</li><li>3. Mr. Devarajan, Application Engg Architect Intel India, Bengaluru (Industry Expert)</li><li>4. Dr. Mohammed Tajuddin, Professor &amp; Head, Dept. of CSE, DSCE, Bengaluru (Subject Expert from Other Institution)</li><li>5. Dr. Raghavendra T S, Professor, Dept. of CSE, Presidency University, Bengaluru (Subject Expert from Other University)</li><li>6. Ms. Ramya S, Assistant Professor, Dept. of AIML DSCE, Bengaluru (Meritorious Aluminous).</li><li>7. Dr. Anitha N, Professor, Dept. of CSE, BNMIT, Bengaluru</li><li>8. Dr. Geetha S, Professor, Dept. of CSE, BNMIT, Bengaluru</li><li>9. Dr. Manjunatha S, Professor, Dept. of CSE, BNMIT, Bengaluru (Member Secretary)</li><li>10. Dr. Swetha M D Associate Professor, Dept. of CSE, BNMIT, Bengaluru (Member)</li><li>11. Dr. Chaitra M Associate Professor, Dept. of CSE, BNMIT, Bengaluru (Member)</li><li>12. Dr. Revanasiddappa Associate Professor, Dept. of CSE, BNMIT, Bengaluru (Member)</li><li>13. Prof. Manikata, Assistant Professor, Dept. of CSE, BNMIT, Bengaluru (Member)</li><li>14. Prof. Kalyani S Kumar, Assistant Professor, Dept. of CSE, BNMIT, Bengaluru (Member)</li></ol>
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<b>Agenda</b>	<ol style="list-style-type: none"> <li>1. Welcome address</li> <li>2. Introduction</li> <li>3. Best Practices in Curriculum Design</li> <li>4. Presentation of Scheme &amp; Syllabus of 1st to 4th Semester M. Tech (CSE), and Syllabus 2025 scheme</li> <li>6. Any other suggestions to the department</li> <li>7. Concluding Remarks by Member secretary</li> </ol>
<b>Proceedings of the meeting</b>	
	<ol style="list-style-type: none"> <li>1. PG-BOS Meeting for AY 2025-26 was held on 24<sup>th</sup> May 2025 at 2:30 PM in A-215, Department of CSE, BNMIT.</li> <li>2. BOS Chairman Dr. Chayadevi M L. Prof and Head, Dept. of CSE introduced BNMIT and its antiquity to the external members of BOS.</li> <li>3. A brief introduction of each member with a welcome was made by Dr. Chayadevi M L.</li> </ol> <p><b>Members present during the meeting</b></p> <p><b>External Members:</b></p> <ol style="list-style-type: none"> <li>4. Dr. Harish Kumar B T, Professor, Dept. of CSE, BIT, Bengaluru. (<b>VTU Nominee</b>)</li> <li>5. Mr. Devarajan, Application Engg Architect Intel India, Bengaluru</li> <li>6. Dr. Mohammed Tajuddin, Professor &amp; Head, Dept. of CSE (Cyber Security), DSCE, Bengaluru.</li> <li>7. Dr. Raghavendra T S, Professor, Dept. of CSE, Presidency University, Bengaluru</li> <li>8. Ms. Ramya S, Assistant Professor, Dept. of AIML, DSCE, Bengaluru.</li> </ol> <p>Dr. Deepu Singh has attended the BOS Meeting online on MS Teams platform</p> <p><b>Internal Members</b></p> <ol style="list-style-type: none"> <li>9. Dr. Chayadevi M. L, HoD-CSE, Chairperson</li> <li>10. Dr. Anitha N, Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>11. Dr. Geetha S, Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>12. Dr. Manjunatha S, Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>13. Dr. Swetha M D Associate Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>14. Dr. Chaitra M Associate Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>15. Dr. Revanasiddappa Associate Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>16. Prof. Manikata, Assistant Professor, Dept. of CSE, BNMIT, Bengaluru</li> <li>17. Prof. Kalyani S Kumar, Assistant Professor, Dept. of CSE, BNMIT, Bengaluru</li> </ol>

18. External Members were communicated through the mail.
19. The session started with welcome address and introduction.
20. As per the agenda the discussion started with the PG Scheme and the Syllabus.
21. PG-CSE Courses offered scope and opportunities for students to explore better avenues to research were discussed.
22. With these deliberations and proceedings, Dr. Chayadevi M L, HoD -CSE, Chairperson concluded the meeting by extending gratitude to the members of BOS and Management, BNMIT for supporting, suggesting and appreciating the formation of Scheme and Syllabus for the Academic Year 2025-2026 with Vote of Thanks.

### Recommendations/Suggestions from the BOS Members

### Action Plan

Dr. Harish  
Kumar B T,  
Professor, CSE  
Dept., BIT,  
Bengaluru

- Appreciated the members for retaining Advanced Mathematics for Computer Science with higher credibility as it is one of the key components for M. Tech and Research.
- Verified scheme with Credits as per the VTU regulations.
- Approved and satisfied with revision from 2024 Scheme and Syllabus to 2025 Scheme in comparison with VTU Scheme and Syllabus as well.

- Course coordinators will be informed to prepare the students to explore the application of mathematical foundation in application development.
- MOOC coordinator will be informed to track the NPTEL activities and circulate the list of courses available in the platform

Dr. Mohammed  
Tajuddin,  
Professor &  
Head, CSE  
Dept., DSCE,  
Bengaluru

- Dr. Mohammed Tajuddin sir suggested that the subjects offered in the MTech is difficult to handle since subject have vast syllabus. He suggested frame the syllabus for Deep Learning and Reinforcement Engineering subjects because some topics are similar.
- He also suggested to move Design pattern elective to 2nd sem and add elective Design pattern & Architecture

- Projects, Internship on ethics to be strengthened
- Programming skills to be strengthened.



	<ul style="list-style-type: none"> <li>• He emphasized that SDN Units 2, 3, and 4 need clear differentiation, as overlapping content makes the syllabus appear repetitive.</li> <li>• He also noted that while the subject is strong in content, the teaching approach and assessments require careful</li> <li>• Suggested to include Courses like DevOps and Agile Technology.</li> <li>• Approved and satisfied with 2025 Scheme and Syllabus.</li> </ul>	
Mr. Devarajan, Intel India, Sr. Manager, Bengaluru	<ul style="list-style-type: none"> <li>• Devarajan sir mentioned Advances on databases must be added by replacing High performance computer architecture</li> <li>• He mentioned Ethical hacking in 2nd sem is like cybersecurity and Digital forensics and suggested check all the topics in detail.</li> <li>• He suggested moving Software Architecture and Design Patterns to the second semester, allowing students to grasp core architecture concepts early in the M. Tech program.</li> <li>• Suggested giving more projects to understand the various concepts in all the subjects if there is a provision.</li> <li>• Approved and satisfied with the 2025 Scheme.</li> </ul>	<ul style="list-style-type: none"> <li>• Planned to stabilize a strong alumni network and to arrange the various Workshops in the various advanced technologies and technical talk from the expert alumni in their expertise.</li> <li>• Course coordinator will inform to conduct more hands-on by using suitable tools</li> </ul>
Dr. Raghavendra T S, Professor, Dept. of CSE, Presidency University, Bengaluru	<ul style="list-style-type: none"> <li>• Dr. Raghavendra, Sir, mentioned Advanced Operating Systems must include a lab component at the M. Tech level and be made PCI-compliant.</li> <li>• Based on the input from industry experts, Operating Systems should not be recommended as a standalone subject.</li> <li>• Additionally, live demonstrations must be incorporated into the Cybersecurity course to enhance practical understanding</li> </ul>	<ul style="list-style-type: none"> <li>• The course coordinator will inform us to frame the lab component and to conduct the same by using suitable software.</li> <li>• Planned for more Mini projects in the cybersecurity course with suitable tools to</li> </ul>

	<ul style="list-style-type: none"> <li>• Approved and satisfied with 2025 Scheme and Syllabus.</li> <li>• Suggested to go with hands on session on all courses offered for PG program to understand the implementation of various concepts in real time application.</li> </ul>	demonstrate the fundamentals.
Ms. Ramya S, Assistant Professor, Dept. of AIML, DSCE, Bengaluru	<ul style="list-style-type: none"> <li>• Enquired about the MOOC's and Professional Elective Courses offered.</li> <li>• Approved and satisfied with the 2025 Scheme.</li> <li>• Suggested to go with hands on session on all courses offered for PG program to understand the implementation of various concepts in real time application. With Mini project.</li> </ul>	
Concluding Remarks by Member Secretary	<ul style="list-style-type: none"> <li>• The Suggestions from the VTU Nominee and all the external experts have been recorded and would be considered. M. Tech- PG Scheme and syllabus are approved and satisfied as per the members.</li> </ul>	

#### GALLERY:



Welcome Address by BOS Chairman-Dr. Chayadevi M L






BOS Meeting Discussion with Members



BOS Meeting Discussion with Members

  
**PG Coordinator**

  
**HoD**

  
**Additional Director & Principal**

**Additional Director & Principal**  
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