

B N M Institute of Technology

An Autonomous Institution under VTU. Approved by AICTE

Department: Electronics and Communication Engineering

Scheme of Teaching and Examination - Autonomous Effective from Academic year 2024-25

IV Semester M. Tech (VLSI Design and Embedded Systems)

Sl. No	Course Type	Course Code	Course Title	Teaching Department	Teaching Hours /Week					Examination			
					L	T	P	J	Hours/Week	Credits	CIA Marks	SEA Marks	Total Marks
1	PEC	24VDEP241X	MOOC/NPTEL (Subjected to offering by NPTEL)	Online	3	-	-	-	3	3	50	50	100
2	PRJ	24VDEJ242	Project Work Phase-2	ECE	-	-	-	26	26	13	50	50	100
TOTAL					3	-	-	26	29	16	100	100	200

Project work:Phase-2 (24VDEJ242): Based on the abilities of the students and recommendations of the mentor, a single discipline or a multidisciplinary project can be assigned to an individual student. The progress of the project work will be evaluated continuously. There will be 2 seminars in 2 phases and one final presentation with demonstration which are evaluated by a panel of faculty members with HOD as the Chairperson.