

B.N.M. Institute of Technology

An Autonomous Institution under VTU, Approved by AICTE

Department of Electronics and Communication Engineering

Minutes of the Meeting

Meeting Details	PG BOS Meeting
Date	10.06.2026
Time	11:00 am
Venue	N401 (New Block Seminar Hall)

Members Present	1. Dr. Yasha Jyothi M Shirur, Professor and Head Dept. of ECE, BNMIT 2. Dr. Manju Devi, Prof. and Head Dept. of ECE, The Oxford College of Engineering 3. Dr. S. N Prasad, Professor of ECE, Manipal Academy of Higher Education (MAHE), B'lore 4. Dr. Jayashree H. V, Graphic Research staff Engineer, Qualcomm. 5. Ms. Nandika R, AI Engineer Tata Consultancy Services 6. Dr. Bindu S, Professor, Member Secretary 7. Dr. Subodh Kumar Panda, Professor, BNMIT 8. Dr. Rekha P, Professor, BNMIT 9. Dr. Bhuvana Suganthi D, Associate Professor, BNMIT 10. Dr. Manjunath G Asuti, Associate Professor, BNMIT 11. Dr. Ashwini Savanth, Associate Professor, BNMIT 12. Dr. Sujaya B L, Associate Professor, BNMIT 13. Dr. Vrunda Kusanur, Associate Professor, BNMIT 14. Dr. Priyadarshini K Desai, Associate Professor, BNMIT 15. Dr. Smitha Gayatri D, Associate Professor, BNMIT 16. Dr. Lakshmi Bhaskar, Associate Professor, BNMIT
Agenda	1. Welcome Address and Introduction 2. Presentation of Scheme and Syllabus of 1st and 2nd Semester of 2026 scheme 3. Presentation of Scheme and Syllabus of 3rd and 4th Semester of 2025 scheme 4. Any other suggestions to the department 5. Concluding remarks by Member Secretary
Proceedings of the meeting	
The BoS meeting for PG program in M. Tech VLSI Design & Embedded Systems, was held on 10th June 2026 in N-401, New Building Seminar Hall, BNMIT, Bengaluru. The meeting started at 11.00 am in Hybrid mode. Dr. Yasha Jyothi M Shirur HoD of ECE, BoS Chairman (UG & PG) welcomed everyone to the meeting and introduced the external and internal BoS members. To start with Chairperson presented the semester wise scheme and syllabus for 2025 and 2026 batch.	

All the BoS members appreciated the systematic way of framing the syllabus from 1st to 4th semester and gave suggestions to make the curriculum better. The member secretary Dr. Rekha P noted down the suggestions given by the committee and provided the concluding remarks and thanked all the BoS members. Few photographs/snapshots of the BoS meeting have been attached herewith. The attendance sheet of the BoS meeting is also enclosed herewith.		
Expert	Recommendations/Suggestions from the BOS Members	Action taken
Dr. Manju Devi Prof. and Head Dept. of ECE, The Oxford College of Engineering, Bangalore	RMIPR may be offered as MOOC/NPTEL course to only those students who have not completed it during UG. Otherwise, Content of UG and PG course should be different.	Recommendation considered. Offered as NCMC online course – Non Credit Mandatory Course.
Dr. Jayashree H. V. Graphic Research staff Engineer AL Application Acceleration on GPU. Qualcomm, Texas McCombs School of Business Bengaluru, Karnataka	1. Appreciated introduction of Scripting for EDA course in 2nd sem. 2. FPGA implementation in Hybrid AI course is recommended.	Recommendation considered. Procurement of suitable FPGA development boards is in progress.
Dr. S. N Prasad, Professor of ECE, Manipal Academy of Higher Education (MAHE), B'lore	Appreciated that systematic way of organizing the courses from 1st to 4th semester.	-----
Ms. Nandika R AI Engineer Tata Consultancy Services, Bengaluru, Karnataka	1. RTL to GDS II course may be offered in 2nd or 3rd semester. This will aid in better understanding of STA, Synthesis and optimization courses. 2. A course on IC Manufacturing, Packaging may be introduced.	1. Subjected to availability of the course by NPTEL. 2. It may be offered as an elective course.
Concluding Remarks by Member Secretary, Dr. Rekha P	In place of RMIPR, a PCC course - Neuromorphic Chip Design and Computing is introduced in I semester. Multi Core computer architecture is introduced in place of Real time operating system as a prerequisite in I sem. RTOS course is shifted to II sem. Noted the suggestions and recommendation given by PG BOS Committee Members. Mentioned that the suggestions will be incorporated in M. Tech (VLSI Design and Embedded Systems) autonomous curriculum to make it better to meet the requirements of the Industry and Societal needs. She thanked all the external and internal BoS members.	

Photographs / Snapshots during BoS Meeting 10.06.2026



Rekha P.
Member Secretary
Dr. Rekha P

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HoD- ECE
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20/06
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