

B. N.M. Institute of Technology
An Autonomous Institution under VTU
Department of Electrical & Electronics Engineering
REPORT on Events Conducted under
IEEE – SSIT - BNMIT Student Branch Chapter
(SSIT- Society on Social Implications of Technology)

Sl. No.	Event Name	Date	Total Number of Participants
1	Inauguration of IEEE – SSIT BNMIT Student Branch Chapter	14 / 03 / 2026	90
2	Technical Talk by: 1. Dr. Ramakrishna Kappagantu, Chief Technology Advisor Eficaa EnSmart Solutions Pvt. Ltd., 2. Mr. Balaji B G, Partner, Visistata Enterprises, Chair, IEEE SSIT-Bangalore Section	14 / 03 / 2026	90
3	Celebration of National Technology Day and Technical Talk on “PCB Design and Manufacture” Activity on: Reeling in Challenge – Where creativity meets Global Responsibility	14 / 05 / 2026	106
4	Innovation and Entrepreneurship Outreach Program in School	25 / 07 /2026	35

IEEE – SSIT – BNMIT STUDENT BRANCH

CHAPTERS COMMITTEE MEMBERS LIST

Sl. No.	Name	Executive Committee Designation	Phone Number	Mail ID
1.	Ajay K C (6 th sem EEE)	Chair	9980205551	ajaybcs2004@gmail.com
2.	Shraddha Shetty G R (4 th sem EEE)	Vice – Chair	9611948999	shraddhashettygr2006@gmail.com
3.	Sharin Cherian (6 th sem EEE)	Secretary	9916693491	sharincherian05@gmail.com
4.	Abhishek A Reddy (6 th sem EEE)	Treasurer	9353497771	abhishekareddy2005@gmail.com
5.	Adithya P S (4 th sem EEE)	Web / IT Coordinator - Design / Creative Head-1	8951050011	aadithya.p.s1396@gmail.com
6.	Ishan Nandakumar Pai (4 th sem EEE)	Joint Secretary	8217289642	ishanpai0811@gmail.com
7.	Achyutha Kodangala Saralaya (4 th sem EEE)	Web / IT Coordinator - Design / Creative Head -1	9110231512	onexpression15@gmail.com
8.	Aditi R Prasad (6 th sem EEE)	Technical Coordinator	9880145106	aditi.grp05@gmail.com
9.	Dr. Priyashree S	Faculty Advisor	9945976556	priyashrees@bnmit.in

Report on

“Inauguration of IEEE – SSIT - BNMIT Student Branch Chapter”

Start Date: 14 / 03 / 2026

End Date: 14 / 03 / 2026

Duration of Activity (in Hrs): 2:00 pm to 3: 30 pm (1 hr 30 min)

No of Students Participated: 72, No of Faculty Participated: 18

No of External Participants: 04, Total No of Participants: 90

External Coordinators:

- 1. Chief Guest: Mr. Balaji B G, Partner, Visistata Enterprises**
Chair -IEEE SSIT Bangalore Section
- 2. Guest of Honor: Dr. Ramakrishna Kappagantu,**
Chief Technology Advisor at Eficaa EnSmart Solutions Pvt. Ltd.
- 3. Dr R S Geetha, Prof & HoD, Dept of EEE, BMSCE, Bangalore –**
Vice Chair--IEEE SSIT Bangalore Section
- 4. Dr Umavathi, Associate Prof, Dept of EEE, BMSCE, Bangalore –**
Secretary--IEEE SSIT Bangalore Section

Mode of Session Delivery: Offline

BNMIT STAFF CORDINATORS:

Dept. of EEE:

- 1. Dr. Priyashree S, Associate Professor, Dept. of EEE, BNMIT: Faculty Counselor– IEEE-SSIT BNMIT Student Branch Chapter.**
- 2. Smt. Kruthi Jayaram, Assistant Professor, Dept. of EEE, BNMIT**

Objective:

The objective of the inauguration of the IEEE Society on Social Implications of Technology (SSIT) BNMIT Student Branch Chapter is to formally establish a platform for students to explore the social, ethical, and technological impacts of engineering. It aims to encourage interdisciplinary learning, innovation, and responsible use of technology while fostering professional development and community engagement among students.

Benefit in terms of Learning / Skill / knowledge obtained:

The chapter provides students with opportunities to engage in technical activities, workshops, and discussions under IEEE - SSIT, enhancing their knowledge beyond the classroom. It also helps develop leadership, teamwork, and social responsibility while connecting students with a global professional network.

About the Event:

The IV and VI semester students of Electrical & Electronics Engineering (EEE) participated in the Inauguration of IEEE-SSIT BNMIT Student Branch Chapter on 14th March 2026. The inauguration of the IEEE SSIT Student Branch Chapter began with a warm welcome address by the Additional Director & Principal, **Dr. S Y Kulkarni**, that set an enthusiastic and inspiring tone for the event. The formal inauguration of the chapter was carried out by the dignitaries through the unveiling of the banner, marking the official launch of the IEEE Society on Social Implications of Technology (SSIT) at the institution. The Details of Students branch committee members were presented during the Inauguration. **Mr. Ajay K C**, Student Chair, IEEE-SSIT-BNMIT, delivered an insightful note about the objectives of SSIT chapter, highlighting its vision to explore the intersection of technology and society, and encouraging students to actively engage in socially responsible innovation. He also briefed about the various upcoming activities proposed under the Student Branch Chapter.

The event included the honoring of the Chief Guest, **Mr. Balaji B G** with his keynote address. The Guest of Honor, **Dr. Ramakrishna Kappagantu**, was also honored and he addressed the gathering.



Lighting of the Lamp during the Inauguration



Unveiling of the banner by the Dignitaries (Soft Banner)



Unveiling of the banner by the Dignitaries



Introduction of SSIT-BNMIT Students Branch Members

Report on Technical Talk :

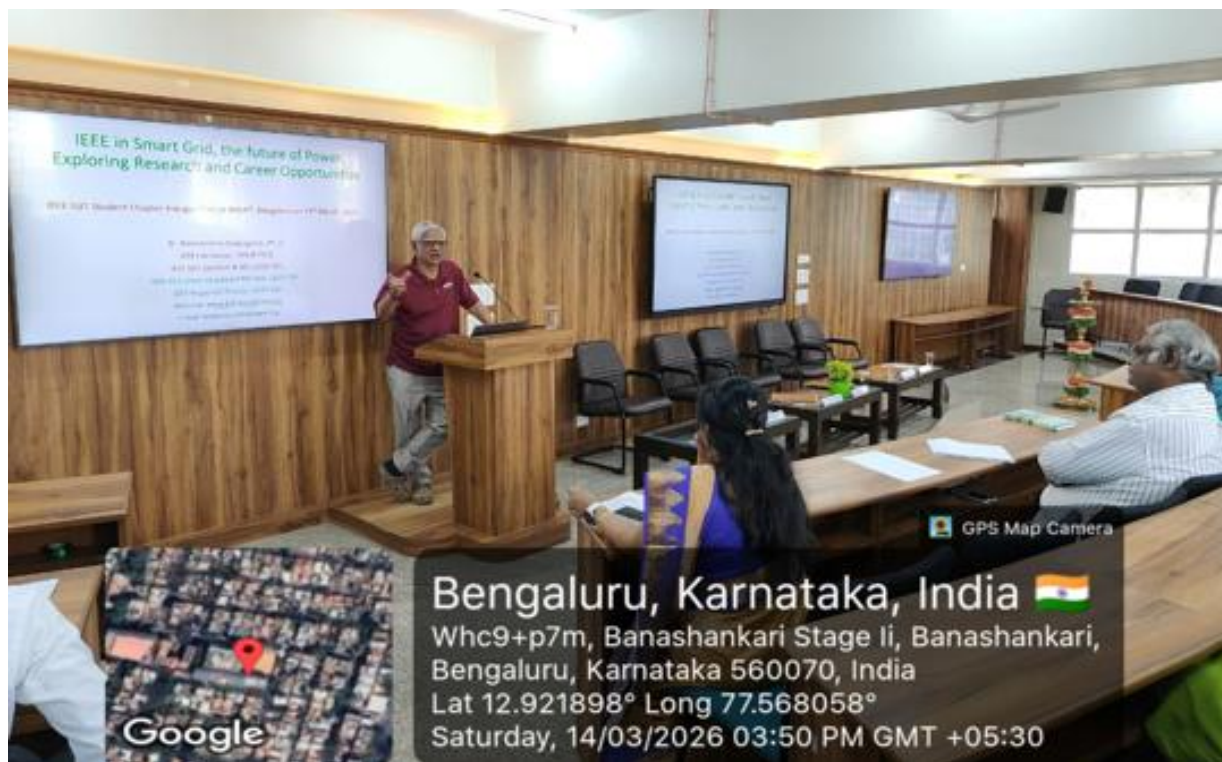
“About the IEEE – SSIT Student Chapter and its Activities for membership drive”

The Chair of the IEEE Bangalore Section, Mr. Balaji B G, shared motivating words, emphasizing the importance of ethical technology development and the role of students in shaping a sustainable future. Dr. Ramakrishna Kappagantu, inspired the audience, stressing the significance of technological advancements aligned with societal needs.

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Technical Talk by Mr. Balaji B G



Technical Talk by Dr. Ramakrishna Kappagantu

REPORT on
Celebration of National Technology Day
Technical Talk on “PCB Design and Manufacture”
Activity on: Reeling in Challenge – Where creativity meets
Global Responsibility

Duration of the activity: 1day

Start Date: 14 / 05 / 2026

End Date: 14 / 05 / 2026

Duration of Activity (in Hrs): (9 hrs) 1 day

No of Students Participated: 99, **No of Faculty Participated:** 06

No of External Participants: 01

Mr. Ravi K S, Sr.Dy. Gen Manager, BEL (Retd.), Bangalore

Expenditure Amount: NIL

Mode of Session Delivery: Offline

STAFF CORDINATOR:

1. Dr. Priyashree S, Associate Professor, Dept. of EEE, BNMIT

EVALUATORS:

1. Mr. Sujith T, Assistant Professor, Dept. of EEE, BNMIT
2. Smt. Shruti V Joshi, Assistant Professor, Dept. of EEE, BNMIT

STUDENT COORDINATORS:

1. Ms. Shraddha Shetty, IV sem EEE
2. Mr. Aditya P S, IV sem EEE
3. Mr. Ajay K C, VI sem EEE
4. Ms. Aditi R Prasad, VI Sem, EEE

Objective:

The objective of the technical talk on “PCB Design and Manufacture” was to create awareness and enhance knowledge about the fundamentals of PCB technology, design methodologies, and manufacturing processes among students. The event aimed to familiarize participants with PCB design software packages, schematic creation, component placement, routing techniques, and industry standards followed in PCB fabrication and assembly. It also intended to bridge the gap between academic learning and industrial practices by providing practical insights into real-world PCB development.

Another key objective of the event was to support the vision of the Electronic Sector Skills Council of India (ESSCI) and the National Policy on Electronics - 2012 in promoting skill development and strengthening the electronics manufacturing sector in India. The session encouraged participants to explore career opportunities in electronics design and manufacturing while developing technical competencies relevant to the growing electronics industry.

The objective of the “Reeling in Change” activity was to encourage students to creatively address social and global issues through impactful and meaningful digital storytelling.

Benefit in terms of Learning / Skill / knowledge obtained:

The technical talk greatly benefited the participants by providing practical exposure to PCB design and manufacturing concepts used in the electronics industry. Attendees gained knowledge about PCB design software tools, schematic development, component placement, routing techniques, and manufacturing standards, which helped improve their technical understanding and industry readiness. The session also created awareness about career opportunities and skill requirements in the rapidly growing electronics and embedded systems sector.

The Reeling Challenge activity benefited participants by enhancing their creativity, teamwork, communication, and digital content creation skills while increasing awareness about important social and environmental issues.

About the Event:

The IV and VI semester students of Electrical & Electronics Engineering (EEE) attended the Technical talk on “PCB Design and Manufacture”. The talk was organized to provide

participants with a comprehensive understanding of Printed Circuit Board (PCB) technologies and their growing importance in the electronics industry. Mr. Ravi K S, Sr.Dy. Gen Manager, BEL (Retd.), highlighted the objectives of the Electronic Sector Skills Council of India (ESSCI) and the vision of the National Policy on Electronics - 2012 in promoting skill development and strengthening the electronics manufacturing ecosystem in India. He shared insights into the fundamentals of PCB design and manufacturing, emphasizing the role of PCB technology in modern electronic products and industrial applications.

The session also covered various PCB design software packages and explained the complete PCB design flow, including library creation, schematic design, component placement, and routing techniques. Participants gained knowledge about industry standards followed in PCB design and manufacturing to ensure quality, reliability, and efficiency in electronic systems. The talk served as an informative platform for students and professionals to understand practical aspects of PCB development and emerging opportunities in the electronics sector.

The “Reeling in Change” activity was organized to encourage creativity, awareness, and social responsibility among students through short reel creation. The event focused on meaningful themes such as Technology & Innovation for Good, Environment & Sustainability, Social Justice & Human Rights, and Health & Wellbeing. A total of 14 teams actively participated in the competition, showcasing innovative ideas and impactful messages through engaging visual storytelling. Participants were given 6 hours of preparation time to conceptualize, script, shoot, and edit their reels creatively.

The event provided a platform for students to express their perspectives on important societal issues while improving their teamwork, communication, and digital content creation skills. The reels were evaluated based on editing quality, content quality, creativity, and relevance to the assigned themes. The activity successfully inspired participants to use media and technology as tools for awareness and positive social change, making the event both educational and engaging.

Outcome:

The outcome of the technical talk was an enhanced understanding among participants regarding the complete PCB design and manufacturing process, including the use of PCB design software, schematic creation, placement, routing, and industry standards. The session improved the

B.N.M. INSTITUTE OF TECHNOLOGY

National Policy on Electronics - 2019 :

Salient Features of NPE 2019:

- Creation of ecosystem for globally competitive EDP&D centers by providing financial incentives and support in the form of grants, equity share, credit financing, insurance cover is offered for manufacturing of new electronic components.
- Providing special package of incentives for mega projects which are relatively high-tech products/ mega investments, such as semiconductor facilities (using 300mm and 450mm).
- Development of reliable software and hardware capabilities to strengthen domestic and ecosystem of existing units.
- Promotion of Industry-Academia and associated in-situ set-up of electronics facilities in high-tech universities and state-of-the-art research emerging technology centres such as DR, Institute of Things, IIT Bombay, National Institute of IT, Indian Institute of Space Science and Technology (IISST), Bharat Electronics Limited, Indian Institute of Space Research and Organisation (ISRO).

Bengaluru, Karnataka, India

Sri Sadhashiva Krupa, 116 Sadashiva Krupa 12th Main, 28th Cross Rd, Behind Bnm Institute Of Technology, Banashankari 2nd Stage, Banashankari, Bengaluru, Karnataka 560070, India
 Lat 12.92164° Long 77.568049°
 Thursday, 14/05/2026 11:30 AM GMT +05:30

Participants during the Event



Felicitation to the Speaker by Professor Dr A Kumar



Student Coordinators Guiding the participants during the Activity

REPORT on
Innovation and Entrepreneurship Outreach Program in
Schools/Community

Engineering Projects Exhibition at Sri Jayabharathi
Educational Society

The Innovation and Entrepreneurship Outreach Program provided an opportunity for the students to enhance their creativity, critical thinking, and problem-solving abilities, helping them move beyond textbook learning to practical application. This event built the confidence, communication skills, teamwork, and leadership qualities through interactive activities and idea pitching among the student fraternity. It gave an exposure to the students for entrepreneurial opportunities and real-world challenges, encouraging self-initiative, resilience, and innovative thinking. Overall, it empowered young learners to become proactive, future-ready individuals capable of identifying opportunities and contributing positively to society and economic development.



Group photo of all the participants



Group photo School Students



Group photo of BNMIT Students



School Students exhibit Projects during the event



BNMIT Students exhibit Projects during the event